



晶采光電科技股份有限公司  
AMPIRE CO., LTD.

## SPECIFICATIONS FOR LCD MODULE

|                          |                            |
|--------------------------|----------------------------|
| <b>CUSTOMER</b>          |                            |
| <b>CUSTOMER PART NO.</b> |                            |
| <b>AMPIRE PART NO.</b>   | <b>AM-800480N5TZQW-00H</b> |
| <b>APPROVED BY</b>       |                            |
| <b>DATE</b>              |                            |

☐ Preliminary Specification

☐ Approved Specification

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|--------------------|-------------------|---------------------|
|                    |                   |                     |

## RECORD OF REVISION

| Revision Date           | Page | Contents  | Editor           |
|-------------------------|------|---|------------------|
| 2019/02/25<br>2019/6/18 | -    | New Release<br>Modify structure of the LED Back-light | Raymond<br>Kokai |

## 1. Features

5 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module.  
This module is composed of a 5" TFT-LCD panel and backlight unit.

- (1) Construction: 5" a-Si TFT active matrix and White LED Backlight .
- (2) Resolution (pixel): 800(R.G.B) X 480
- (3) Number of the Colors : 16.7M colors ( R , G , B, 8bit digital each)
- (4) LCD type : **IPS : Transmissive , normally Black**
- (5) Viewing Direction: All Direction.
- (6) LCD Interface : 24 Bit TTL RGB interface
- (7) Power Supply Voltage: 3.3V single power input. Built-in power supply circuit.

## 2. PHYSICAL SPECIFICATIONS

| NO | Item              | Specification                   | Remark            |
|----|-------------------|---------------------------------|-------------------|
| 1  | LCD Size          | 5.0 inch (Diagonal)             |                   |
| 2  | Driver element    | a-Si TFT active matrix          |                   |
| 3  | Resolution        | 800 x 3 (RGB) x 480             |                   |
| 4  | Display Mode      | Normally Black. Transmissive    |                   |
| 5  | Dot pitch         | 0.135(W) x 0.135(H) mm          |                   |
| 6  | Active area       | 108.0(W) x 64.8(H) mm           |                   |
| 7  | Module Size       | 117.8(W) x 76.4(H) x 2.86(T) mm | Note 1            |
| 8  | Color arrangement | RGB-stripe                      |                   |
| 9  | Luminance         | 300 (typ)                       | Cd/m <sup>2</sup> |

(Note1) Refer to the mechanical drawing.

### 3. ABSOLUTE MAX. RATINGS

The following values are maximum operation conditions, If exceeded , it may cause faulty operation or damage

#### 3.1 Electrical Absolute max. ratings

| Item          | Symbol   | Condition | Min. | Max.         | Unit | Remark |
|---------------|----------|-----------|------|--------------|------|--------|
| Power voltage | $V_{DD}$ | GND=0     | -0.3 | 4.0          | V    |        |
| Input voltage | $V_{in}$ |           | -0.3 | $V_{DD}+0.3$ | V    | Note 1 |

Note1:Hsync, Vsync, DE, PCLK, DISP, R0~R7, G0~G7,  
B0~B7,LEFT/RIGHT,UP/DOWN.

#### 3.2 Environmental Absolute max. ratings

| Item          | OPERATING      |     | STORAGE        |     | Remark          |
|---------------|----------------|-----|----------------|-----|-----------------|
|               | MIN            | MAX | MIN            | MAX |                 |
| Temperature   | -20            | 70  | -30            | 80  | Note2,3,4,5,6,7 |
| Humidity      | Note1          |     | Note1          |     |                 |
| Corrosive Gas | Not Acceptable |     | Not Acceptable |     |                 |

Note1 : Ambient temperature  $T_a \leq 40^{\circ}\text{C}$  : 85% RH max

$T_a > 40^{\circ}\text{C}$  : Absolute humidity must be lower than the humidity of 85%RH at  $40^{\circ}\text{C}$

Note2 : For storage condition  $T_a$  at  $-30^{\circ}\text{C} < 48\text{h}$  , at  $85^{\circ}\text{C} < 100\text{h}$

For operating condition  $T_a$  at  $-20^{\circ}\text{C} < 100\text{h}$

Note3 : Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note4 : The response time will be slower at low temperature.

Note5 : Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at  $+25^{\circ}\text{C}$

Note6 : When LCM panel is operated over  $60^{\circ}\text{C}$  ( center of the panel surface temperature), the  $I_{LED}$  of the LED back-light should be adjusted to 27mA

Note7 : This is center of the panel surface temperature, not ambient temperature.

## 4. ELECTRICAL CHARACTERISTICS

### 4.1 DC CHARACTERISTICS

Typical operating conditions (GND=0V)

| Item                       |         | Symbol   | Min.         | Typ. | Max.         | Unit | Remark |
|----------------------------|---------|----------|--------------|------|--------------|------|--------|
| Power supply               |         | $V_{DD}$ | 3.0          | 3.3  | 3.6          | V    |        |
| Input Voltage<br>for logic | H Level | $V_{IH}$ | $0.7 V_{DD}$ | --   | $V_{DD}$     | V    | Note 1 |
|                            | L Level | $V_{IL}$ | 0            | --   | $0.3 V_{DD}$ | V    |        |
| Power Supply current       |         | $I_{DD}$ |              | TBD  | --           | mA   | Note 2 |

Note1: :Hsync, Vsync, DE, PCLK, DISP, R0~R7, G0~G7,  
B0~B7, LEFT/RIGHT, UP/DOWN.

Note2: TFT power supply current.

$V_{DD}=3.3V$ ,  $f_v=60Hz$ ,  $T_a=25^{\circ}C$ , Display pattern: All White

### 4.2 LED BACKLIGHT UNIT

Electrical characteristic of LED Back-light

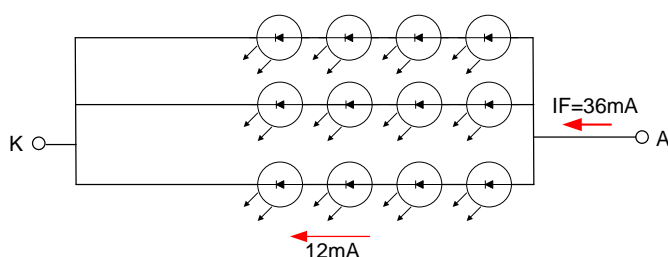
| Item                | Symbol | Min.   | Typ. | Max. | Unit | Note                          |
|---------------------|--------|--------|------|------|------|-------------------------------|
| LED Forward Current | IF     | --     | 36   | 60   | mA   | $T_a=25^{\circ}C$             |
| LED Forward Voltage | VF     | 10.8   | 11.4 | 13.6 | V    | IF=36mA,<br>$T_a=25^{\circ}C$ |
| Power Dissipation   | PD     |        | 410  |      | mW   | F=36mA,<br>$T_a=25^{\circ}C$  |
| LED life time       |        | 20,000 | -    | -    | Hr   | IF=36mA,<br>$T_a=25^{\circ}C$ |

Note 1:  $T_a$  means ambient temperature of TFT-LCD module.

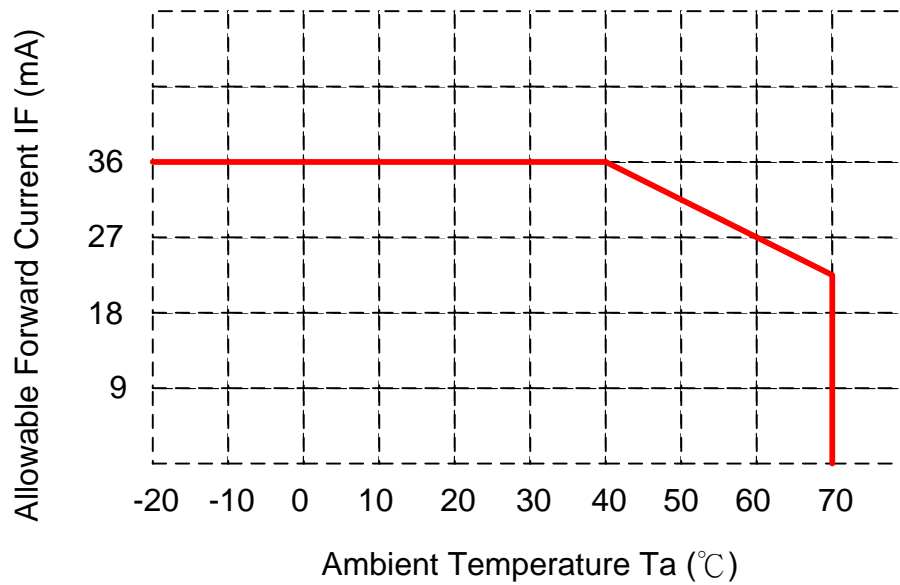
Note 2: If the module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

Note 3: the structure of LED B/L shows as below.

4 Serial x 3 Parallel.



Note 4: When LCM is operated over 60°C ambient temperature, the  $I_F$  of the LED back-light should be adjusted to 18mA max



## 5. OPTICAL CHARACTERISTICS OF LCD

| Item               |        | Symbol      | Condition                                | Min.         | Typ.   | Max.         | Unit              | Remark   |
|--------------------|--------|-------------|--|--------------|--------|--------------|-------------------|--|
| Response Time      |        | $T_r + T_f$ | $\Theta = 0^\circ$                       |              | 30     | 40           | ms<br>ms          | Note 1,2,3,5   |
| Contrast ratio     |        | CR          | At optimized viewing angle               | (800)        | (1000) | -            |                   | Note 1,2,4,5   |
| Viewing Angle      | Top    |             | $CR \geq 10$                             | 70           | 80     | -            | deg.              | Note1,2, 5,6   |
|                    | Bottom |             |  | 70           | 80     | -            |                   |  |
|                    | Left   |             |  | 70           | 80     | -            |                   |  |
|                    | Right  |             |  | 70           | 80     | -            |                   |  |
| Brightness         |        | $Y_L$       | $I_{LED}=36.0mA, 25^\circ C$             | 240          | 300    | -            | cd/m <sup>2</sup> | Note 7   |
| Red chromaticity   | XR     |             | $\Theta = 0^\circ$<br>$\Theta = 0^\circ$ | Typ<br>-0.05 | 0.629  | Typ<br>+0.05 |                   | Note 7<br>For reference only. These data should be update according the prototype. |
|                    | YR     |             |  |              | 0.326  |              |                   |  |
| Green chromaticity | XG     |             |  |              | 0.337  |              |                   |  |
|                    | YG     |             |  |              | 0.546  |              |                   |  |
| Blue chromaticity  | XB     |             |  |              | 0.136  |              |                   |  |
|                    | YB     |             |  |              | 0.143  |              |                   |  |
| White chromaticity | XW     |             |  |              | 0.320  |              |                   |  |
|                    | YW     |             |  |              | 0.345  |              |                   |  |

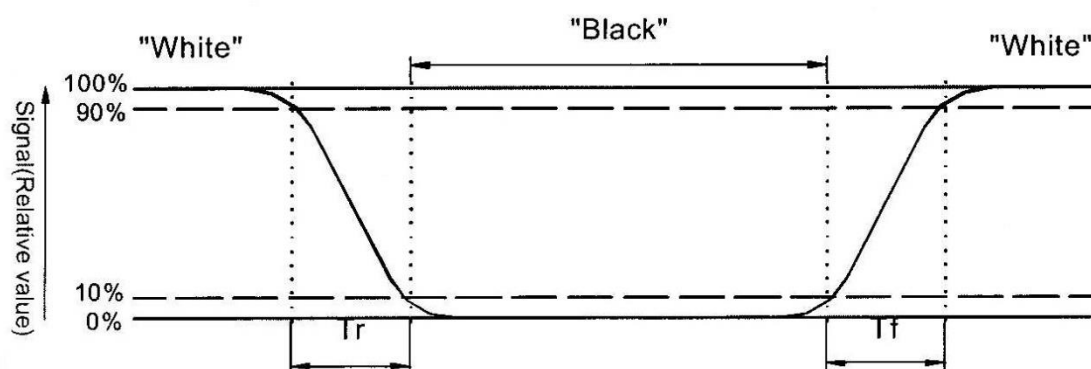
( ) For reference only. These data should be update according the prototype.

Note 1: Ambient temperature=25°C, and lamp current  $I_{LED}=36mA$ . To be measured in the dark room.

Note 2: To be measured on the center area of panel with a viewing cone of 1° by Topcon luminance meter BM-7, after 10 minutes operation.

Note 3. Definition of response time:

The output signals of photo detector are measured when the input signals are changed from "black" to "white" (falling time) and from "white" to "black" (rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes. Refer to figure as below.



Note 4. Definition of contrast ratio:

Contrast ratio is calculated with the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness of All White}}{\text{Brightness of All Black}}$$

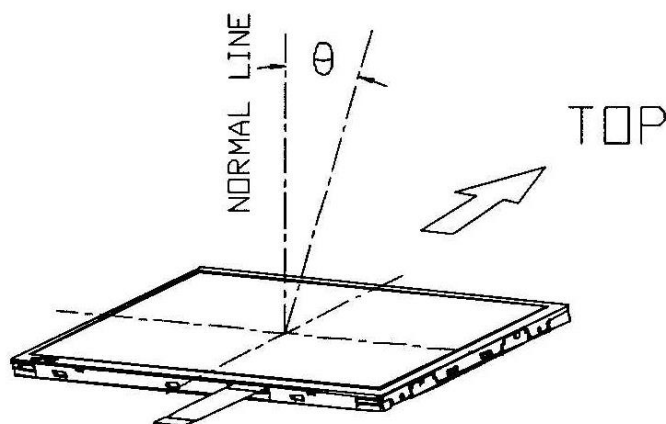
Note 5: White  $V_i = V_{i50} + 1.5V$       Black  $V_i = V_{i50} + 2.0V$

“±” means that the analog input signal swings in phase with  $V_{COM}$  signal.

“ ” means that the analog input signal swings out of phase with  $V_{COM}$  signal.

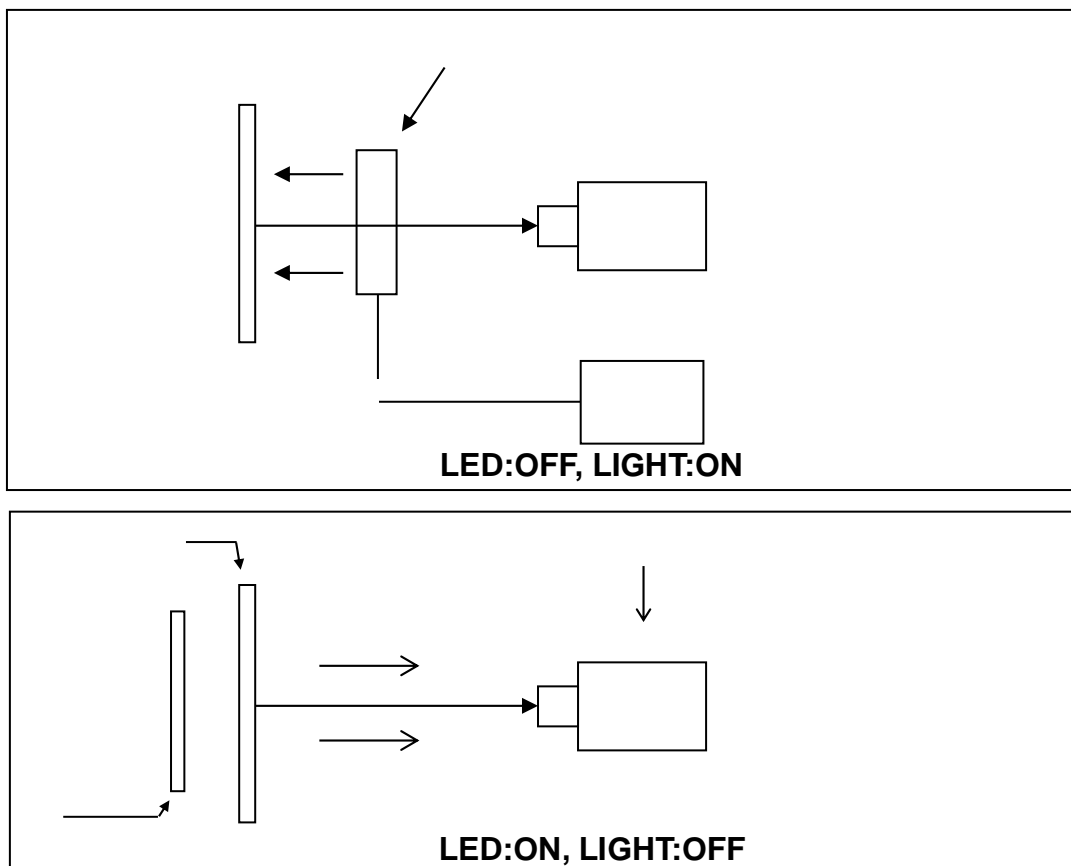
$V_{i50}$  : The analog input voltage when transmission is 50%. The 100% Transmission is defined as the transmission of LCD panel when all the Input terminals of module are electrically opened.

Note 6. Definition of viewing angle. Refer to figure as below.





Note 7.Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

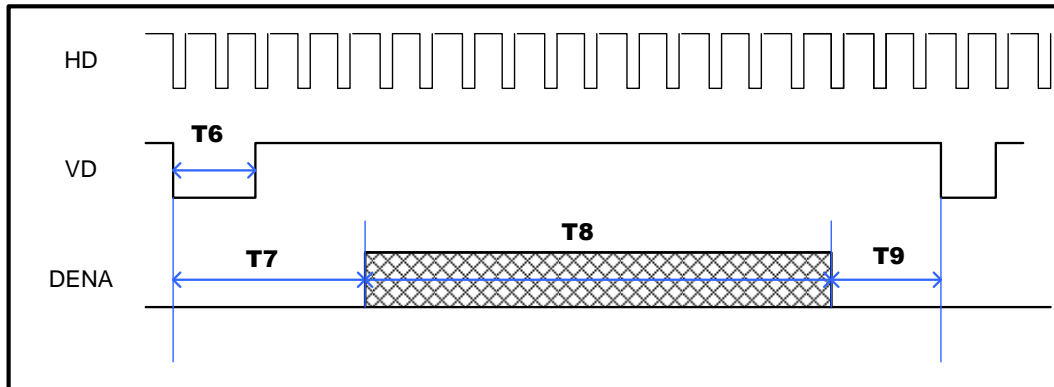
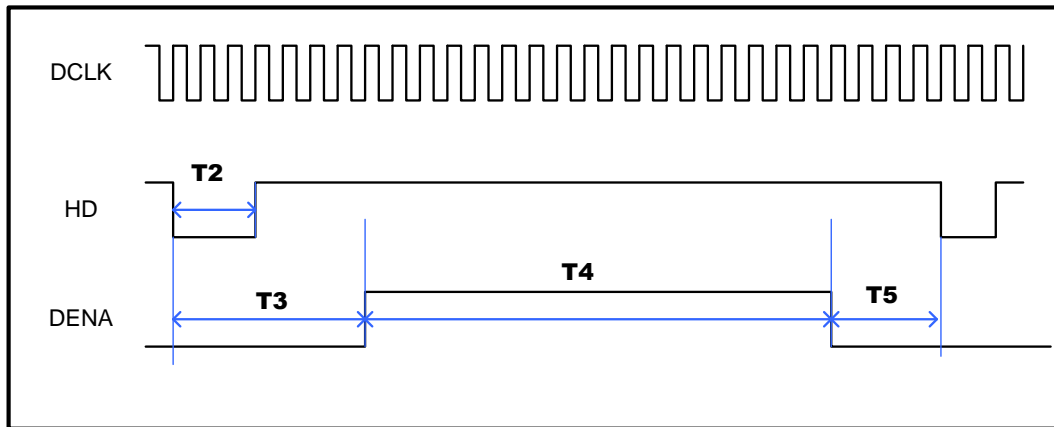
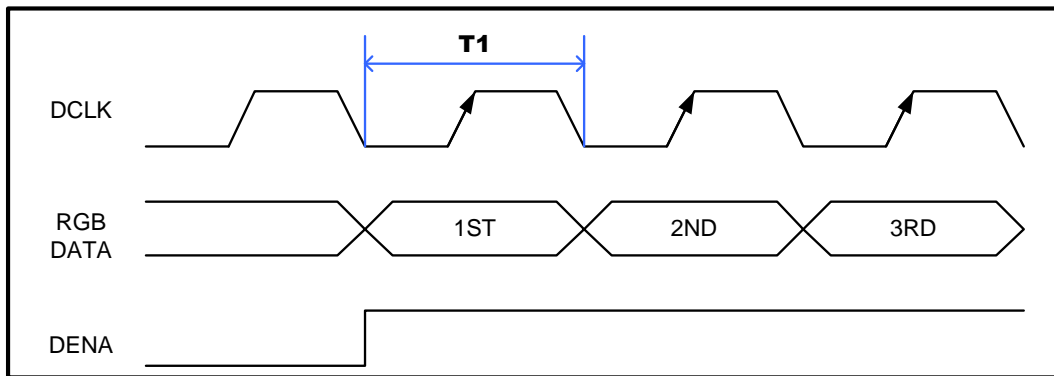


## 6.INTERFACE

| Pin no | Symbol     | I/O | Description                                     | Remark |
|--------|------------|-----|---|--------|
| 1      | LEDK       | P   | LED Back-light Cathode                          |        |
| 2      | LEDA       | P   | LED Back-light Anode                            |        |
| 3      | GND        | P   | Power GND                                       |        |
| 4      | VDD        | P   | Power supply for the logic (3.3V)               |        |
| 5      | R0         | I   | Red Data (LSB)                                  |        |
| 6      | R1         | I   | Red Data  |        |
| 7      | R2         | I   | Red Data  |        |
| 8      | R3         | I   | Red Data  |        |
| 9      | R4         | I   | Red Data  |        |
| 10     | R5         | I   | Red Data  |        |
| 11     | R6         | I   | Red Data  |        |
| 12     | R7         | I   | Green Data (MSB)                                |        |
| 13     | G0         | I   | Green Data (LSB)                                |        |
| 14     | G1         | I   | Green Data                                      |        |
| 15     | G2         | I   | Green Data                                      |        |
| 16     | G3         | I   | Green Data                                      |        |
| 17     | G4         | I   | Green Data                                      |        |
| 18     | G5         | I   | Green Data                                      |        |
| 19     | G6         | I   | Green Data                                      |        |
| 20     | G7         | I   | Green Data (MSB)                                |        |
| 21     | B0         | I   | Blue Data (LSB)                                 |        |
| 22     | B1         | I   | Blue Data                                       |        |
| 23     | B2         | I   | Blue Data                                       |        |
| 24     | B3         | I   | Blue Data                                       |        |
| 25     | B4         | I   | Blue Data                                       |        |
| 26     | B5         | I   | Blue Data                                       |        |
| 27     | B6         | I   | Blue Data                                       |        |
| 28     | B7         | I   | Blue Data (MSB)                                 |        |
| 29     | GND        | P   | Power GND                                       |        |
| 30     | PCLK       | I   | Clock signal. Latching data at the rising edge. |        |
| 31     | DISP       | I   | L : Standby mode. H: Normal display mode        |        |
| 32     | HSYNC      | I   | Horizontal sync input in digital RGB mode       |        |
| 33     | VSYNC      | I   | Vertical sync input in digital RGB mode.        |        |
| 34     | DE         | I   | Input data enable control                       |        |
| 35     | NC         | -   | No connection                                   |        |
| 36     | GND        | P   | Power GND                                       |        |
| 37     | LEFT/RIGHT | I   | L: From right to left<br>H: From left to right  |        |
| 38     | UP/DOWN    | I   | L: From down to left<br>H: From up to down      |        |
| 39     | NC         |     | No connection                                   |        |
| 40     | NC         |     | No connection                                   |        |

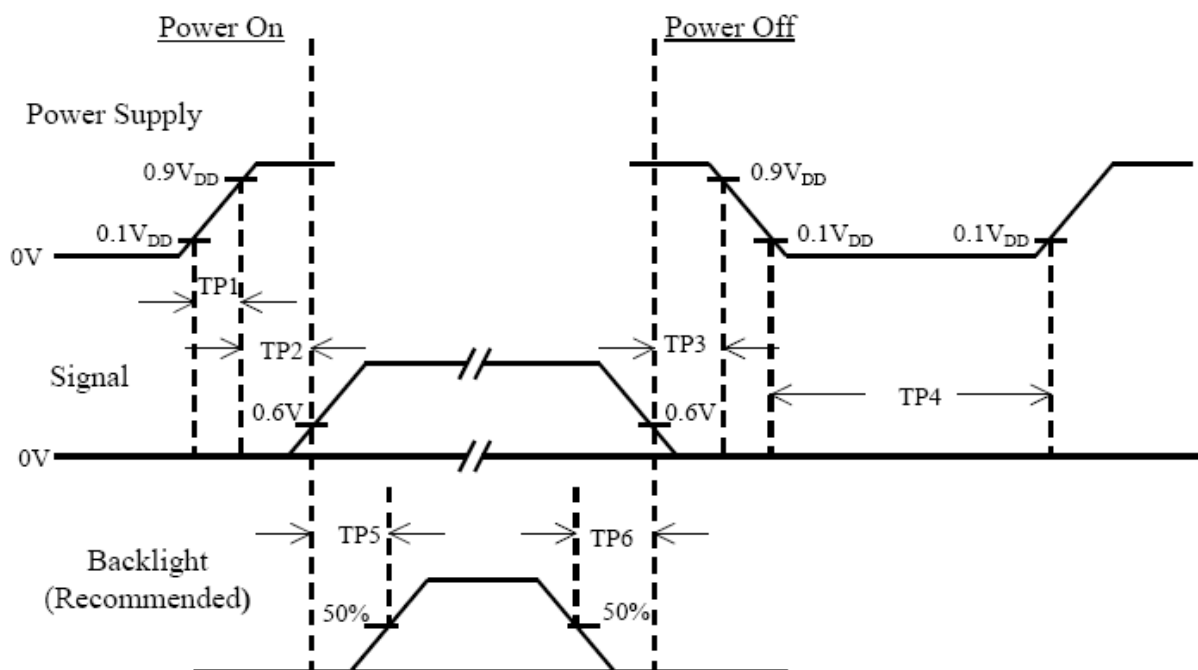
## 7. LCD INTERFACE TIMING

### 7.1 TTL RGB



| ITEM                      | SYMBOL   | MIN | TYP | MAX | UNIT   |
|---------------------------|----------|-----|-----|-----|--------|
| Clock Frequency           | 1/T1     | 23  | 25  | 27  | MHz    |
| HSYNC Pulse Wide          | T2       | 2   | 8   | 8   | clocks |
| HSYNC Back Porch          | T3       | 4   | 8   | 48  | Clocks |
| HSYNC Front Porch         | T5       | 4   | 8   | 48  | Clocks |
| Horizontal Display Period | T4       | 800 |     |     | Clocks |
| Horizontal total Period   | T3+T4+T5 | 808 | 816 | 896 | Clocks |
| VSYNC Pulse Wide          | T6       | 2   | 4   | 8   | Lines  |
| VSYNC Back Porch          | T7       | 4   | 8   | 12  | Lines  |
| VSYNC Front Porch         | T9       | 4   | 8   | 12  | Lines  |
| Vertical Display Period   | T8       | 480 |     |     | Lines  |
| Vertical total Period     | T7+T8+T9 | 488 | 496 | 504 | Lines  |

## 7.2 Power On/Off Sequence



| Item | Min. | Typ. | Max. | Unit | Remark |
|------|------|------|------|------|--------|
| TP1  | 0.5  | --   | 10   | msec |        |
| TP2  | 0    | --   | 50   | msec |        |
| TP3  | 0    | --   | 50   | msec |        |
| TP4  | 500  | --   | --   | msec |        |
| TP5  | 250  | --   | --   | msec |        |
| TP6  | 100  | --   | --   | msec |        |

Note :

- (1) The supply voltage of the external system for the module input should be the same as the definition of VDD.
- (2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become white.
- (3) In case of VDD = off level, please keep the level of input signal on the low or keep a high impedance.
- (4) TP4 should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

## 8. Reliability Test Items

| Test Item                                | Test Conditions   | Note |
|--|---|------|
| High Temperature Operation               | 70±3°C , t=240 hrs  |      |
| Low Temperature Operation                | -20±3°C , t=240 hrs   |      |
| High Temperature Storage                 | 80±3°C , t=240 hrs  | 1,2  |
| Low Temperature Storage                  | -30±3°C , t=240 hrs   | 1,2  |
| Storage at High Temperature and Humidity | 60°C, 90% RH , 240 hrs  | 1,2  |
| Thermal Shock Test                       | -20°C (30min) ~ 70°C (30min)<br>100 cycles  | 1,2  |
| Vibration Test (Packing)                 | Sweep frequency : 10 ~ 55 ~ 10 Hz/1min<br>Amplitude : 0.75mm<br>Test direction : X.Y.Z/3 axis<br>Duration : 30min/each axis | 2    |

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions (15-35°C , 45-65%RH).

Note 3 : The module shouldn't be tested more than one condition, and all the test conditions are independent.

Note 4 : All the reliability tests should be done without protective film on the module.

## **9. General Precautions**

### **9-1 Safety**

Liquid crystal is poisonous. Do not put it your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

### **9-2 Handling**

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

### **9-3 Static Electricity**

1. Be sure to ground module before turning on power or operation module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

### **9-4 Storage**

1. Store the module in a dark room where must keep at  $+25\pm 10^{\circ}\text{C}$  and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

### **9-5 Cleaning**

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

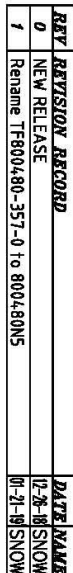
### **9-7 Others**

1. AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.
2. Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver

## Date : 2019/6/18

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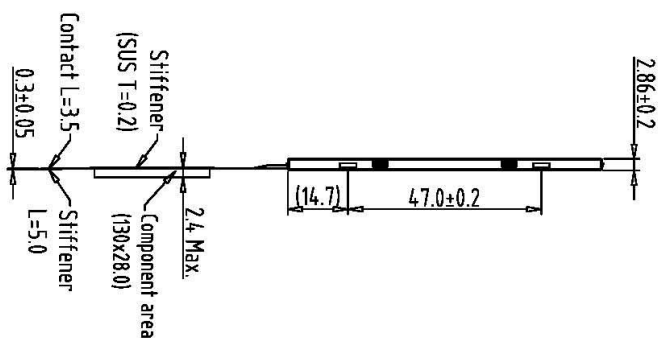
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Technical drawing of the LED module assembly. The drawing shows a side view of the assembly with various dimensions and components labeled.

Dimensions (mm):

- Overall width:  $177.8 \pm 0.3$  (LED)
- Overall height:  $51.9$
- LED chip width:  $60.0 \pm 0.5$
- LED chip height:  $76.4 \pm 0.3$  (LED)
- Stiffener width:  $30.0$
- Stiffener height:  $15.0$
- Stiffener material: SUS T=0.2
- LED chip dimensions:  $2-R0.3$ ,  $P0.5 \times 3.9$
- LED chip pitch:  $20.5 \pm 0.1$
- LED chip thickness:  $0.5$
- LED chip width:  $4.0$
- LED chip height:  $W=0.3$
- LED chip material:  $(12.0 \times 6.0 \times 0.06T)$
- LED chip label: Tape



Note: "107"

1. Unless indicated, Tolerance  $\pm 0.3"$
2. UV Glue For OLB Protection.
3. LCD 800x480 (R.G.B) TFT LCD  $\Rightarrow 5.0"$  TFT LCD

Back view

|   |                |    |  |                 |      | TITLE                         |
|---|----------------|----|--|-----------------|------|-------------------------------|
| 1 | 800480N3       | 7  |  | DIM.            | DWN. | 800480N5<br>(5.0") IPS<br>[1] |
| 2 | IF800480-357-0 | 8  |  | MM              | SNOW |                               |
|   |                | 9  |  | IR NO.          | DATE |                               |
| 3 |                | 10 |  | CHEK.           | DATE |                               |
| 4 |                | 11 |  | PARTS NO. ICM-1 | DATE |                               |
| 5 |                | 12 |  | APPD.           | DATE |                               |
| 6 |                |    |  | 800480N5        |      | DWG. NO.<br>*181212BMA        |
|   |                |    |  |                 |      | SHEET 1 OF 1                  |