



晶采光電科技股份有限公司
AMPIRE CO., LTD.

Specifications for LCD module

Customer	
Customer part no.	
Ample part no.	AM-800600C4TMQW-00H-A
Approved by	
Date	

Preliminary Specification
 Formal Specification

AMPIRE CO., LTD.

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Approved by	Checked by	Organized by
Patrick	Simon	Jessica

This Specification is subject to change without notice.

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2025/02/24		New Release	Jessica

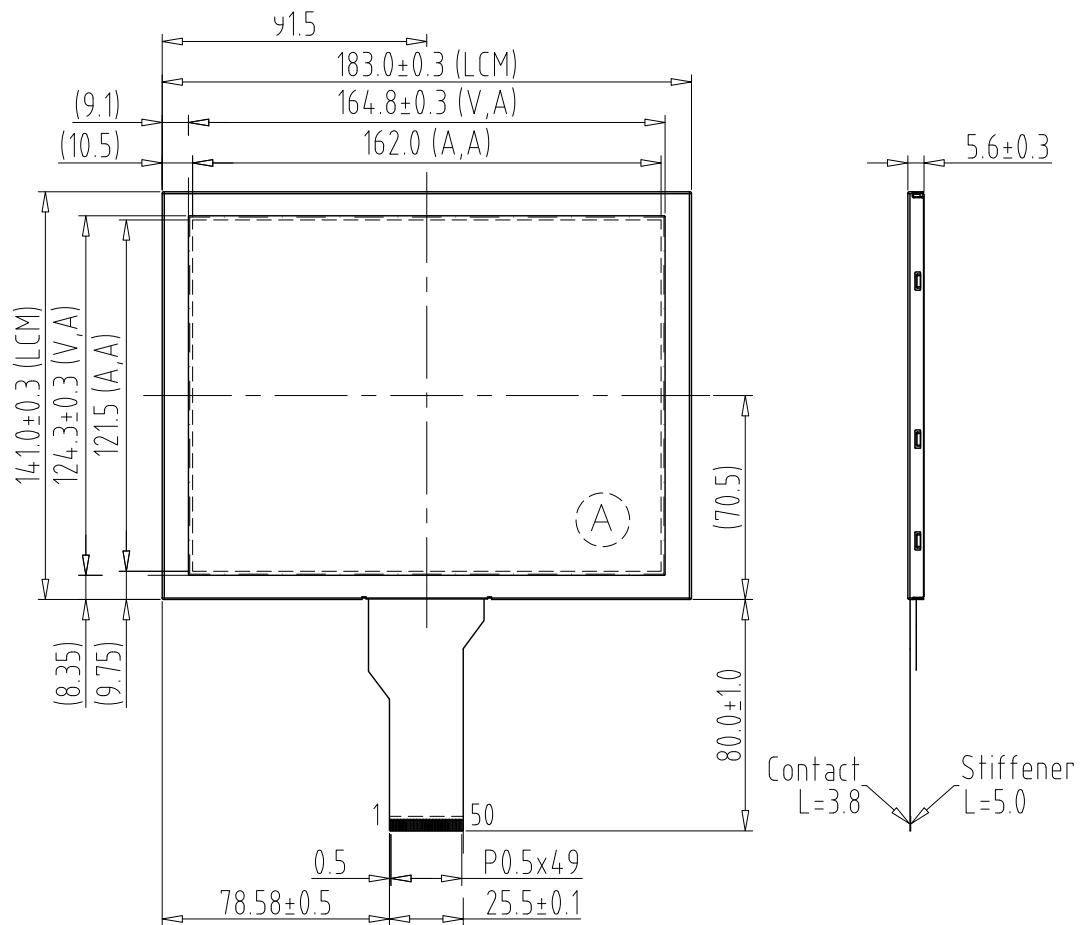
1. Features

8 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 8" TFT-LCD panel and LED backlight.

- (1) Construction: 8" a-Si TFT active matrix, White LED Backlight.
- (2) Resolution (pixel): 800(R.G.B) X600
- (3) Number of the Colors : 16M colors (R , G , B 8 bit digital each)
- (4) LCD type : Transmissive, normally White
- (5) Interface: RGB interface 50 pin
- (6) Power Supply Voltage: 3.3V for logic voltage.
- (7) Viewing Direction: 6 O'clock (Gray inversion)
- (8) New LCD driver IC

2. Physical Specifications

Item	Specifications	unit
LCD size	8 inch (Diagonal)	
Resolution	800 x (RGB) x 600	dot
Dot pitch	0.0675(W) x 0.2025(H)	mm
Color arrangement	RGB-stripe	
interface	Digital	



3. Absolute Max. Ratings

Item	Symbol	Values		UNIT	Note
		Min.	Max.		
Power voltage	VCC	-0.3	5	V	
	AVDD	6.5	13.5		
	VGH	-0.3	42		
	VGL	-20	0.3		
	VGH-VGL	-0.3	40		
Input signal voltage	Vi	-0.3	VCC+0.3	V	Note 1
Operation temperature	TOP	-20	70	°C	
Storage temperature	TST	-30	80	°C	
LED Reverse Voltage	V _R		1.2	V	Each LED Note 2
LED Forward Current	I _F		25	mA	Each LED

Note(1) The product is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above. Signals include: DCLK, DE, HS, VS, R0~R5, G0~G5, B0~B5.

Note(2) V_R Conditions: Zener Diode 20mA

4. Optical Characteristics

4.1 Typical Operation Conditions

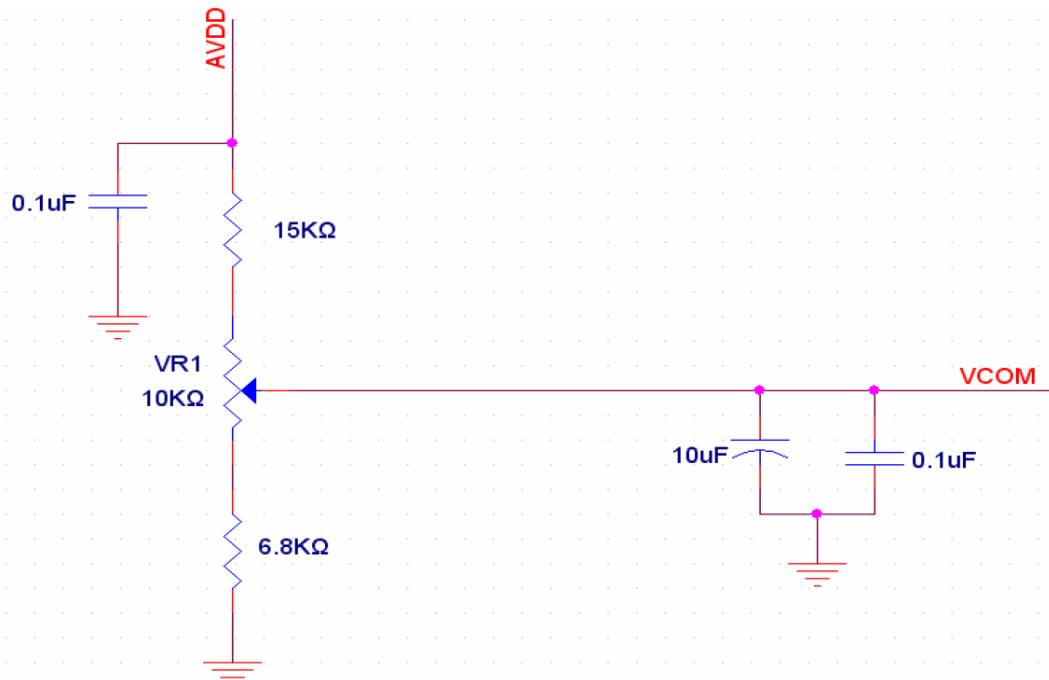
Item	Symbol	Values			Unit	Remark
		MIN	TYP	MAX		
Power Voltage	VCC	3.0	3.3	3.6	V	Note 2
	AVDD	10.2	10.4	10.6	V	
	VGH	15.3	16.0	16.7	V	
	VGL	-7.7	-7.0	-6.3	V	
Input signal voltage	VCOM	2.8	3.8	4.8	V	
Logic input high voltage	VTH	0.7VCC	-	VCC	V	Note 3
Logic input low voltage	VTL	GND	-	0.3VCC	V	Note 3

Note(1) Be sure to apply VCC and VGL to the LCD first, and then apply VGH.

Note(2) VCC setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note(3) DCLK,HS,VS,RSTB,UPDN,STLR,MODE,DITHB.

Note(4) Typical VCOM is only a reference value, it must be optimized according to each LCM. Be sure to use VR;



4.2 Current Consumption

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Current for Driver	ICC	-	5.5	10	mA	VCC=3.3V
	IAVDD	-	32	50	mA	AVDD=10.4V
	IGH	-	0.2	0.5	mA	VGH=16.0V
	IGL	-	0.2	1.0	mA	VGL=-7.0V

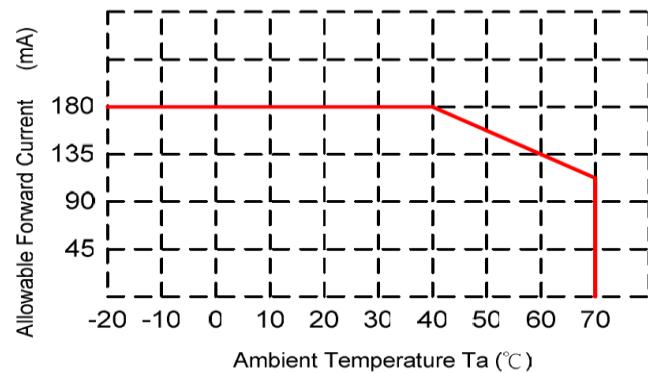
4.3 Backlight Driving Conditions

Item	Symbol	Values			Unit	Note
		Min.	Typ.	Max.		
LED voltage	VL	9.3	9.9	10.5	V	Note 1
LED current	IL	162	180	198	mA	Note 1
LED life time	--	20,000	--	--	Hr	Note 2

Note(1) The LED Supply Voltage is defined by the number of LED at $T_a=25^\circ C$ and $IL=180mA$.

Note(2) The “LED life time” is defined as the module brightness decrease to 50% original brightness at $T_a=25^\circ C$ and $IL=180mA$. The LED lifetime could be decreased if operating IL is larger than 180mA.

Note(3) When LCM is operated over $40^\circ C$ ambient temperature, the IL should be follow :



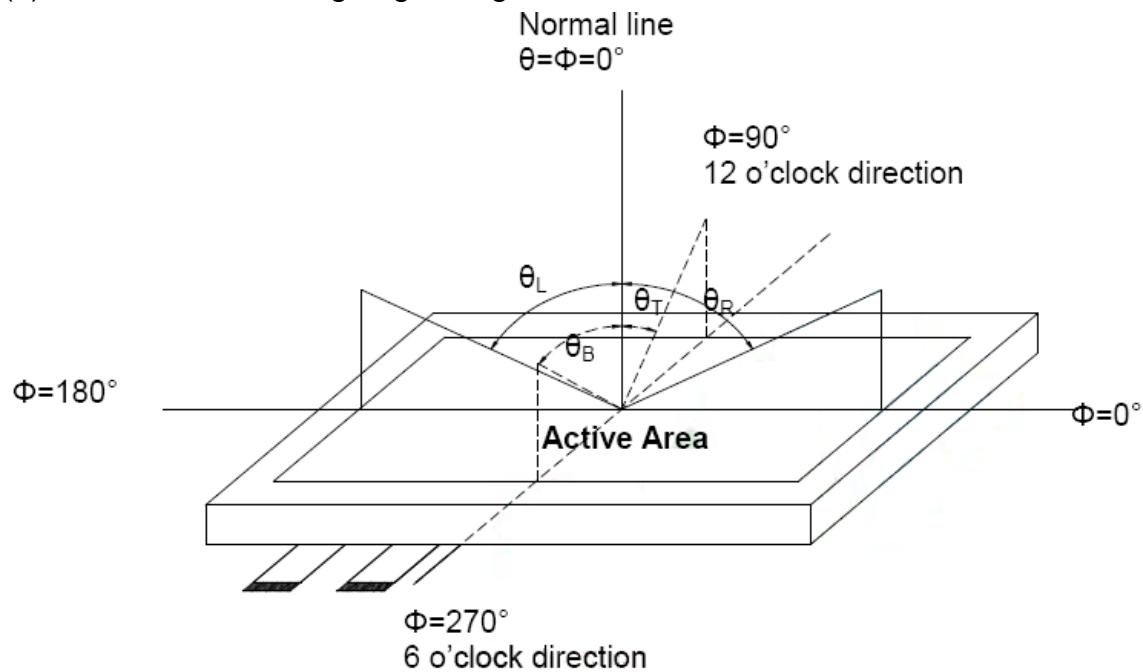
5. Optical Specifications

Item	Symbol	Condition	Values			Unit	Note
			Min.	Typ.	Max.		
Viewing angle (CR \geq 10)	θ L	$\Phi = 180^\circ$ (9 o'clock)	60	70	--	degree	Note1
	θ R	$\Phi = 0^\circ$ (3 o'clock)	60	70	--		
	θ T	$\Phi = 90^\circ$ (12 o'clock)	40	50	--		
	θ B	$\Phi = 270^\circ$ (6 o'clock)	60	70	--		
Response time	TON	Normal $\theta = \Phi = 0^\circ$	--	10	20	msec	Note3
	TOFF		--	15	30	msec	
Contrast ratio	CR		400	500	--	--	Note4
Color chromaticity	WX		0.26	0.31	0.36	--	Note5 Note6
	WY		0.28	0.33	0.38	--	
Luminance	L		200	250	--	cd/m ²	Note6
Luminance uniformity	YU		70	75	--	%	Note7

Test Conditions:

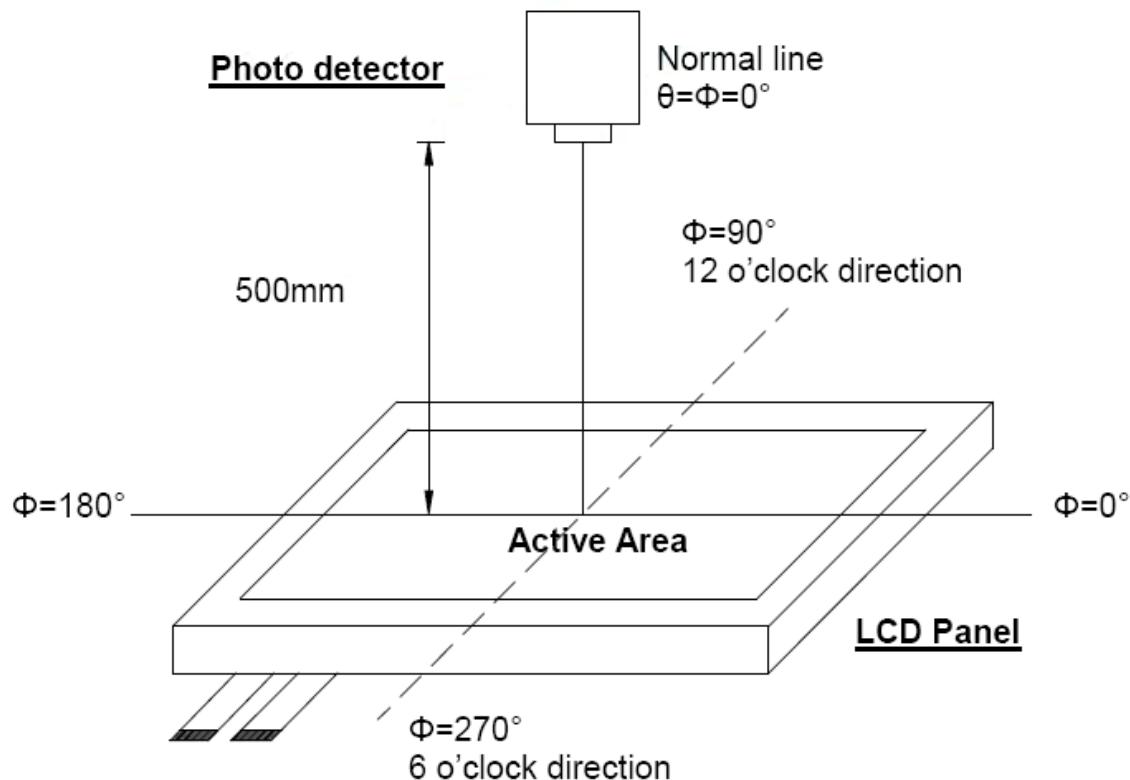
1. VCC = 3.3V, IL = 180mA (Backlight current), the ambient temperature is 25°C.
2. The test systems refer to Note 2.

Note(1) Definition of viewing angle range



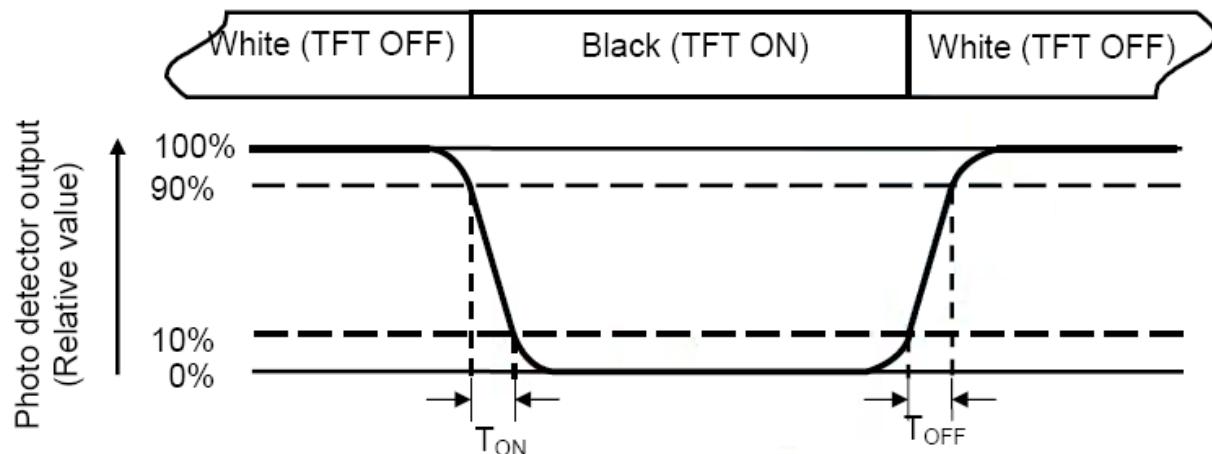
Note(2) Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° / Height: 500mm.)



Note(3) Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.



Note(4) Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note(5) Definition of color chromaticity (CIE1931)

Color coordinated measured at center point of LCD.

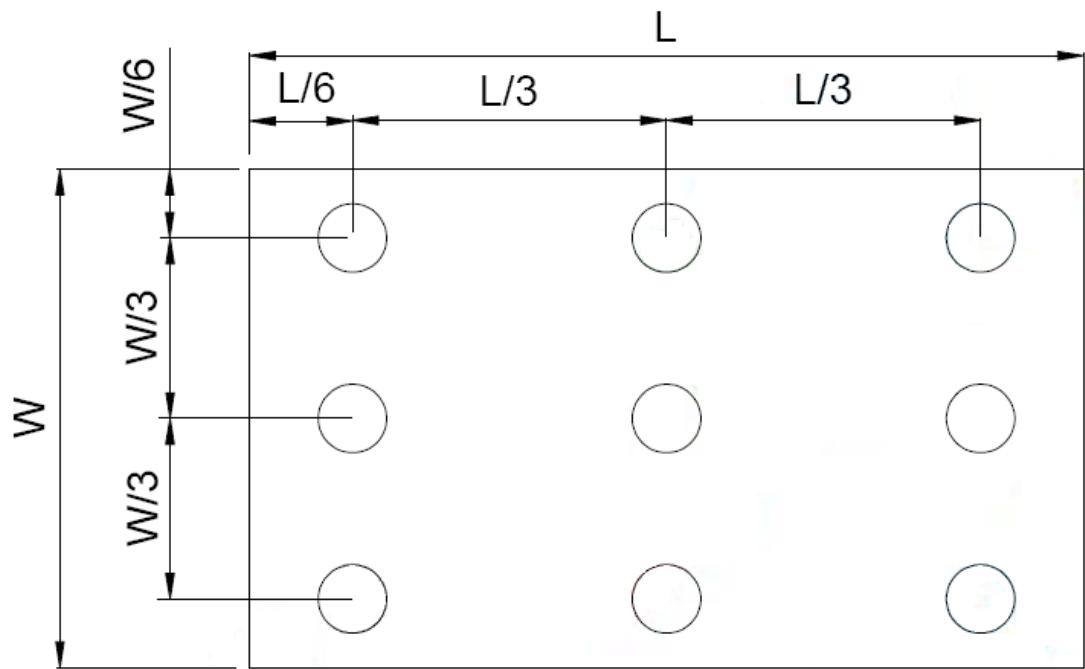
Note(6) All input terminals LCD panel must be ground when measuring the center area of the panel.

Note(7) Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to bellow figure). Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{min}}{B_{max}}$$

L ----- Active area length W ----- Active area width



B_{max} : The measured maximum luminance of all measurement position.

B_{min} : The measured minimum luminance of all measurement position.

6. Interface

TFT LCD Panel Driving Section

Pin No.	Symbol	I/O	Description	Note
1	LED+	P	LED Back-light Anode	
2	LED+	P	LED Back-light Anode	
3	LED-	P	LED Back-light Cathode	
4	LED-	P	LED Back-light Cathode	
5	GND	P	Power ground	
6	VCOM	I	Common voltage	
7	VCC	P	Power for Digital circuit	
8	MODE	I	DE/SYNC mode select	(3)
9	DE	I	Data Input Enable	
10	VS	I	Vertical Sync Input	
11	HS	I	Horizontal Sync Input	
12	B7	I	Blue data(MSB)	
13	B6	I	Blue data	
14	B5	I	Blue data	
15	B4	I	Blue data	
16	B3	I	Blue data	
17	B2	I	Blue data	
18	B1	I	Blue data	
19	B0	I	Blue data(LSB)	
20	G7	I	Green data(MSB)	
21	G6	I	Green data	
22	G5	I	Green data	
23	G4	I	Green data	
24	G3	I	Green data	
25	G2	I	Green data	
26	G1	I	Green data	
27	G0	I	Green data(LSB)	
28	R7	I	Red data(MSB)	
29	R6	I	Red data	
30	R5	I	Red data	
31	R4	I	Red data	
32	R3	I	Red data	
33	R2	I	Red data	
34	R1	I	Red data	
35	R0	I	Red data(LSB)	
36	GND	P	Power ground	
37	DCLK	I	Sample clock	
38	GND	I	Power ground	
39	L/R	I	Right/ left selection	(2),(5)
40	U/D	I	Up/down selection	(2),(5)
41	VGH	P	Gate ON voltage	
42	VGL	P	Gate OFF voltage	
43	AVDD	P	Power for Analog circuit	
44	RESET	I	Global reset pin	(1)

45	NC	-	No connection	
46	VCOM	I	Common voltage	
47	DITHB	I	Dithering function	(4)
48	GND	P	Power ground	
49	NC	-	No connection	
50	NC	-	No connection	

I: input, O: output, P: power

Note(1) Global reset pin. Active Low to enter Reset State. Suggest to connecting with an RC reset circuit for stability. Normally pull high.

Note(2) Selection of scanning mode

Setting of control input		Scanning direction
U/D	R/L	
GND	VCC	Up to down, left to right
VCC	GND	Down to up, right to left
GND	GND	Up to down, right to left
VCC	VCC	Down to up, left to right

Note(3) DE/SYNC mode select, normally pull high.

H: DE mode.

L: HS/VS mode.

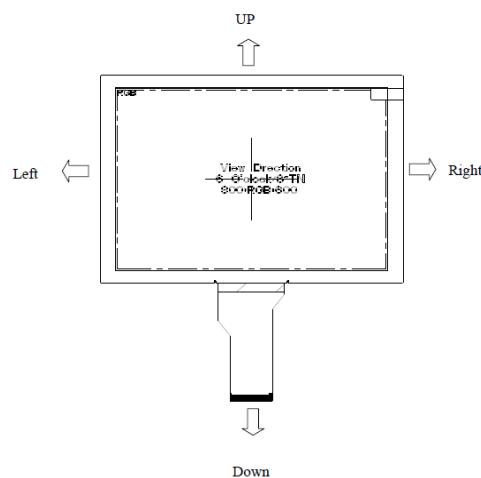
Note(4) Dithering function enables control. Normally pull high.

DITHB="1", Disable internal dithering function. For 18bit RGB interface, connect two LSB bits of all the R/G/B data buses to GND.

DITHB="0", Enable internal dithering function, For TTL 24bit parallel RGB image data input.

Note(5) Definition of scanning direction.

Refer to the figure as below:



7. Input Signal

7.1 AC Electrical Characteristics

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
HS setup time	T_{hst}	8	10	12	Ns	
HS hold time	T_{hhd}	8	10	12	Ns	
VS setup time	T_{vst}	8	10	12	Ns	
VS hold time	T_{vhd}	8	10	12	Ns	
Data setup time	T_{dsu}	8	10	12	Ns	
Data hold time	T_{dhd}	8	10	12	Ns	
DE setup time	T_{esu}	8	10	12	Ns	
DE hold time	T_{ehd}	8	10	12	Ns	
VDD Power On Slew rate	T_{POR}	1	2	20	ms	
RSTB pulse width	T_{Rst}	10	20	50	us	
CLKIN cycle time	T_{coh}	20	25	30	Ns	
CLKIN pulse duty	T_{cwh}	40	50	60	%	

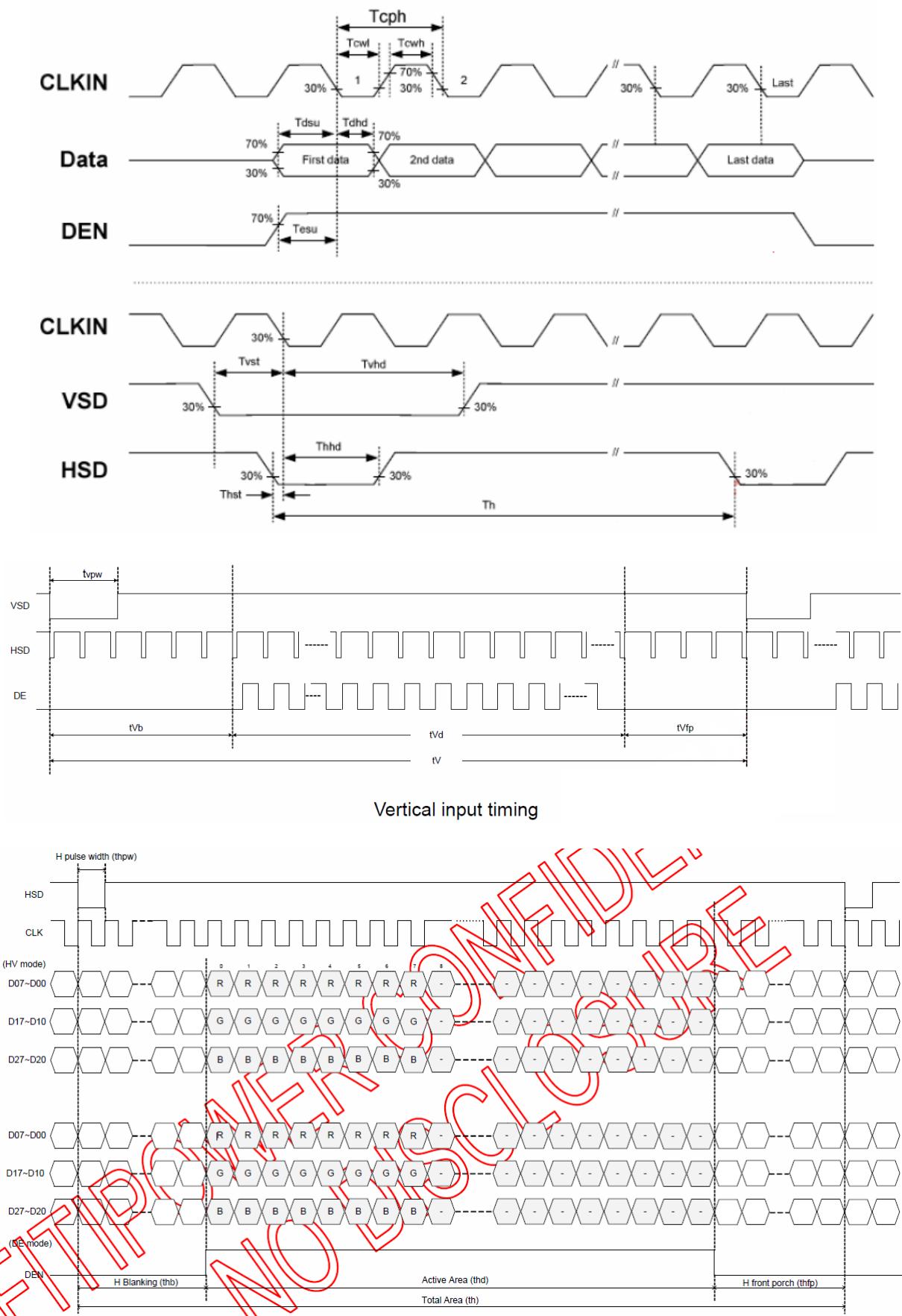
7.2 Timing

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Horizontal Display Area	thd	800			DCLK	
DCLK Frequency	fclk	33	40	50	MHz	
One Horizontal Line	th	862	1056	1200	DCLK	
HS pulse width	thpw	1	6	40	DCLK	
HS Back Porch(Blanking)	thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Vertical Display Area	tvd	600			TH	
VS period time	tv	624	635	700	TH	
VS pulse width	tvpw	1	3	20	TH	
VS Back Porch(Blanking)	tvb	23	23	23	TH	
VS Front Porch	tvfp	1	12	77	TH	

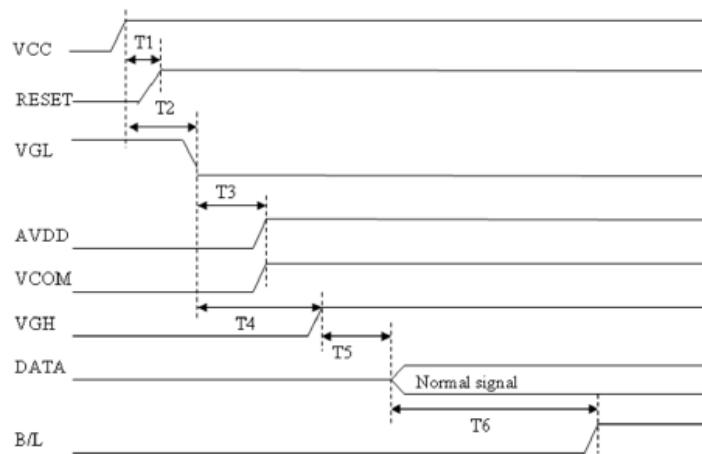
Note: Frame rate is $60\pm5\text{Hz}$

7.3 Input Clock and Data Timing Diagram



8. Power Sequence

Power on sequence

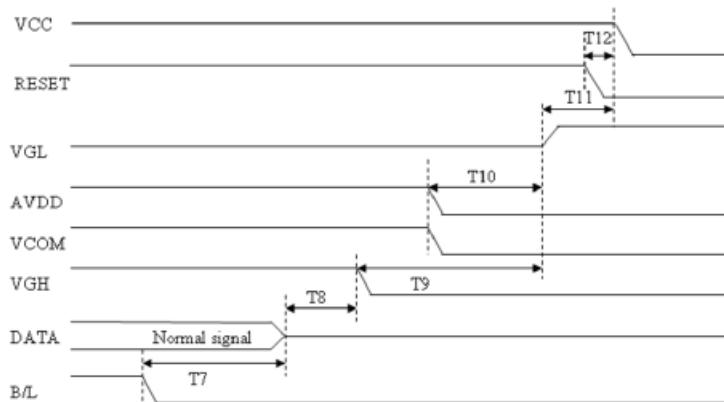


VCC → RESET → DATA → AVDD Gamma → VGL → VGH → B/L

Symbol	SPEC			Unit
	Min.	Typ.	Max.	
T1	1	1	1.5	ms
T2	20	30	50	ms
T3	5	20	50	ms
T4	10	20	50	ms
T5	10	20	50	ms
T6	200	300	400	ms

Note: Data include R0~R7, B0~B7, GO~G7, UD, LR, DCLK, HS, VS, DE.

Power off sequence



Note: Data include R0~R7, B0~B7, GO~G7, STLR, UPDN, DCLK, HS, VS, DE.

Symbol	SPEC			Unit
	Min.	Typ.	Max.	
T7	0	30	50	ms
T8	10	20	50	ms
T9	10	20	50	ms
T10	5	20	50	ms
T11	20	30	50	ms
T12	1	1.5	3	ms

9. Reliability Test

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	60°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 70°C (30min) cycles	100 1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note(1) Condensation of water is not permitted on the module.

Note(2) The module should be inspired after 1 hour storage in normal conditions (15~35°C, 45~65%RH).

Note(3) The module shouldn't be tested over one condition, and all the tests are independent.

Note(4) All reliability tests should be done without the protective film.

Definitions of life end point:

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of initial value.

10. Use Precautions

10.1 Handling precautions

- (1) Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

10.2 Handling

- (1) The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- (2) The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
- (3) To avoid contamination on the display surface, do not touch the module surface with bare hands.
- (4) Keep a space so that the LCD panels do not touch other components.
- (5) Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
- (6) Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
- (7) Do not leave module in direct sunlight to avoid malfunction of the ICs.

10.3 Static Electricity

- (1) Be sure to ground module before turning on power or operation module.
- (2) Do not apply voltage which exceeds the absolute maximum rating value.

10.4 Storage

- (1) Store the module in a dark room where must keep at $+25\pm10^{\circ}\text{C}$ and 65%RH or less.
- (2) Do not store the module in surroundings containing organic solvent or corrosive gas.
- (3) Store the module in an anti-electrostatic container or bag.

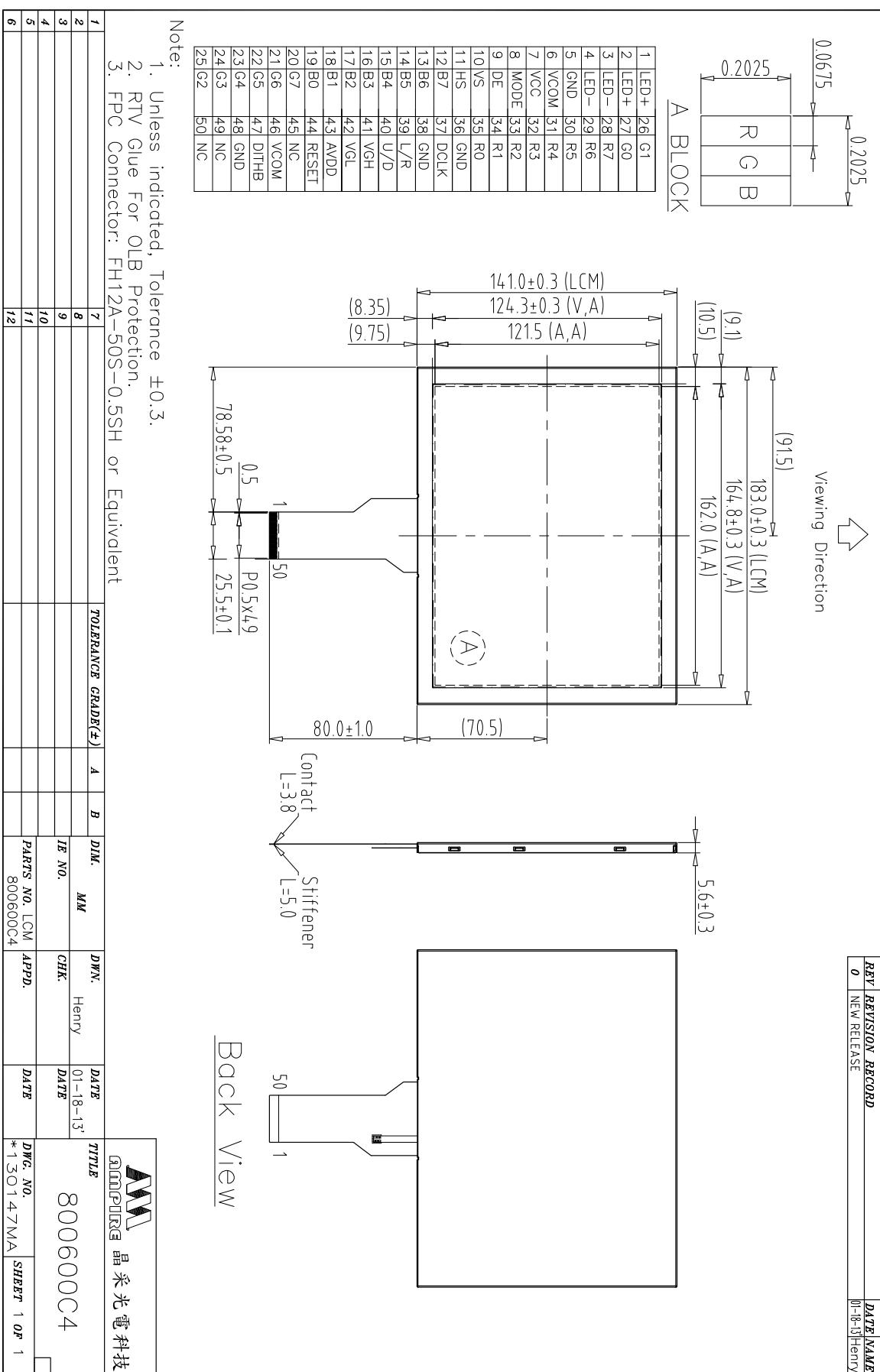
10.5 Cleaning

- (1) Do not wipe the polarizer with dry cloth. It might cause scratch.
- (2) Only use a soft cloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

10.6 Others

- (1) **Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver**
- (2) AMIPRE will provide one year warranty for all products and three months warranty for all repairing products.

11. Outline Dimension



Note: _____

2. RTVO Glue For OLB Protection
3. FPC Connector: FH12A-50S-0.5SH or Equivalent

TOLERANCE GRADE (\pm)	A	B	DIM. MM	DWY. HENRY	DATE 01-18-13	TITLE 800600C4
IE NO.	CHK.					DATE
PARTS NO.	LCM	APPD.	DATE	DWG. NO. *130147MA	SHEET 1 OF 1	
	800600C4					

Date: 2025/02/24

AMPIRE CO., LTD.

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12. Package

TBD