



晶采光電科技股份有限公司  
AMPIRE CO., LTD.

# Specifications for LCD module

Customer	
Customer part no.	
Ampire part no.	AM-800600K2MZQW-A1H
Approved by	
Date	

- ☐ Preliminary Specification  
☒ Formal Specification

**AMPIRE CO., LTD.**

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Approved by	Checked by	Organized by
Patrick	Simon	Jessica

This Specification is subject to change without notice.

## RECORD OF REVISION

Revision Date	Page	Contents	Editor
2025/03/03	-	New Release	Jessica
2025/03/31	6	Update supply voltage	Jessica

## 1. Introduction

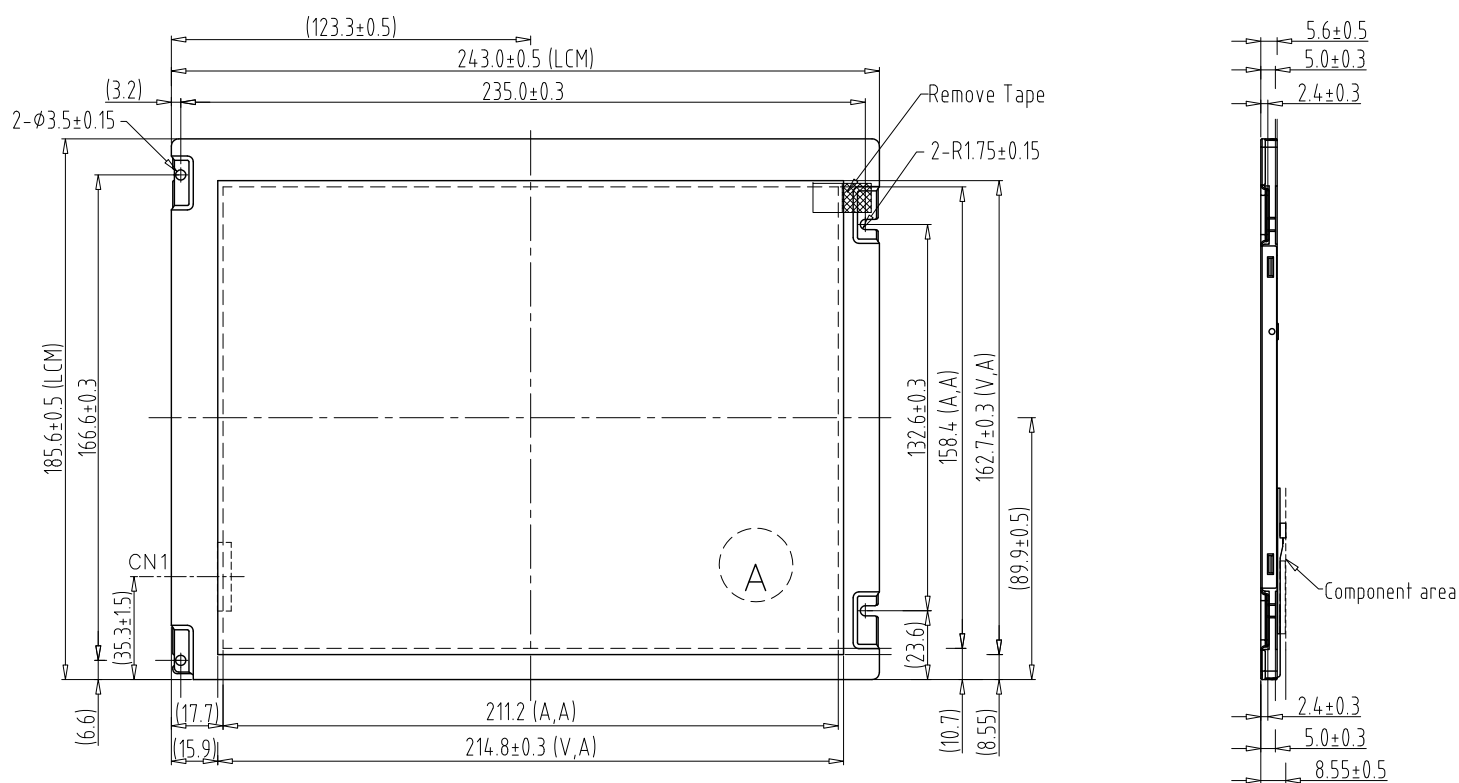
Ampire 10.4" Display Module is a color active matrix TFT-LCD that uses amorphous silicon TFT as a switching device. This model is composed of a TFT-LCD panel, a driving circuit. This TFT-LCD has a high resolution (800(R.G.B) x 600) and can display up to 16.7M colors.

### 1.1 Features

- (1) Construction: a-Si TFT-LCD with driving system, White LED Backlight.
- (2) LCD type : Transmissive , Normally Black
- (3) Number of the Colors : 16.7M colors (R,G,B 8 bit digital each)
- (4) LVDS Interface.
- (5) LCD Power Supply Voltage: 3.3V single power input, built-in power supply circuit.
- (6) Build-in LED driver IC (12V)
- (7) RoHS compliant

## 2. Physical Specifications

Item	Specifications	Unit
Display Resolution(dot)	800RGB (W) x 600(H)	dots
Pixel Pitch	0.264 (W) x 0.264 (H)	mm
Color Configuration	R.G.B -stripe	
Backlight Unit	LED	



### 3. Absolute Max. Ratings

Item	Symbol	Min.	Max.	Unit	Note
Supply Voltage Range	VDD	-0.3	3.96	V	GND=0V
Voltage range at any terminal	VIN	-0.3	VDD+0.3	V	Note 2
Operating Temperature	TOP	<b>-20</b>	<b>70</b>	°C	
Storage Temperature	TSTG	<b>-30</b>	<b>80</b>	°C	

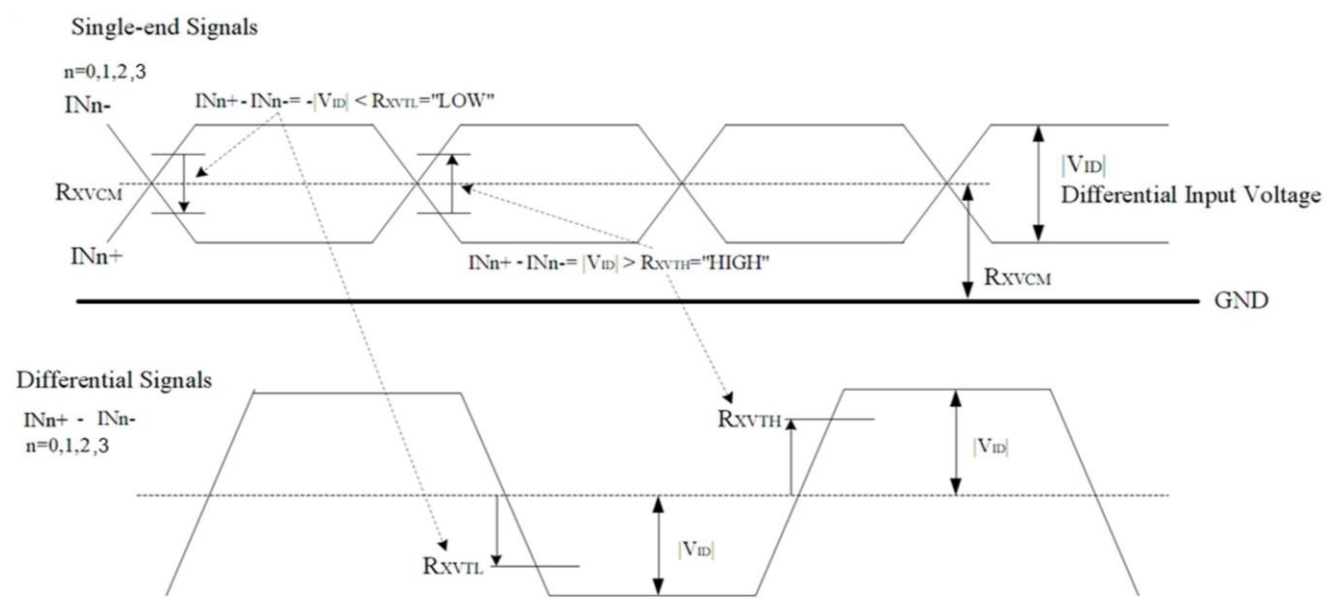
Note(1) The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

## 4. Electrical Characteristics

Temperature 25°C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Supply Voltage	VDD	3.2	3.3	3.4	V	Include ripple
Supply Current	IDD	--	400	--	mA	White pattern
Power Consumption	P	--	1.32	--	W	LCD only 60Hz white pattern without backlight
VDD rush current	Irush	-	-	2.4	A	
LVDS Differential input high threshold	RxVTH	-	-	+37	mV	LVDS Differential input high threshold
LVDS Differential input low threshold	RxVTL	-37	-	-	mV	
Differential input voltage	VID	0.1	-	0.4	V	
LVDS input common mode voltage	RxVCM	600	1200	1375	mV	$RxVCM +  VID  \leq 1650\text{mV}$ $RxVCM -  VID  \geq 400\text{mV}$

Note(1) LVDS DC characteristics



## 5. Backlight Driving Circuit

Temperature 25°C

Item	Symbol	Min.	Typ.	Max.	Unit	Note
LED Driving Voltage	VCC	--	12	--	V	
LED Driving Current	ICC	--	700	--	mA	
LED Driving ON/OFF Voltage	VEN_H	2.4	--	--	V	
	VEN_L	--	--	0.5	V	
LED Driving Dimming Voltage	VDim_H	2.5	--	--	V	
	VDim_L	--	--	0.3	V	
PWM Dimming Frequency	fPWM	0.1	--	25	kHz	
Forward Voltage	VAK	16.2	--	20.4	V	Note(1)(2)(3)(4)(5)
Forward Current	IAK	-	315	-	mA	Note(1)(2)(3)(4)(5)
LED life time		-	50K	-	Hrs	Note(1)(2)(3)(4)(5)

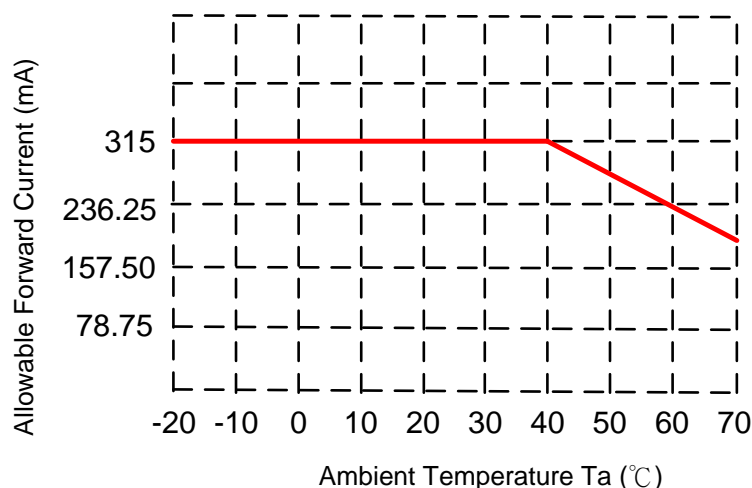
Note(1) If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced.

Note(2) Operating life means brightness goes down to 50% of initial brightness. Typical operating life time is estimated data.

Note(3) The backlight must be driven by constant current source.

Note(4) Brightness to be decreased to 50% of the initial value.

Note(5) When LCM is operated over 40°C ambient temperature, the **IAK** should be follow :



## 6. Optical Characteristics

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Response Time		T <sub>r</sub> +T <sub>f</sub>	Θ=Φ=0°	-	35	45	ms	(1)(4)
Contrast Ratio		CR		700	1000	-	-	(1)(3)
Viewing Angle		ΘT	CR≥10	78	88	-	degree	(1)(2)
		ΘB		78	88	-		
		ΘL		78	88	-		
		ΘR		78	88	-		
Luminance		L	Θ=Φ=0°	800	1000	-	cd/m <sup>2</sup>	(1)(7)
Luminance Uniformity		ΔL		75	80	-	%	(1)(6)
Color Chromaticity	Red	Rx		Typ. -0.05	0.59	Typ. +0.05	-	(1)(5)
		Ry			0.32		-	
	Green	Gx			0.33		-	
		Gy			0.60		-	
	Blue	Bx			0.17		-	
		By			0.11		-	
	White	Wx			0.31		-	
		Wy			0.33		-	



Note(1) Definition of optical measurement system

The optical characteristics should be measured in dark room. The optical characteristics are measured at the center point of the LCD screen.

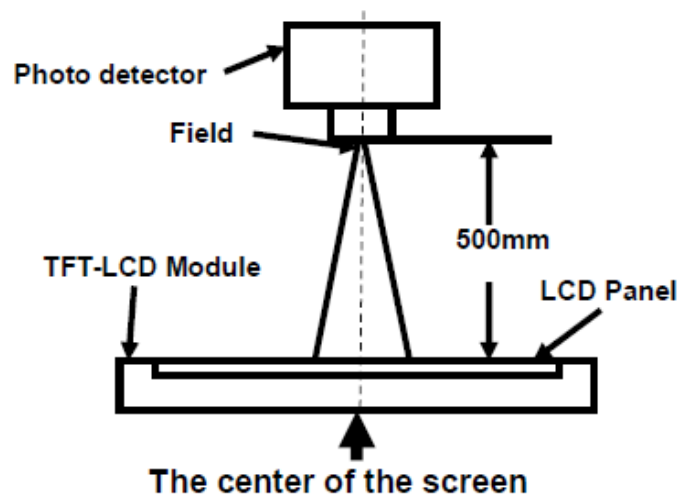


Fig1.Measurement Set Up

Note(2) Definition of viewing angle range and measurement system. Viewing angle is measured at the center point of the LCD.

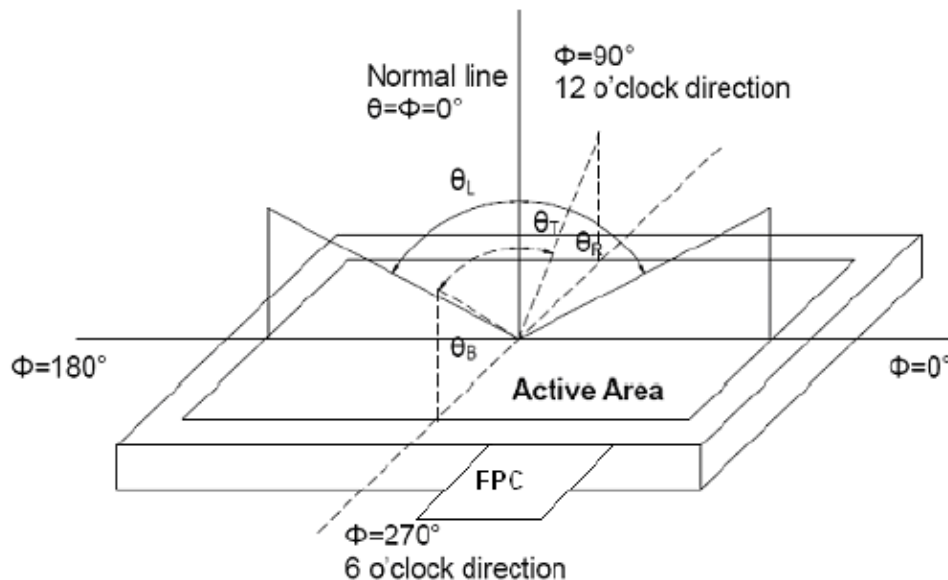


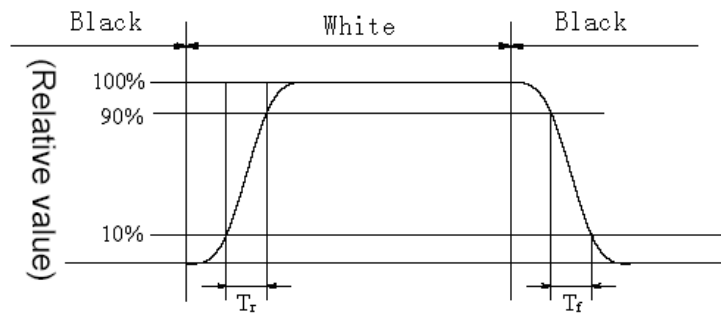
Fig2.Measurement viewing angle

Note(3) Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

Note(4) Definition of Response time

The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time ( $T_r$ ) is the time between photo detector output intensity changed from 10% to 90%. And fall time ( $T_f$ ) is the time between photo detector output intensity changed from 90% to 10%.



Note(5) Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note(6) Definition of Luminance Uniformity

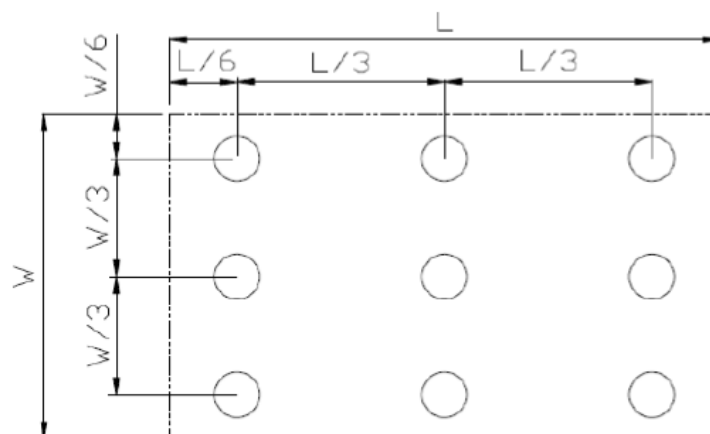
Active area is divided into 9 measuring areas (Refer Fig.5). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity ( $U$ ) =  $L_{min}/L_{max}$

$L_{max}$ : The measured Maximum luminance of all measurement position.

$L_{min}$ : The measured Minimum luminance of all measurement position.

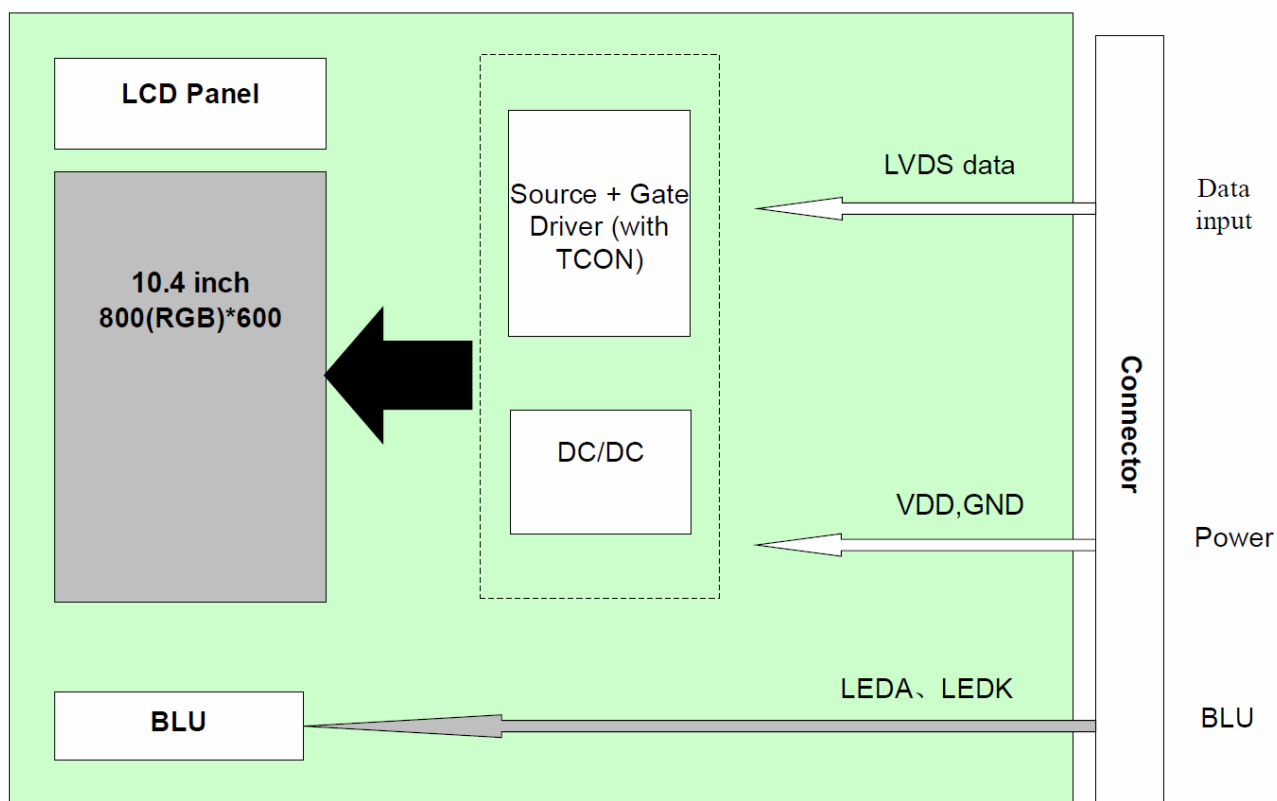
$L$ -----Active area length;  $W$ -----Active area width



Note(7) Definition of Luminance:

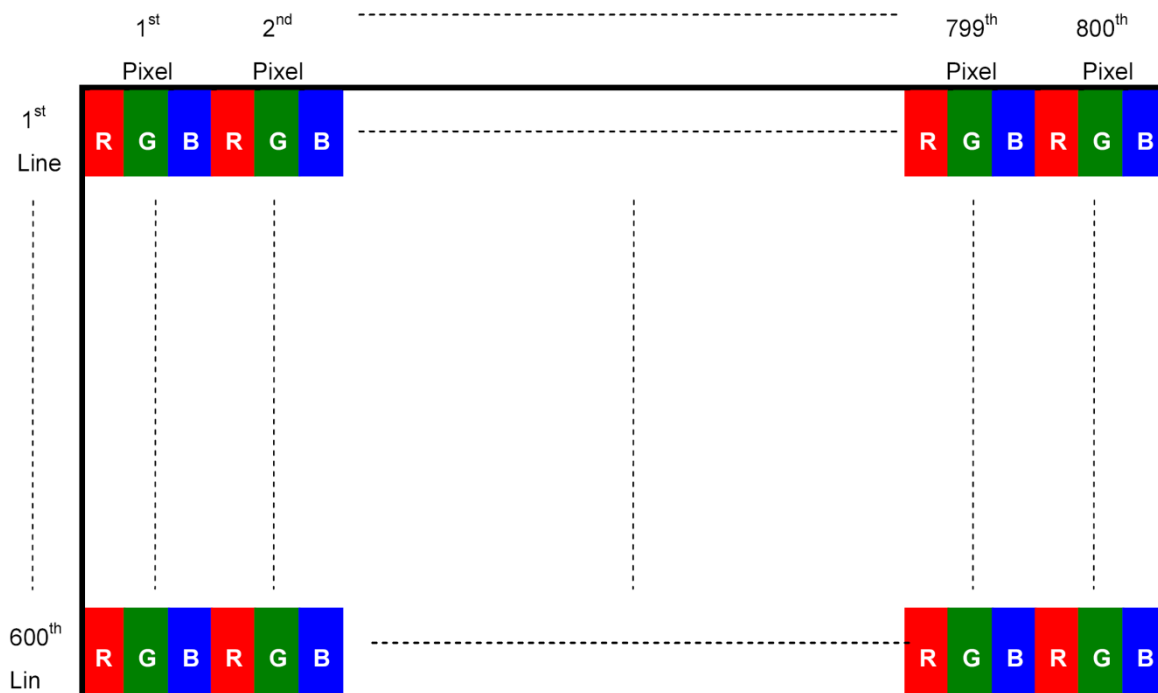
Measure the luminance of white state at center point.

## 7. Block Diagram



## LCD Pixel Format

Following figure shows the relationship between input signal and LCD pixel format.



## 8. Interface

### LVDS CN

CN1: STM MSB24013P20HA or Equivalent, Mating Connector: STM P24013P20 or Equivalent

Pin no	Symbol	Function
1	VDD	Power Supply:3.3V
2	VDD	Power Supply:3.3V
3	GND	Power Ground
4	DPS	Reverse Scan Function [H: Enable ; L: Disable]
5	RxIN0-	Transmission Data of Pixels 0
6	RxIN0+	Transmission Data of Pixels 0
7	GND	Power Ground
8	RxIN1-	Transmission Data of Pixels 1
9	RxIN1+	Transmission Data of Pixels 1
10	GND	Power Ground
11	RxIN2-	Transmission Data of Pixels 2
12	RxIN2+	Transmission Data of Pixels 2
13	GND	Power Ground
14	RxCLKIN-	LVDS receiver signal clock
15	RxCLKIN+	LVDS receiver signal clock
16	GND	Power Ground
17	RxIN3-	Transmission Data of Pixels 3
18	RxIN3+	Transmission Data of Pixels 3
19	GND	Power Ground
20	GND	Power Ground

Voltage level - H: 3.3V; L: 0V

### Backlight adjustment PIN Definition

CN2: ENTERY 3808K-F05N-03L or Equivalent, Mating Connector: ENTERY H208K-P05N-02B or Equivalent

Pin no	Symbol	Function
1	VCC	Power Supply:12V
2	GND	Power Ground
3	ON/OFF	Function selection
4	Dimming	Adjust brightness
5	NA	No Connection

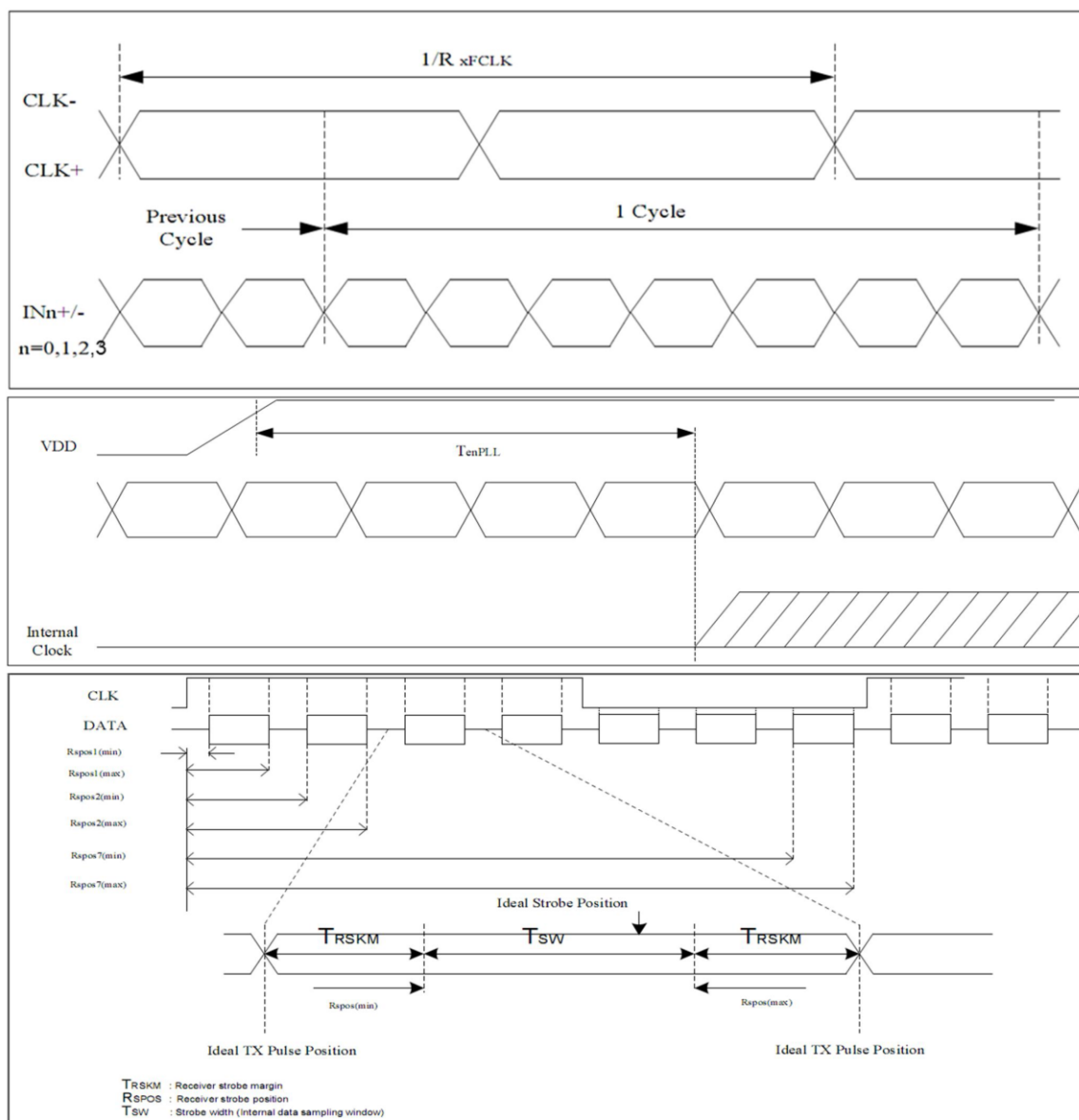
### Backlight Power Source PIN Definition

Pin no	Symbol	Function
1	A	LEDA
2	K	LEDK

## 9. Timing Characteristic of the LVDS

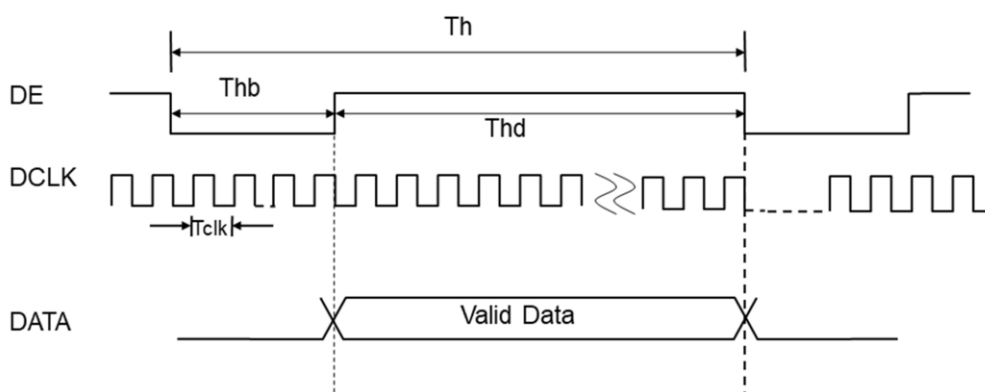
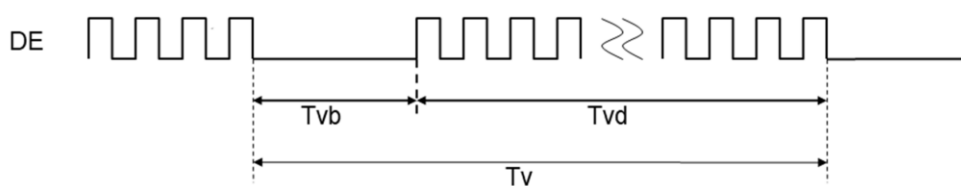
### 9.1 AC characteristics

Parameter	Symbol	Min	Typ	Max	Unit	Remark
Dclk Frequency	RxFCLK	36.3	-	50.3	MHZ	
Input Data Skew Margin	TRSKM	-0.2	-	0.2	UI	VID  = 200mV RxVCM = 1.2V 1UI=1/(RxFCLKx7)
Clock High Time	TLVCH	-	$3.5/(7 \cdot RxFCLK)$	-	ns	
Clock Low Time	TLVCL	-	$3.5/(7 \cdot RxFCLK)$	-	ns	
PLL Wake-up Time	T <sub>emPLL</sub>	-	-	150	us	



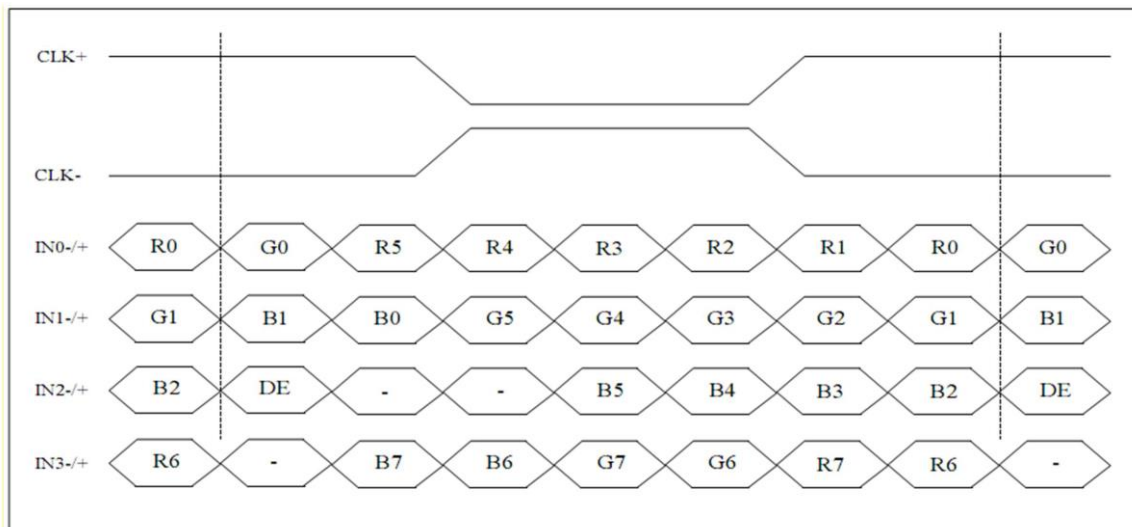
## 9.2 Data Input Timing Parameter Setting

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK frequency		Fclk	36.3	39.6	50.3	MHz	Tclk=1/Fclk
Horizontal section	Horizontal total	Th	945	1000	1150	Tclk	
	Horizontal blanking	Thb	145	200	350	Tclk	
	Valid Data Width	Thd	800			Tclk	
Vertical section	Vertical total	Tv	640	660	730	TH	
	Vertical blanking	Tvb	40	60	130	TH	
	Valid Data Width	Tvd	600			TH	
Frame Rate		F	60			Hz	



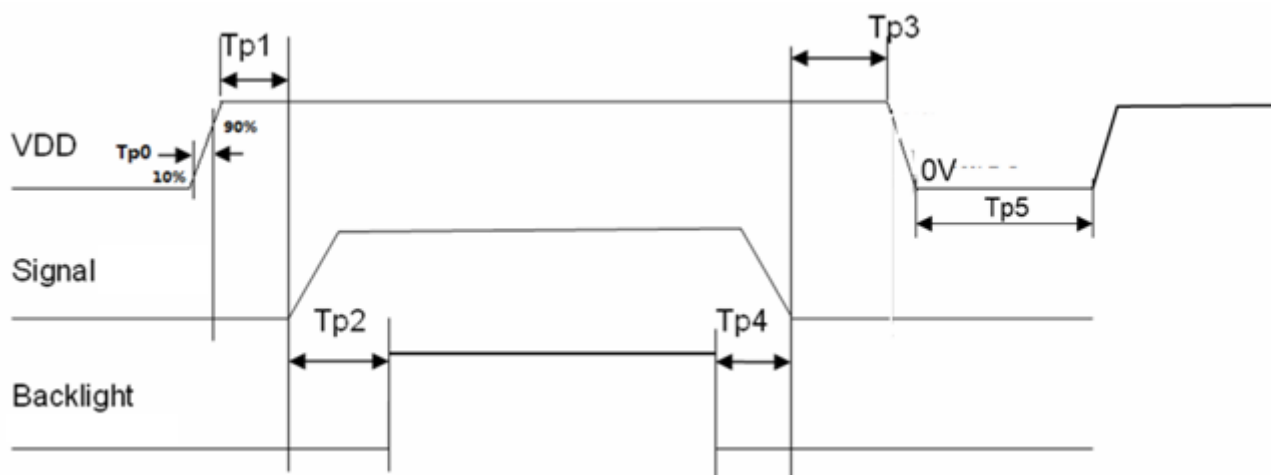
## 10. LVDS data mapping

### 8 bit LVDS data mapping (VESA standard)



## 11. Power ON/OFF Sequence

Item	Symbol	Min	Typ	Max	Unit	Remark
VDD 10% to VDD 90%	Tp0	1	-	5	ms	
VDD to signal starting	Tp1	20	-	50	ms	
Signal starting to backlight on	Tp2	200	-	-	ms	
Signal off to VDD	Tp3	50	-	100	ms	
Backlight off to signal off	Tp4	200	-	-	ms	
To next VDD	Tp5	2	-	-	S	



Note(1) The low level of these signals and analog powers are GND level.

Note(2) All of the power and signals should be kept at GND level before power on. If there are residual voltages on them, the LCD might not work properly.

Note(3) The power on/off sequence is the first version. It will be updated when the design is fixed.

Note(4) LEDA/K is the voltage applied to backlight. Keep it turned off until the display has stabilized.



## 12. Reliability Test

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Humidity Test	60 °C, Humidity 90%, 240 hrs	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note(1) Condensation of water is not permitted on the module.

Note(2) The module should be inspired after 1 hour storage in normal conditions (15~35°C , 45~65%RH).

Note(3) The module shouldn't be tested over one condition, and all the tests are independent.

Note(4) All reliability tests should be done without the protective film.

Definitions of life end point:

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of initial value.

## **13. Use Precautions**

### **13.1 Handling precautions**

- (1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- (2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- (3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- (4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

### **13.2 Installing precautions**

- (1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. 1MΩ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- (2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- (3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- (4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

### **13.3 Storage precautions**

- (1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- (2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- (3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

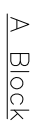
## 13.4 Operating precautions

- (1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- (2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- (3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC drive voltage.
- (4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- (5) Make certain that each signal noise level is within the standard (L level: 0.2V<sub>dd</sub> or less and H level: 0.8V<sub>dd</sub> or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- (6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- (7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
- (8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

## 13.5 Other

- (1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- (2) Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver
- (3) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

## Date: 2025/03/31



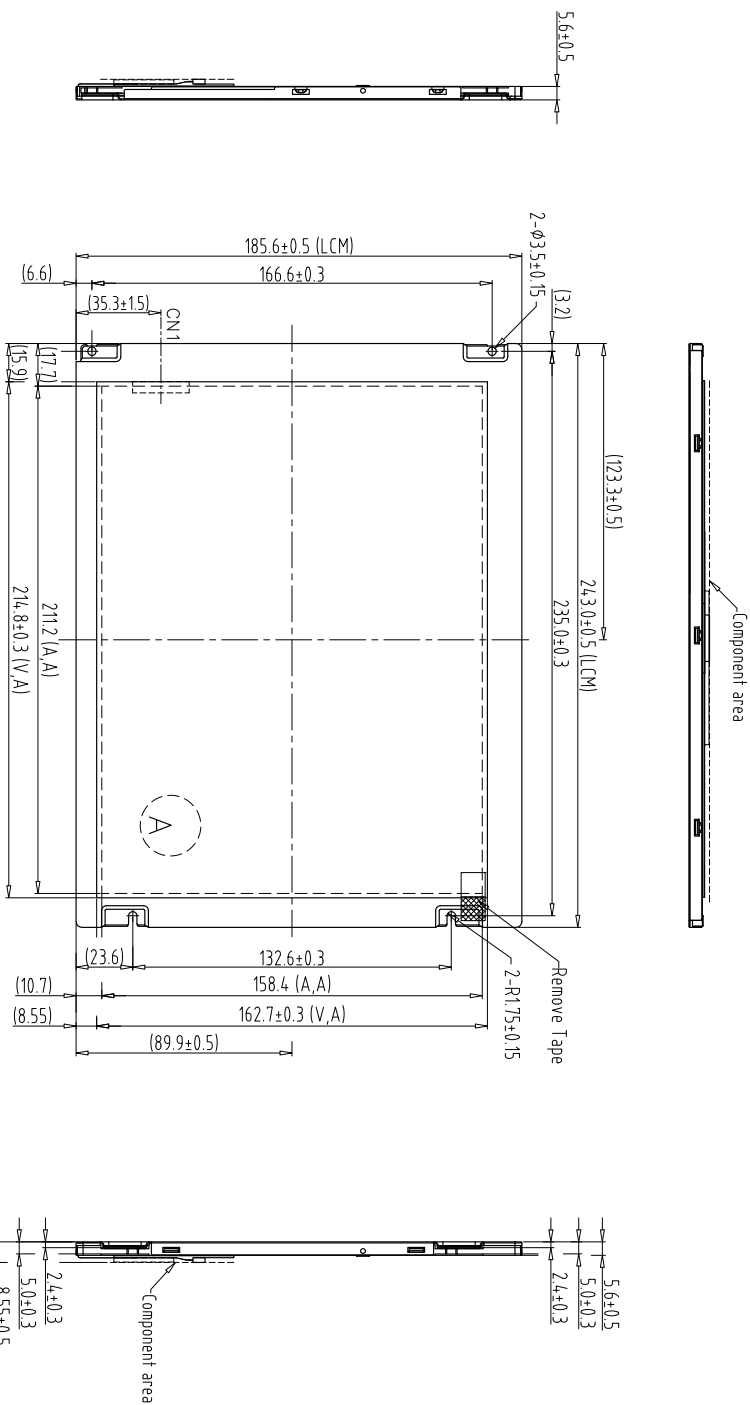
CN4	
1	A Red
2	K White

CN2	
1	VCC
2	GND
3	ON/OFF
4	Dimming
5	NA


1	VDD	11	RxIN2-
2	VDD	12	RxIN2+
3	GND	13	GND
4	DPS	14	RxCCLKIN-
5	RxIN0-	15	RxCCLKIN+
6	RxIN0+	16	GND
7	GND	17	RxIN3-
8	RxIN1-	18	RxIN3+
9	RxIN1+	19	GND
10	GND	20	GND

1. Unless indicated, Tolerance "±0.3"

1. Unless indicated, Tolerance "±0.3"
2. UV Glue For OLB Protection.
3. LCD 800x600 (R.G.B) TFT LCD =>10.4" TFT LCD
4. CN1: STM MSB24013P20HA or Equivalent, Mating Connector: STM P24013P20 or Equivalent
5. CN2: ENTERY 3808K-F05N-03L or Equivalent, Mating Connector: ENTERY H208K-P05N-02B or Equivalent



REV	REVISION RECORD	DATE	NAME
0	NEW RELEASE	08-23-24	SNOW

<div><div> 1000PULS</div><div>晶采光電科技</div></div>													4. CN1: STM MSB24013P20HA or Equivalent, Mating Connector: STM P24013P20 or Equivalent 5. CN2: ENTERY 3808K-F05N-03L or Equivalent, Mating Connector: ENTERY H208K-P05N-02B or Equivalent												
1	800600K2-A0	7			TOLERANCE GRADE(±)	A	B	DIM.	MM	DWN.	SNOW	DATE	08-23-24												
2		8										DATE													
3		9						IE NO.		CHK.		DATE													
4		10																							
5		11						PARTS NO. LCM		APPD.		DATE													
6		12						800600K2M-A0																	
TITLE													800600K2M-A0 (10.4") IPS 8 bits LVDS												
DWG. NO.													*Z40879MA												
SHEET													1 OF 1												



15. Package