Display Solutions tailored for your Application

DATASHEET

AMA-0695A02-DI2511-G010

晶采光電科技股份有限公司
AMPIRE CO., LTD.

/SIMPLEPLUS
Touch Displays

Specifications for LCD module

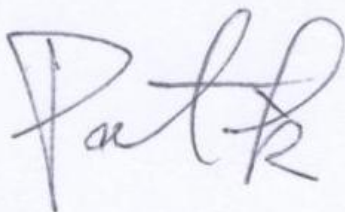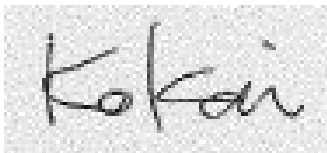| Customer	
Customer part no.	
Ampire part no.	**AMA-695A02-DI2511-G010**
Approved by	
Date	

■ **Preliminary Specification**
☐ **Approved Specification**

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RECORD OF REVISION

Revision Date	Page	Contents	Editor
2018/2/7	-	New release	Emil

1. Features

It's a 7 inches Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This module is composed of a 7" TFT-LCD panel, LED backlight, and Projective capacitive-type touch panel.

(1) Construction: 7" a-Si TFT active matrix, White LED Backlight.
(2) Resolution (pixel): 1024 RGB (H) x 600 (V)
(3) Number of the Colors : 16.7M colors (R , G , B 8 bit digital each)
(4) LCD type : Normally Black
(5) Interface: 8bits LVDS

2. PHYSICAL SPECIFICATIONS

Item	Specifications	unit
LCD size	7 inch (Diagonal)	
Resolution	1024 x (RGB) x 600	dot
Pixel pitch	0.1506(W) x 0.1432(H)	mm
Active area	154.2144(W) x 85.92(H)	mm
Color arrangement	RGB-stripe	

3. ABSOLUTE MAX. RATINGS

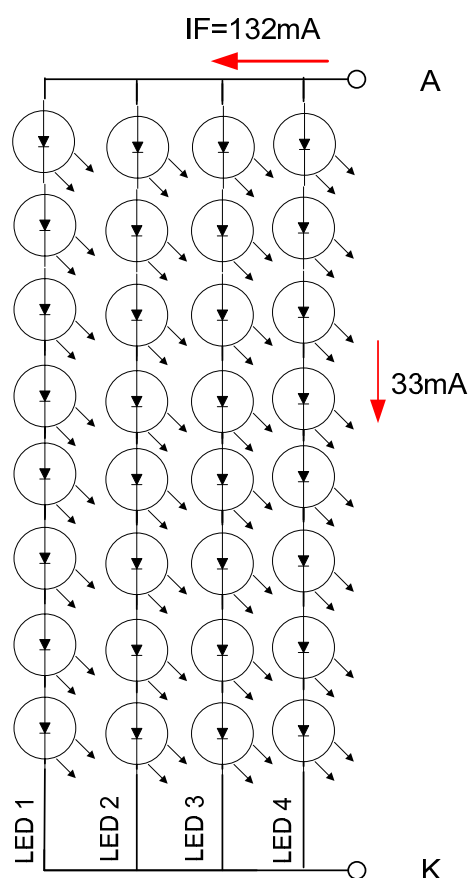| Item	Symbol	Values		Unit	Remark
		MIN	MAX		
Power Voltage	VDD	-0.3	4	V	
LED Driver Power Voltage	VLED	-0.3	19	V	
Operation Temperature	TOP	-20	70	℃	
Storage Temperature	TST	-30	80	℃	

Note(1) The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

4. Backlight Driving Conditions

ITEM	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
LED Driver Power Voltage	VLED	--	12	19	V	
LED Driver Power Current	ILED(VLED=12V)	--	363	--	mA	Ta=25℃
PWM Dimming DC active level	VDIMH	1.5	--	6	V	
	VDIML	--	--	0.6	V	
PWM Dimming Freq.	FDIM	0.2		20	kHz	
BLEN Pin High Voltage	VBLENH	1.4		--	V	
BLEN Pin Low Voltage	VBLENL	--		0.8	V	
LED voltage	VAK	--	26.4	--	V	Note 1
LED current	IF	--	132	--	mA	Note 1
LED life time	--	--	50	--	kHrs	Note 2

Note(1) The LED Supply Voltage is defined by the number of LED at Ta=25℃ and IF=132 mA.

Note(2) The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and IF=132mA. The LED lifetime could be decreased if operating IF is larger than 132mA.

Note(3) When LCM is operated over 40℃ ambient temperature, the I_F should be follow :
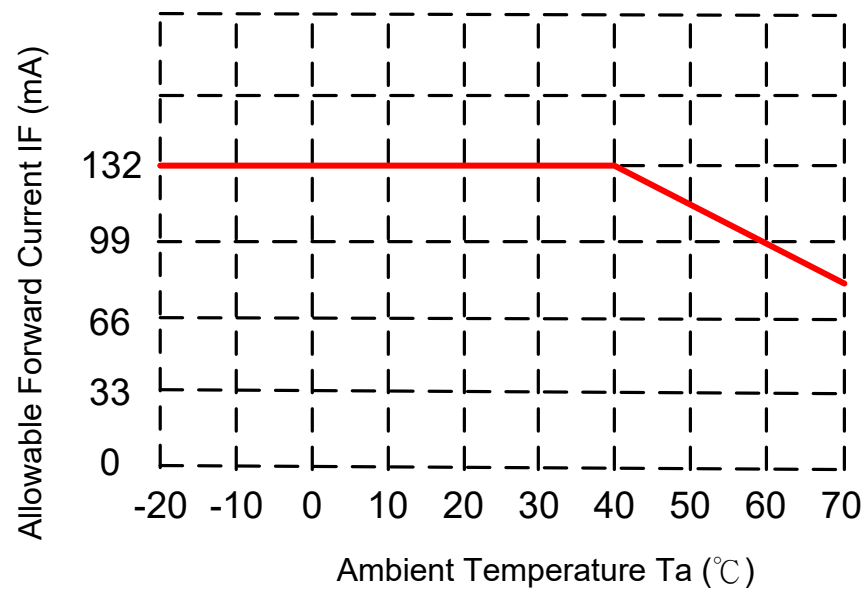
5. Optical Specifications

| Item	Symbol	Condition	Values			Unit	Note
			Min.	Typ.	Max.		
Viewing angle (CR≧10)	θL	Φ = 180° (9 o'clock)	80	85	--	degree	Note1
	θR	Φ = 0° (3 o'clock)	80	85	--		
	θT	Φ = 90° (12 o'clock)	80	85	--		
	θB	Φ = 270° (6 o'clock)	80	85	--		
Response time	TON		--	13	20	msec	Note3
	TOFF		--	15	25	msec	
Contrast ratio	CR		600	800	--	--	Note4
Color chromaticity	WX	Normal θ =Φ=0°	0.26	0.31	0.36	--	Note5 Note6
	WY		0.31	0.36	0.41	--	
	RX		0.56	0.61	0.66		
	RY		0.29	0.34	0.39		
	GX		0.31	0.36	0.41		
	GY		0.52	0.57	0.62		
	BX		0.05	0.10	0.15		
	BY		0.03	0.08	0.13		
Luminance (central point)	L		680	850	--	cd/m^2	Note6
Luminance uniformity	YU		70	75	--	%	Note6

Test Conditions:

VDD = 3.3V, IF = 132 mA (Backlight current), the ambient temperature is 25℃.

The test systems refer to Note 2.

Note (1)   Definition of viewing angle range
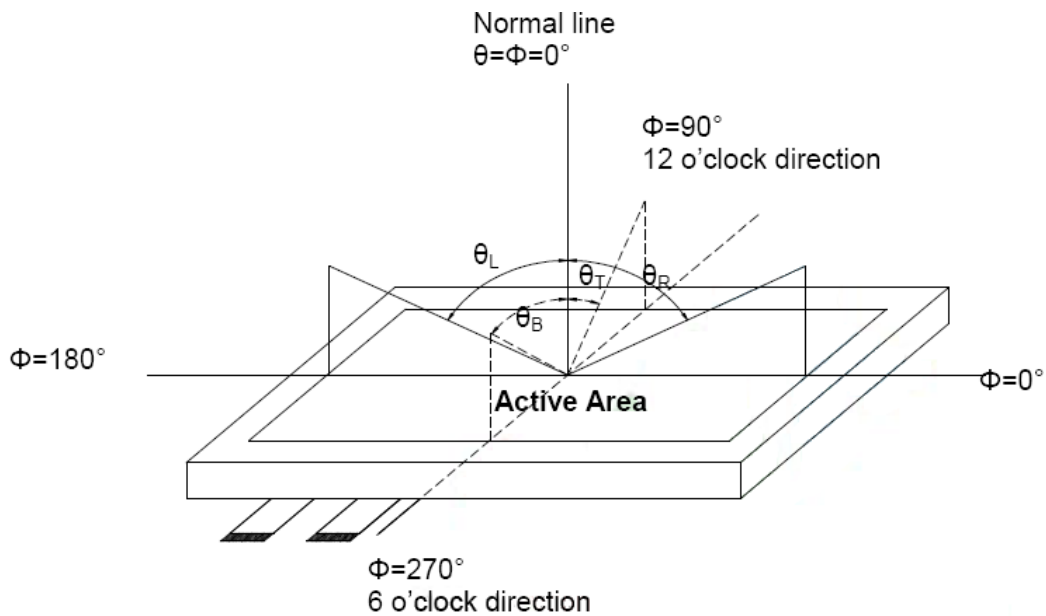
Note (2)   Definition of optical measurement system
The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° / Height: 500mm.)
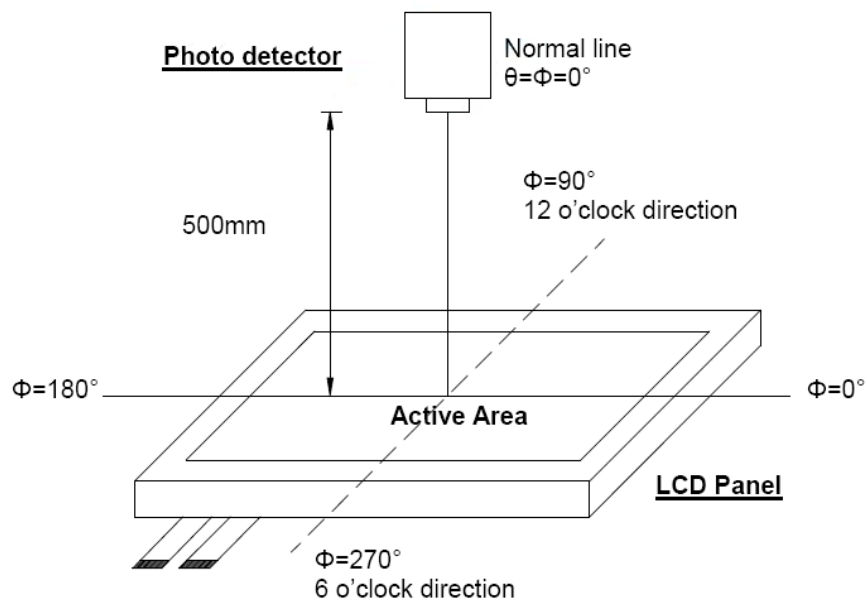
Note (3)   Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (TON) is the time between photo detector output intensity changed from 90% to 10%. And fall time (TOFF) is the time between photo detector output intensity changed from 10% to 90%.
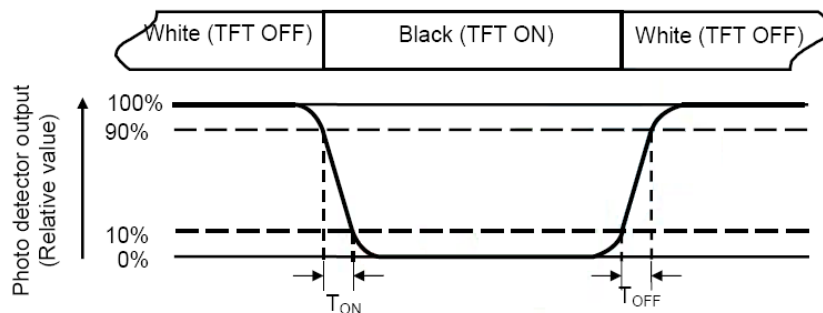
Note (4)   Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note (5)   Definition of color chromaticity (CIE1931)

Color coordinated measured at center point of LCD.

All input terminals LCD panel must be ground when measuring the center area of the panel.

Note (6)   Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to bellow figure).

Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{\text{Bmin}}{\text{Bmax}}$$

L ----- Active area length   W ----- Active area width
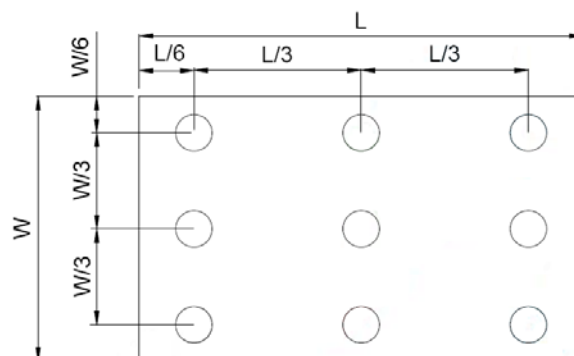
Bmax : The measured maximum luminance of all measurement position.

Bmin : The measured minimum luminance of all measurement position.

Note (7)   Pixel format

6. INTERFACE

CN2:P1.0 20Pin/CP100-S20G-H16 or Equivalent

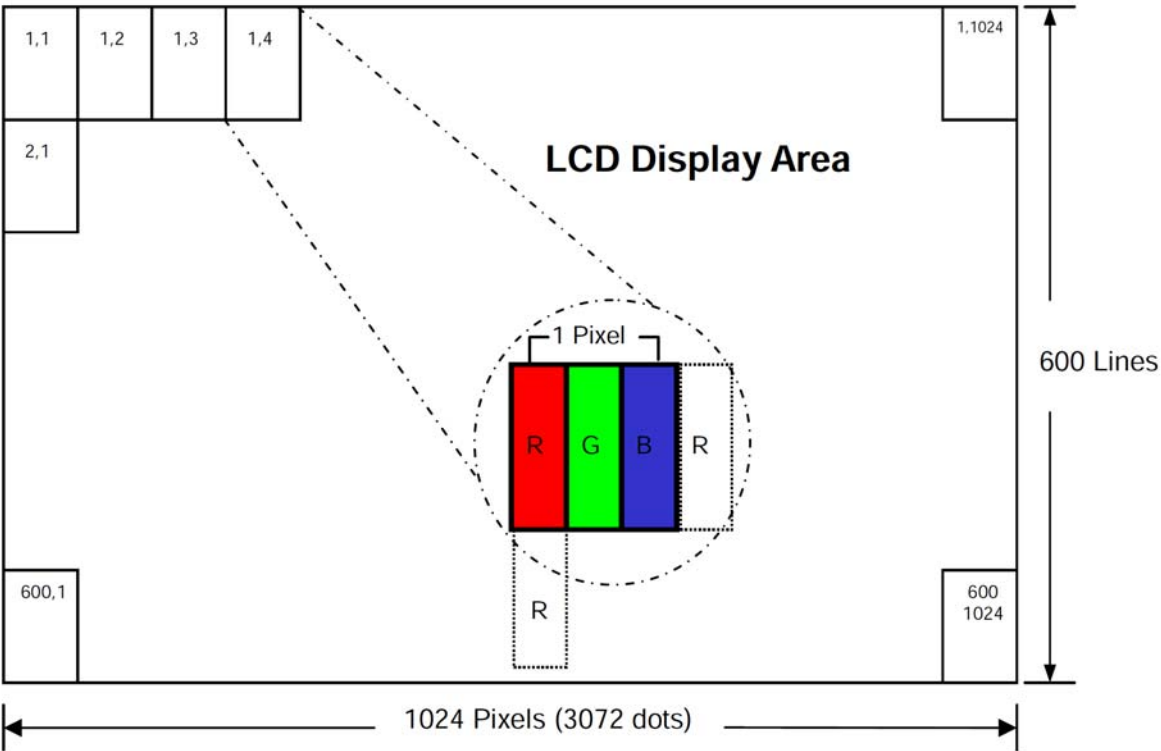| Pin No.	Symbol	Function
1	VDD	POWER SUPPLY
2	VDD	POWER SUPPLY
3	GND	Power Ground
4	GND	Power Ground
5	IN0-	Transmission Data of Pixels
6	IN0+	Transmission Data of Pixels
7	GND	Power Ground
8	IN1-	Transmission Data of Pixels 1
9	IN1+	Transmission Data of Pixels 1
10	GND	Power Ground
11	IN2-	Transmission Data of Pixels 2
12	IN2+	Transmission Data of Pixels 2
13	GND	Power Ground
14	CLK-	Sampling Clock
15	CLK+	Sampling Clock
16	GND	Power Ground
17	IN3-	Transmission Data of Pixels 3
18	IN3+	Transmission Data of Pixels 3
19	GND	Power Ground
20	GND	Power Ground

I: input, O: output, P: power

CN3: ENTERY 3808K-F05N-03L or Equivalent, Mating Connector: ENTERY H208K-P05N-02B or Equivalent

1	VLED	Power supply of LED driving circuit
2	GND	Power Ground
3	BLEN	LED BLU ON/OFF, High: enable, Low: disable
4	DIM	Adjust the LED brightness by PWM
5	NC	No connection

Note (1)   BLU means Backlight Unit

7. ELECTRICAL CHARACTERISTICS

7.1. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Condition
Digital Power Supply Voltage	VDD	3.0	3.3	3.6	V	
Digital Power Supply Current	IDD		55		mA	
Differential Input High Threshold	VTH	--	--	100	mV	VCM=1.2V
Differential Input Low Threshold	VTL	-100	--	--	mV	
Input current	IIN	-10	--	+10	uA	
Differential input Voltage	|VID|	0.2	--	0.6	V	
Common Mode Voltage Offset	VCM	$\dfrac{\|VID\|}{2}$	1.25	$2.4-\dfrac{\|VID\|}{2}$	V	
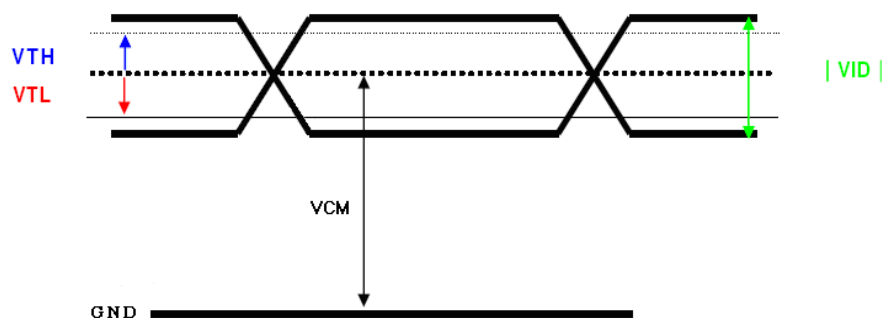
7.2. AC Characteristics
TTL

DE mode					
Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
DCLK frequency	fclk	40.8	51.2	67.2	MHz
Horizontal display area	thd		1024		DCLK
HSD period	th	1114	1344	1400	DCLK
HSD blanking	thb+ thfp	90	320	376	DCLK
Vertical display area	tvd		600		TH
VSD period	tvbp	610	635	800	TH
VSD blanking	tvbp+ tvfp	10	35	200	TH
HV mode					
DCLK frequency	fclk	44.9	51.2	63	MHz
Horizontal display area	thd		1024		DCLK
HSD period	th	1200	1344	1400	DCLK
HSD pulse Width	thpw	1	-	140	DCLK
HSD back porch	thbp		160		DCLK
HSD front porch	thfp	16	160	216	DCLK
Vertical display area	tvd		600		TH
VSD period	tv	624	635	750	TH
VSD pulse Width	tvpw	1	-	20	TH
VSD back porch	tvbp		23		TH
VSD front porch	tvfp	1	12	127	TH

7.3. Power ON/OFF sequence

VDD power and LED on/off sequence are as follows. Interface signals are also shown in the chart. Signal shall be Hi-Z state or low level when VDD is off.
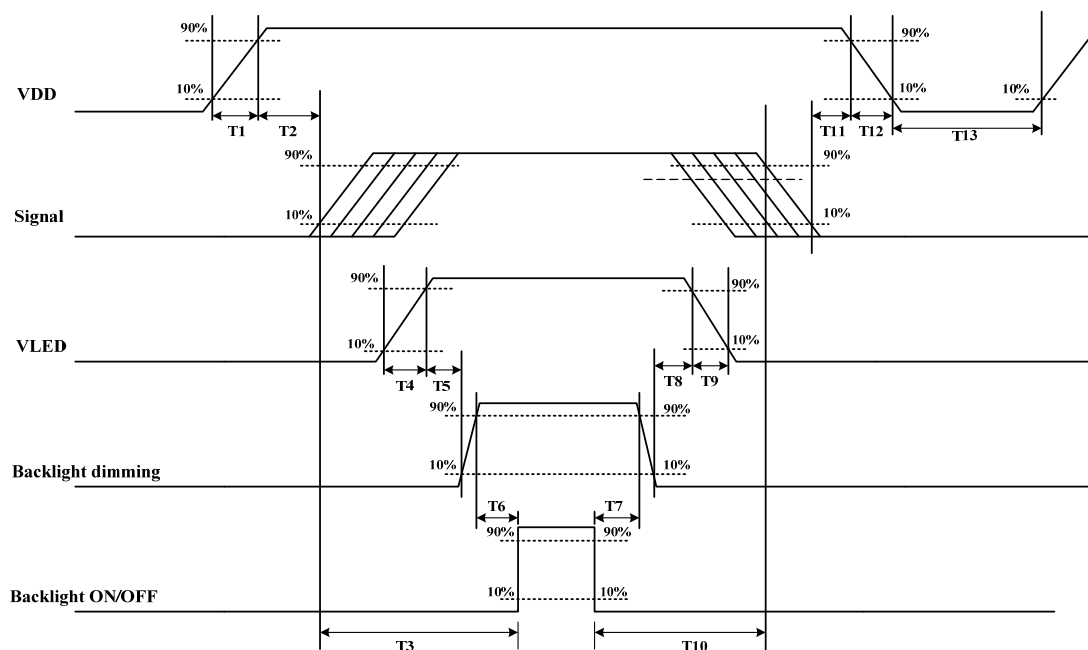
Parameter	Value			Units
	Min.	Typ.	Max.	
T1	0.5	-	10	[ms]
T2	0	40	50	[ms]
T3	200	-	-	[ms]
T4	0.5	-	10	[ms]
T5	10	-	-	[ms]
T6	10	-	-	[ms]
T7	0	-	-	[ms]
T8	10	-	-	[ms]
T9	-	-	10	[ms]
T10	110	-	-	[ms]
T11	0.5	16	50	[ms]
T12	-	-	100	[ms]
T13	1000	-	-	[ms]

7.4. 24-BIT LVDS Input Data Format
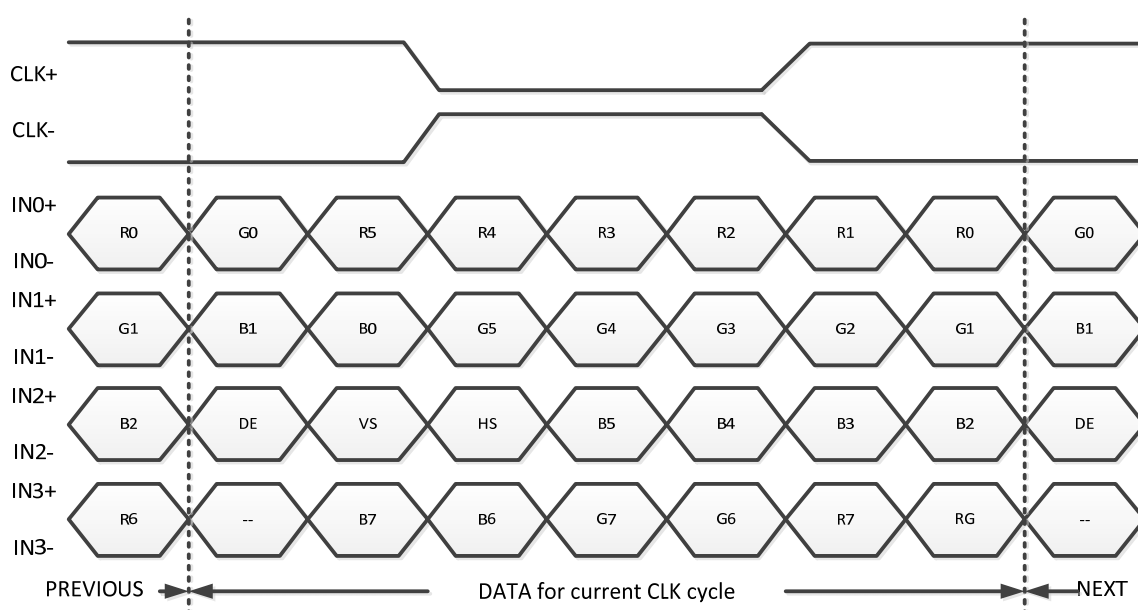
Note: R/G/B data 7: MSB, R/G/B data 0: LSB

Signal Name	Description	Remark
R7 R6 R5 R4 R3 R2 R1 R0	Red Data 7 (MSB) Red Data 6 Red Data 5 Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0 (LSB)	Red-pixel Data Each red pixel's brightness data consists of these 8 bits pixel data.
G7 G6 G5 G4 G3 G2 G1 G0	Green Date 7 (MSB) Green Date 6 Green Date 5 Green Date 4 Green Date 3 Green Date 2 Green Date 1 Green Date 0 (LSB)	Green-pixel Data Each green pixel's brightness data consists of these 8 bits pixel data.
B7 B6 B5 B4 B3 B2 B1 B0	Blue Data 7 (MSB) Blue Data 6 Blue Data 5 Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0 (LSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 8 bits pixel data.
CLK+ CLK-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync Signal	
HS	Horizontal Sync Signal	

8. Projected capacitive-type TOUCH PANEL ELECTRICAL SPECIFICATION

8-1 Basic Characteristic

ITEM	SPECIFICATION
Type	Projective Capacitive Touch Panel
Activation	Multi-finger
X/Y Position Reporting	Absolute Position
Touch Force	No contact pressure required
Calibration	No need for calibration
Report Rate	Approx. 100 points/sec
Interface/Protocol	IIC/V3.X
Control IC	ILI2511
Conductive susceptibility IEC/EN61000-4-6	10Vrms
Radiated Susceptibility IEC/EN61000-4-3	30V/m
Cover Glass	1.1mm chemically strength glass with black border
Bonding method	CG to sensor: optical bonding
	TP module to LCM: tape bonding

8-2 Electrical Characteristic

8-2-1 IIC Interface

Specify the normal operating condition

(GND=0V)

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Power Supply Voltage		VIN	3	3.3	3.6	V	
Signal IIC Interface Logic level	Low	V_{IL}	0	-	0.3*VIN	V	
	High	V_{IH}	0.7*VIN	-	VIN	V	
Power Consumption		I_{VIN}		50		mA	Ref.

8-2-2 Interface

Pin No.	Symbol	Function
1	SCL	IIC Clock
2	SDA	IIC Data
3	VIN	Power supply 3.3V
4	RESET	Reset touch panel controller Active "Low"
5	INT	Interrupt signal Active "Low"
6	GND	Power GND

8-2-3 Power- on Timing Chart (IIC interface)
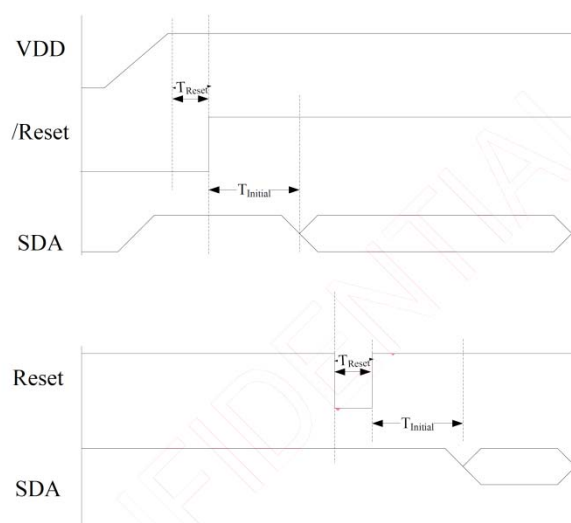
Symbol	Parameter	MIN.	MAX.	Unit
$T_{Initial}$	After powering-on or resetting the device, the device needs I_{nitial} time to configure the system.	-	100	ms
T_{Reset}	/Reset pin low hold time	50	-	µs

8-2-4 IIC AC Waveform
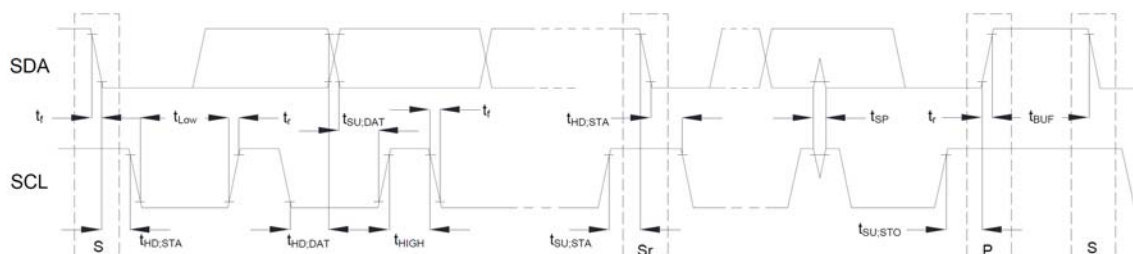
8-2-5 IIC Characteristics

Symbol	Parameter	100KHz			400KHz		
		Min	Max	Unit	Min	Max	Unit
f_{SCL}	SCL clock frequency	0	100	kHz	0	400	KHz
$t_{HD;STA}$	Hold time (repeated) START condition. After this period, the first clock pulse is generated	4.0	–	μs	0.6	–	μs
t_{LOW}	LOW period of the SCL clock	4.7	–	μs	1.3	–	μs
t_{HIGH}	HIGH period of the SCL clock	4.0	–	μs	0.6	–	μs
$t_{SU;STA}$	Set-up time for a repeated START condition	4.7	–	μs	0.6	–	μs
$t_{HD;DAT}$	Data hold time	0	3.45	μs	0	0.9	μs
$t_{SU;DAT}$	Data set-up time	250	–	ns	100	–	ns
t_r	Rise time of both SDA and SCL signals	–	1000	ns	–	300	ns
t_f	Fall time of both SDA and SCL signals	–	300	ns	–	300	ns
$t_{SU;STO}$	Set-up time for STOP condition	4.0	–	μs	0.6	–	μs
t_{BUF}	Bus free time between a STOP and START condition	4.7	–	μs	1.3	–	μs

8-2-6 Format Protocol
Protocol V3.X Command List

CMD Code	Name	Set /Get	Note	b7	b6	b5	b4	b3	b2	b1	b0
0x10	Touch Information	Get		0: No touch 1: Last Report at ID 0 to ID 5 (include release status) 2: Last Report at ID 6 to ID 9 (include release status)							
			ID0	1: Touch Down, 0: Touch Off	0	X_High direction coordinate					
				X_Low direction coordinate							
				0	0	Y_High direction coordinate					
				Y_Low direction coordinate							
				Touch Pressure							
			ID1	1: Touch Down, 0: Touch Off	0	X_High direction coordinate					
				X_Low direction coordinate							
				0	0	Y_High direction coordinate					
				Y_Low direction coordinate							
				Touch Pressure							

			ID2	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID3	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID4	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		

			ID5	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
0x14	Touch Information 2	Get	ID6	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID7	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		

			ID8	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID9	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
0x20				The maximum X coordinate (bit 7:0)		
				The maximum X coordinate (bit 15:8)		
				The maximum Y coordinate (bit 7:0)		
				The maximum Y coordinate (bit 15:8)		
				The channel numbers of X direction		
				The channel numbers of Y direction		
				The maximum report points		

CMD Code	Name	Set / Get		Description
				The channel numbers of TouchKey / Scrolling Bar
				For Touch Key Application (Maximum supports 31 Touch Key) 　Byte 8 : The Touch Key number (<32) 　Byte 9: 0xFF
0x30	Enter Sleep Mode	Set		--
0x40	Firmware Version	Get		Chip ID Code
				Major firmware version
				Minor firmware version
				Release firmware version
				For Customer Firmware Version
				For Customer Firmware Version
				For Customer Firmware Version
				For Customer Firmware Version
0x42		Get		Major protocol version : 0x03
				Minor protocol version : XX
				Release protocol version : XX

Protocol V3.X Data Format

CMD Code	Name	Set / Get	Note	b7	b6	b5	b4	b3	b2	b1	b0
0x10	Touch Information	Get	Packet Number	0: No touch 1: Last Report at ID 0 to ID 5 (include release status) 2: Last Report at ID 6 to ID 9 (include release status)							
			ID0	1: Touch Down, 0: Touch Off	0	X_High direction coordinate					
				X_Low direction coordinate							
				0	0	Y_High direction coordinate					
				Y_Low direction coordinate							
				Touch Pressure							

			ID1	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID2	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID3	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID4	1: Touch Down, 0: Touch Off	0	X_High direction coordinate

				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		
			ID5	1: Touch Down, 0: Touch Off	0	X_High direction coordinate
				X_Low direction coordinate		
				0	0	Y_High direction coordinate
				Y_Low direction coordinate		
				Touch Pressure		

8-2-7 Interrupt Pin (INT) Control

When a finger touches on the sensor surface, the INT pin will be pull low. TP controller supports two different type control method.

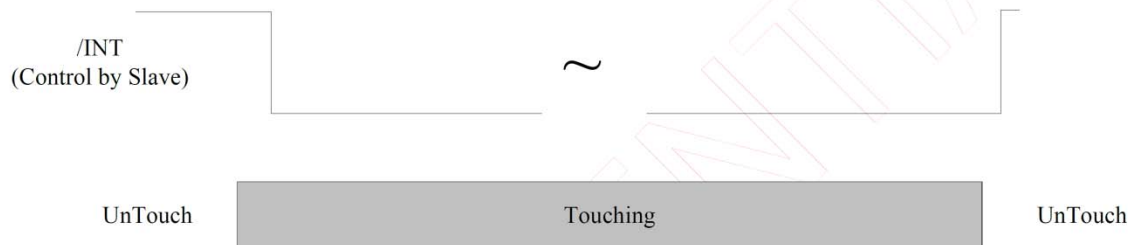Method 1(Polling): The $\overline{INT}$ will continue to be low until the finger leaves the sensor surface.

Fig 9: Method 1: $\overline{INT}$ Pin Control Diagram (Finger Touch)

Method 2(Interrupt): The $\overline{INT}$ will continue to be pull low until host read 0x10 command.
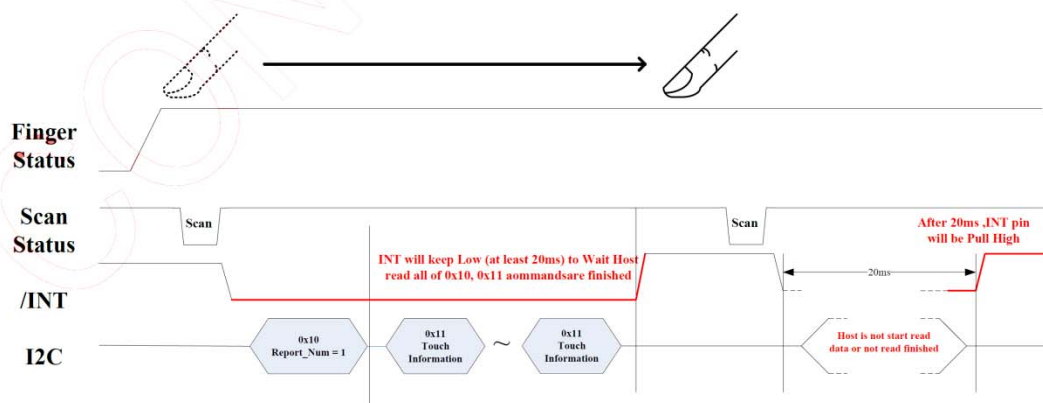

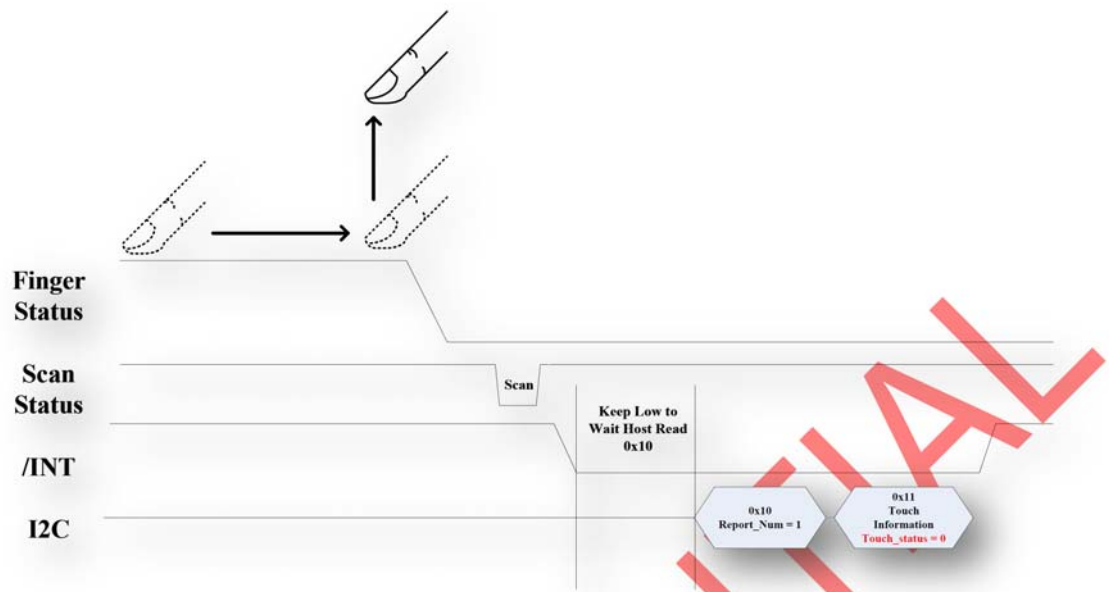Fig 10: Method 2: $\overline{INT}$ Pin Control Diagram (Finger Touch)

Fig 11:   Method 2: $\overline{\text{INT}}$ Pin Control Diagram (Finger Release)

8-2-8 Device Address

MSB							LSB
1	0	0	0	0	0	1	0/1
Device Address							R/W

7-bit Device Address: 0x41

8-bit Device Read Address:0x83

8-bit Device Write Address:0x82

8-2-9 Data Transfer

Data is transferred over the IIC bus with 8-bit address and 8-bit data.

1	7	1	1	8	1	1
S	Slave Address	Wr	A	Data Byte	A	P

S	Start Condition
Sr	Repeated Start Condition
Rd	Read (bit value of 1)
Wr	Write (bit value of 0)
A/NA	Acknowledge (this bit position may be '0' for an ACK or '1' for a NACK)
P	Stop Condition
	Master-to-Slave
	Slave-to-Master
---	Continue

I2C Write timing
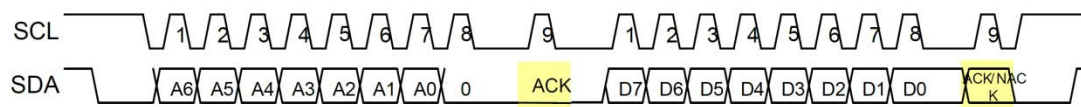
= > slave to master

I2C Read timing
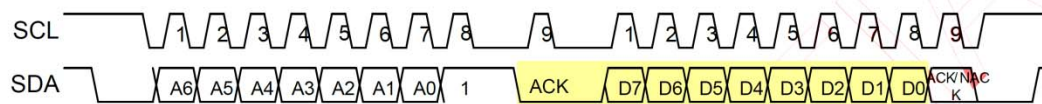
= > slave to master

Byte Write

S	Slave Address	Wr	A	Command Code	A	Data Byte	A	P

Byte Write

Byte Read

c

S	Slave Address	Wr	A	Command Code	A	Sr	Slave Address	Rd	A	Data Byte	A	P
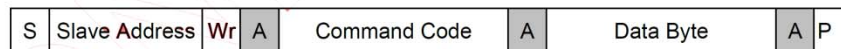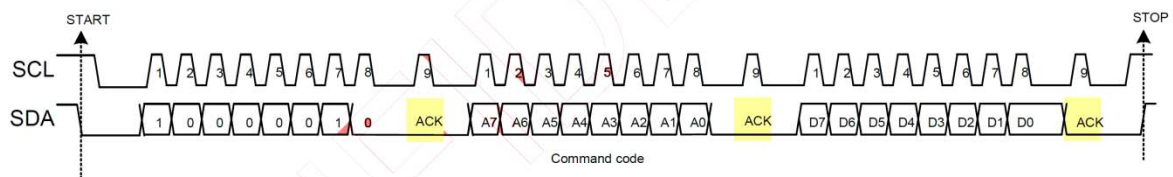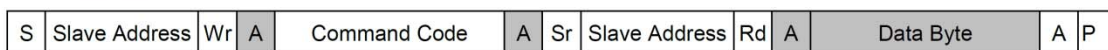
Byte Read

Multi-Byte Write
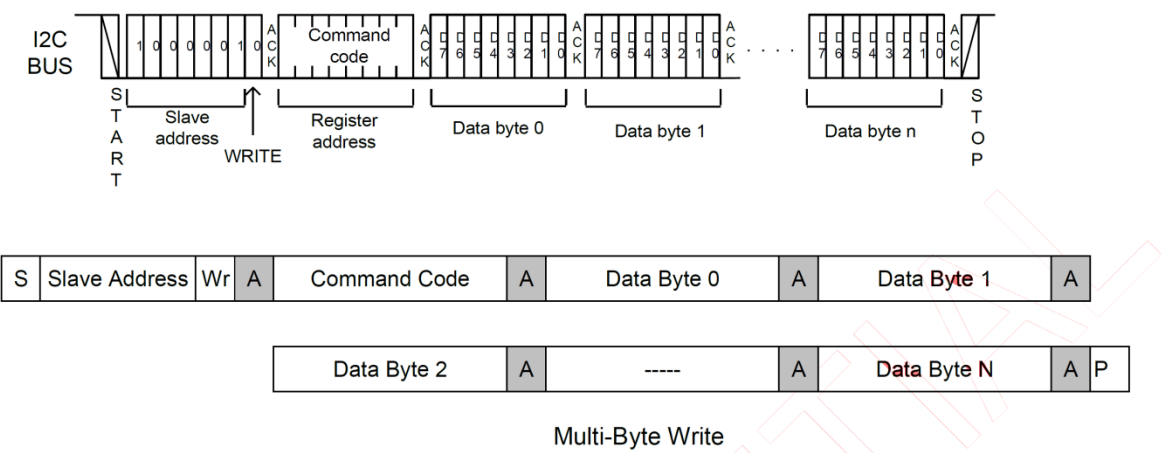
Multi-Byte Read
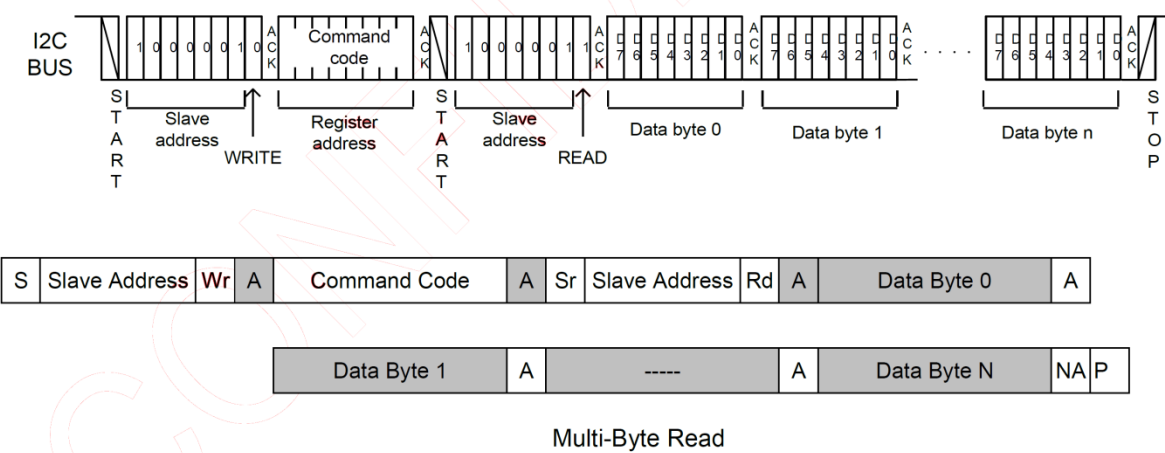
9. RELIABILITY TEST CONDITIONS

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , Dry t=240 hrs	
Low Temperature Operation	-20±3°C , Dry t=240 hrs	
High Temperature Storage	80±3°C , Dry t=240 hrs	1,2
Low Temperature Storage	-30±3°C , Dry t=240 hrs	1,2
Storage at High Temperature and Humidity	60°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min.) ~ 25°C(5min.) ~ 70°C (30min.) 100 cycles	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note 1 : Condensation of water is not permitted on the module.

Note 2 : The module should be inspected after 1 hour storage in normal conditions
(15-35°C , 45-65%RH).

Note 3 : The module shouldn't be tested more than one condition, and all the test
conditions are independent.

Note 4 : All the reliability tests should be done without protective film on the module.

10. GENERAL PRECAUTION

10-1 Safety

Liquid crystal is poisonous. Do not put it your month. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

10-2 Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.

2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.

3. To avoid contamination on the display surface, do not touch the module surface with bare hands.

4. Keep a space so that the LCD panels do not touch other components.

5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.

6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.

7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

10-3 Static Electricity

1. Be sure to ground module before turning on power or operation module.

2. Do not apply voltage which exceeds the absolute maximum rating value.

10-4 Storage

1. Store the module in a dark room where must keep at +25±10℃ and 65%RH or less.

2. Do not store the module in surroundings containing organic solvent or corrosive gas.

3. Store the module in an anti-electrostatic container or bag.

10-5 Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.

2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

10-6 Mechanism (if the LCM using air bonding)

(1) Please mount LCD module by using mounting holes arranged in four corners tightly.

(2) The square adhesive tape which is between the touch panel and display can't provide well supporting in the long term and high ambient temperature condition. Whether upright or horizontal position the support holder which is in the back side of the display is needed. Do not let the display floating.
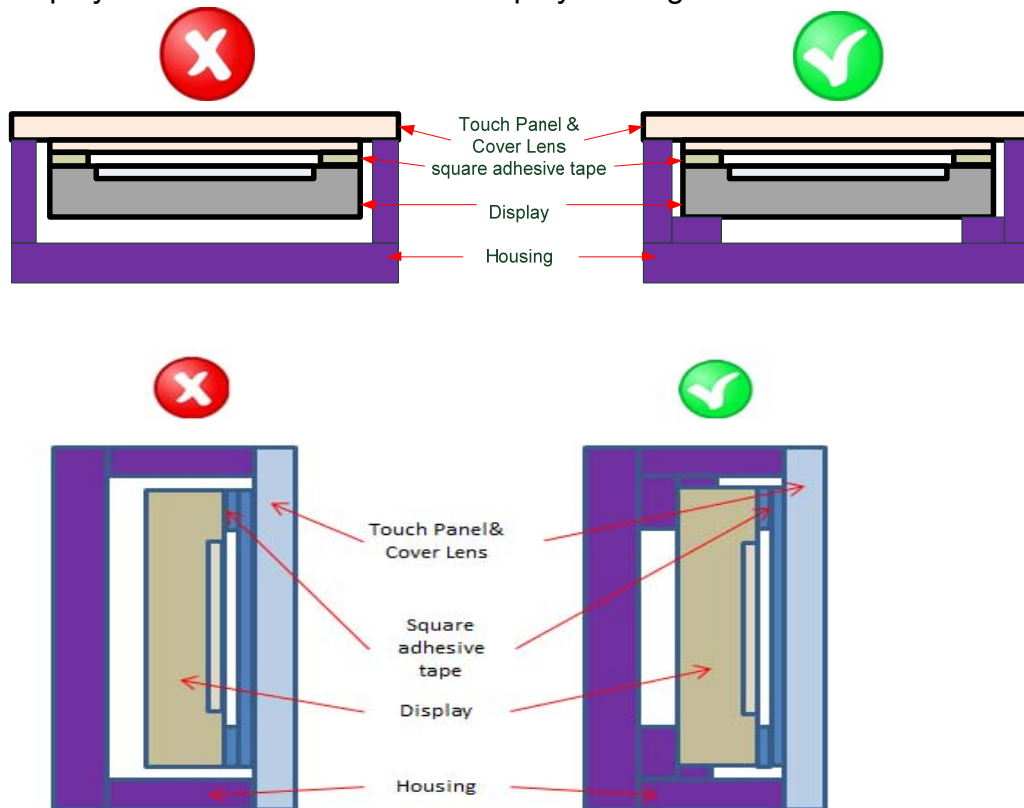
(3) TP needs to work in environment with stable stray capacitance. In order to minimize the variation in stray capacitance, all conductive mechanical parts must not be floating. Intermittent floating any conductive part around the touch sensor may cause significant stray capacitance change and abnormal touch function. It is recommended to keep all conductive parts having same electrical potential as the GND of the touch controller module.
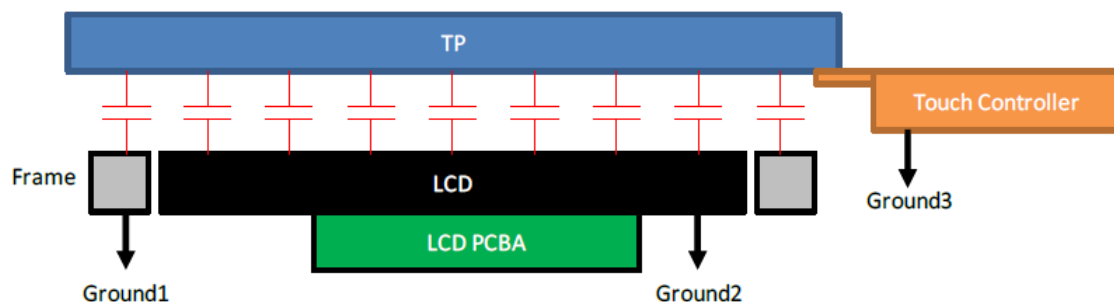
GND1, GND2 and GND3 should be connected together to have the same ground

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10-7 Others

1. AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.

2. Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver

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11. OUTLINE DIMENSION

CN3

1	VLED
2	GND
3	BLEN
4	DIM
5	NC

LVDS 8bit

1	VDD	11	IN2-
2	GND	12	IN2+
3	GND	13	GND
4	GND	14	CLK-
5	IN0-	15	CLK+
6	IN0+	16	GND
7	GND	17	IN3-
8	IN1-	18	IN3+
9	IN1+	19	GND
10	GND	20	GND

CN6 (I2C)

1	SCL
2	SDA
3	VDD
4	RESET
5	INT
6	GND

A Block

R G B

Note:
1. Unless indicated, Tolerance "±0.3"
2. UV Glue For OLB Protection.
3. CN2:P1.0 20Pin/CP100-S20G-H16 or Equivalent
4. CN1:P0.5 40Pin/CS050-40ZST-H12-U or Equivalent
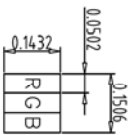5. CN3: ENTERY 3808K-F05N-03L or Equivalent, Mating Connector: ENTERY H208K-P05N-02B or Equivalent
6. LCD 1024X3(R,G,B)x600=> 7.0" Digital TFT LCD

7	P0.5 6PIN/089H06-000000-G2-R	12
8	P-CAP 10246O0D4-T(17910246J2A)	11
2	10246O0D-A6 LCM	
3	DITO(FOG/I2C/IL2511)	9
4	Cover 10246O0D4-T/153102461O	10
5	(173.0x17.0x1.1mmT)	11
6	OCA一貼/ロ字樣二貼	12

	TOLERANCE GRADE(±)	A	B	DIM.
				MM
				IE NO.
				PART'S NO.LCM
				AMA-695A02-DI2511-G010

DWN. EMILY DATE 02-05-18'
CHK. DATE
APPD. DATE

Component area T=1.5mm

CN6 6 1

117.0±0.3 (Cover Lens)
100.0±0.2 (T/P)
88.24±0.2 (A,A for T/P)
86.432±0.2 (V,A for Cover Lens)
(6.235)
(14.38)
(15.284)
(9.18)
(8.78)
(5.54)
(5.95)
(7.5)
(9.3928)
4-R3.5

154.64±0.2 (V,A for Cover Lens)
155.44±0.2 (A,A for T/P)
164.9±0.2 (T/P)
173.0±0.3 (Cover Lens)

158.0±0.3 (V,A for Bezel)
164.9±0.5 (LCM)
154.214 (A,A for LCD)
(86.5±0.3)

32.0±0.3
(13.24±1.0)
(15.54)
(13.65)
(5.425)
(77.07±1.0)

85.92 (A,A for LCD)
89.7±0.3 (V,A for Bezel)
100.0±0.5 (LCM)
(58.5±0.3)

Remove tape

A

Cover Lens T=1.1

Stiffener L=4.0±0.5
Contact L=3.0±0.3
0.3±0.05

9.65±0.5
12.125±0.5
10.925±0.5
8.45±0.5
5.7±0.3

0.0502
0.1432
0.1506

A Block

0.0502
0.1432
0.1506
R G B

CN3

1	VLED
2	GND
3	BLEN
4	DIM
5	NC

LVDS 8bit

1	VDD	11	IN2−
2	VDD	12	IN2+
3	GND	13	GND
4	GND	14	CLK−
5	IN0−	15	CLK+
6	IN0+	16	GND
7	GND	17	IN3−
8	IN1−	18	IN3+
9	IN1+	19	GND
10	GND	20	GND

CN6 (I2C)

1	SCL
2	SDA
3	VDD
4	RESET
5	INT
6	GND

Note:
1. Unless indicated, Tolerance "±0.3"
2. UV Glue For OLB Protection.
3. CN2:P1.0 20Pin/CP100-S20G-H16 or Equivalent
4. CN1:P0.5 40Pin/CS050-40ZST-H12-U or Equivalent
5. CN3: ENTERY 3808K-F05N-03L or Equivalent, Mating Connector: ENTERY H208K-P05N-02B or Equivalent
6. LCD 1024X3(R.G.B)x600=> 7.0" Digital TFT LCD

Back View
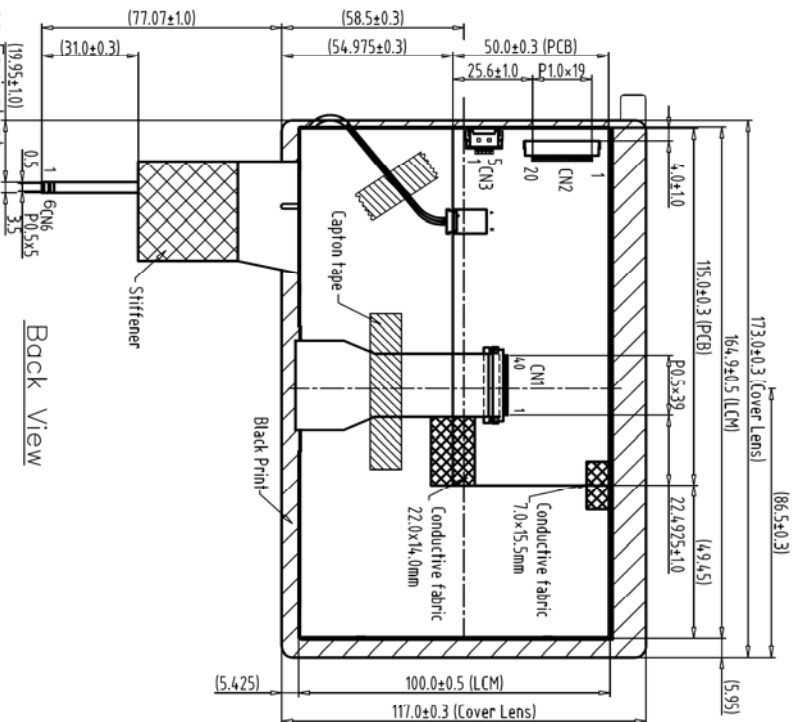
Stiffener
Kapton tape
Black Print
Conductive fabric 22.0x14.0mm
Conductive fabric 7.0x15.5mm

(77.07±1.0) (58.5±0.3)
(31.0±0.3) (54.975±0.3) 50.0±0.3 (PCB)
25.6±1.0 P1.0x19
[9.95±1.0]
0.5
3.5 P0.5x5 6N6
CN3
5 20 CN2 1
4.0±1.0
115.0±0.3 (PCB)
164.9±0.5 (LCM)
173.0±0.3 (Cover Lens)
(86.5±0.3)
CN1 40 1
P0.5x39
22.4925±1.0
(4.945)
(5.95)
(5.425) 100.0±0.5 (LCM)
117.0±0.3 (Cover Lens)

No.		DIM.					
1	1024600D-A6 LCM	7					
2	P-CAP 1024600D4-T(179102463ZA)	8					
3	DITO(F0G/12C/IL2511)	9	P0.5 6PIN/089H06-000000-G2-R				
4	Cover 1024600D4-T/1531024610	10					
5	(173.0x17.0x1.1mmT)	11					
6	OCA—貼/口字模二貼	12					

TOLERANCE GRADE(±)

DIM.	A	B	IE NO.	MM	DWN.	CHK.	APPD.	DATE

PARTS NO. LCM—1
AMA-695A02-DI2511-G010