

SPECIFICATION

PRODUCT NO. : TCXD035IBLON-43

VERSION : Ver 1.0

ISSUED DATE : 2022-4-12

This module uses ROHS material

FOR CUSTOMER: _____

☐: APPROVAL FOR SPECIFICATION

☒: APPROVAL FOR SAMPLE

DATE	APPROVED BY

Xinli Optoelectronics :

Presented by	Reviewed by	Organized by

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2. General Description and Features

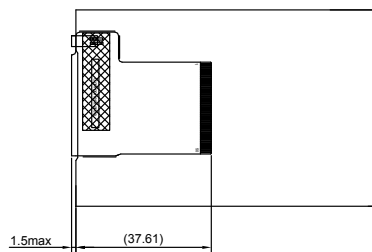
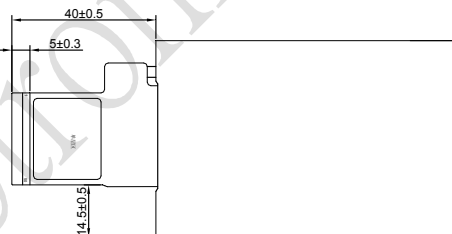
The 10.1 inch Module named TCXD035IBLON-43 is a-Si TFT-LCD module, which is the type of transmissive. It is consisted of TFT-LCD Panel, Driver IC, FPC,CG and Back-Light unit. Features of this product are listed in the following table.

NO	Item	Contents	Unit
(1)	Module Outline	54.5(H)*83(V)*2.1(T)	mm
(2)	LCD Active area	48.96(H)*73.44(V)	mm
(3)	Dot Number	320RGB*480	/
(4)	Sub pixel size	0.153(H)*0.153(V)	mm
(5)	LCD type	TFT Transmissive, Normally Black	/
(6)	Display Color	262K	/
(7)	Viewing direction	ALL	O'clock
(8)	Drive IC	ST7796	
(9)	Power Supply	2.8 (TYP)	V
(10)	Backlight Type	8-chip LED	/
(11)	Interface type	SPI /MCU/RGB /MIPI Interface	/
(12)	Module weight	TDB	g

NOTES:

- 1.DISPLAY TYPE: 3.5" TFT-LCD, TRANSMISSIVE, NORMALLY BLACK;
DRIVER IC: ILI9486;
- 2.OPERATING TEMP.: -20°C~+70°C;
- 3.STORAGE TEMP.: -30°C~+80°C;
- 4.VIEWING DIRECTION: FREE;
- 5.BACKLIGHT: 8 Chip-White LEDs, If=40mA, Vf=10.8V~13.6V;
- 6.SURFACE BRIGHTNESS: 500cd/m² (TYP)
- 7.GENERAL TOLERANCE: ±0.2mm;

***IMPORTANT DIMENSION, " REVISION RECORD (1) (REFERENCE DIMENSION,
COMPLY TO ROHS.



FPC弯折示意图
FPC展开出货

P/N	ASSIGNMENT
1	A
2	K
3	GND
4	DEN
5	DOP
6	GND
7	CLKN
8	GND
9	GND
10	INDI
11	INI
12	IMI
13	VST
14	RST
15	HS
16	DCLK
17	DE
18	GND
19-36	D1H1Z-B00
37	GND
38	SBO
39	SBN
40	SW
41	WX
42	TX
43	CS
44	NC
45	NC
46	NC
47	NC
48	NC
49	VDD
50	VDD

B/L Circuit



IM0	IM1	IM2	
0	0	0	18-bit DB(17:0)
1	0	0	9-bit DB(8:0)
0	1	0	16-bit DB(15:0)
1	1	0	8-bit DB(7:0)
0	1	1	MIP/DONP/CLKMP
1	1	1	SP1/SDA/SDO

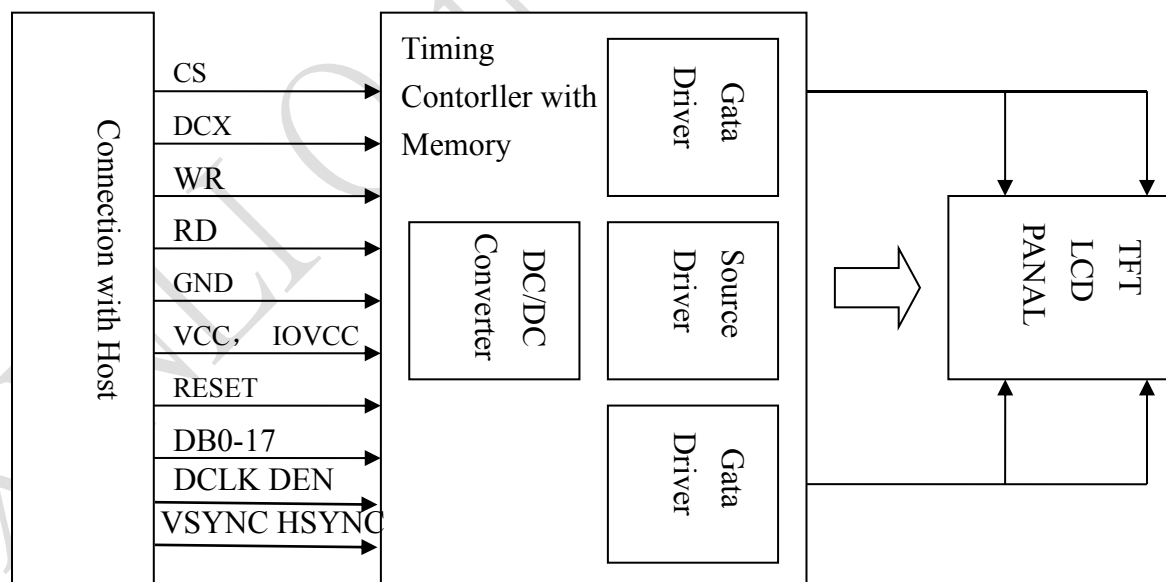
 浙江新力光电科技有限公司 Zhejiang Xinli Optoelectronics Technology Co., Ltd.				MODULE NO.: TCX0035B1LON+41	
TITLE			LCM OUTLINE DIMENSION	PREPARED	DATE
D/N			TCX0035B1LON+41	PAGE 1 / 1	CHECK
DATE			VER: A	REV: 0	APPROVED
F			G	H	I
					J

4. Interface Pin Connection

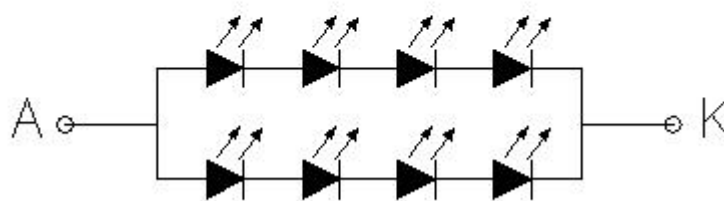
NO	Symbol	Level	Description																												
1	LEDA	P	LED power																												
2	LEDK	P	LED string Ground																												
3	GND	P	Power ground																												
4	D0N	I/O	Negative polarity of low voltage differential data signal																												
5	D0P	I/O	Positive polarity of low voltage differential data signal																												
6	GND	P	Power ground																												
7	CLK_N	I	Negative polarity of low voltage differential data signal																												
8	CLK_P	I	Positive polarity of low voltage differential data signal																												
9	GND	P	Power ground																												
10	IM0	I	<table><tr><td>IM0</td><td>IM1</td><td>IM2</td><td></td></tr><tr><td>0</td><td>0</td><td>0</td><td>18-bit DB[17:0]</td></tr><tr><td>1</td><td>0</td><td>0</td><td>9-bit DB[8:0]</td></tr><tr><td>0</td><td>1</td><td>0</td><td>16-bit DB[15:0]</td></tr><tr><td>1</td><td>1</td><td>0</td><td>8-bit DB[7:0]</td></tr><tr><td>0</td><td>1</td><td>1</td><td>MIPI D0N/P;CLKN/P</td></tr><tr><td>1</td><td>1</td><td>1</td><td>SPI4;SDA/SDO</td></tr></table>	IM0	IM1	IM2		0	0	0	18-bit DB[17:0]	1	0	0	9-bit DB[8:0]	0	1	0	16-bit DB[15:0]	1	1	0	8-bit DB[7:0]	0	1	1	MIPI D0N/P;CLKN/P	1	1	1	SPI4;SDA/SDO
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1	1	1	SPI4;SDA/SDO																												
11	IM1	I																													
12	IM2	I																													
13	RESET	I	Reset pin																												
14	VS	I	Vertical synchronizing input signal for RGB interface operation.																												
15	HS	I	Horizontal synchronizing input signal for RGB interface																												
16	DCLK	I	Dot clock signal for RGB interface operation.																												
17	DE	I	Data enable signal for RGB interface operation																												
18	GND	P	Power ground																												
19-36	DB17-DB0		Data Pin																												
37	GND	P	Power ground																												
38	SDO	O	SPI interface output pin.																												
39	SDA	I	SPI interface input/output pin.																												
40	RD	O	Read enable																												
41	WR	I	Write enable																												

42	RS	I	Data/Command Selection pin
43	CS	I	Chip selection pin. Low-active.
44	NC	-	No Connection
45	NC	-	No Connection
46	NC	-	No Connection
47	NC	-	No Connection
48	NC	-	No Connection
49	VCC	P	Power supply
50	VCC	P	Power supply

5. Block Diagram



B/L Circuit



6. Maximum Rating

Item	Symbol	Rating	Unit
Operating temperature	Top	-20 to 70	°C
Storage temperature	Tst	-30 to 80	°C
Power supply for LCD	VCC	-0.3 ~ 4.6	V
	IOVCC	-0.3 ~ 4.6	V

NOTE:

If the module was used these absolute maximum ratings as above, it may be damaged permanently. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability. VCC>GND must be maintained.

7. Electrical Characteristics

Item		Symbol	Condition	Min.	Typ.	Max.	Unit
Power supply for LCD		VCC	-	2.5	2.8	3.3	V
		IOVCC		1.65	1.8	3.3	V
Logic input signal Voltage	H level	V _{IH}		0.7*IOVCC	-	IOVCC	V
	L level	V _{IL}		GND	-	0.3*IOVCC	V
Logic output signal Voltage	H level	V _{OH}		0.8*IOVCC	-	IOVCC	V
	L level	V _{OL}		GND	-	0.2*IOVCC	V

8. Backlight Characteristics

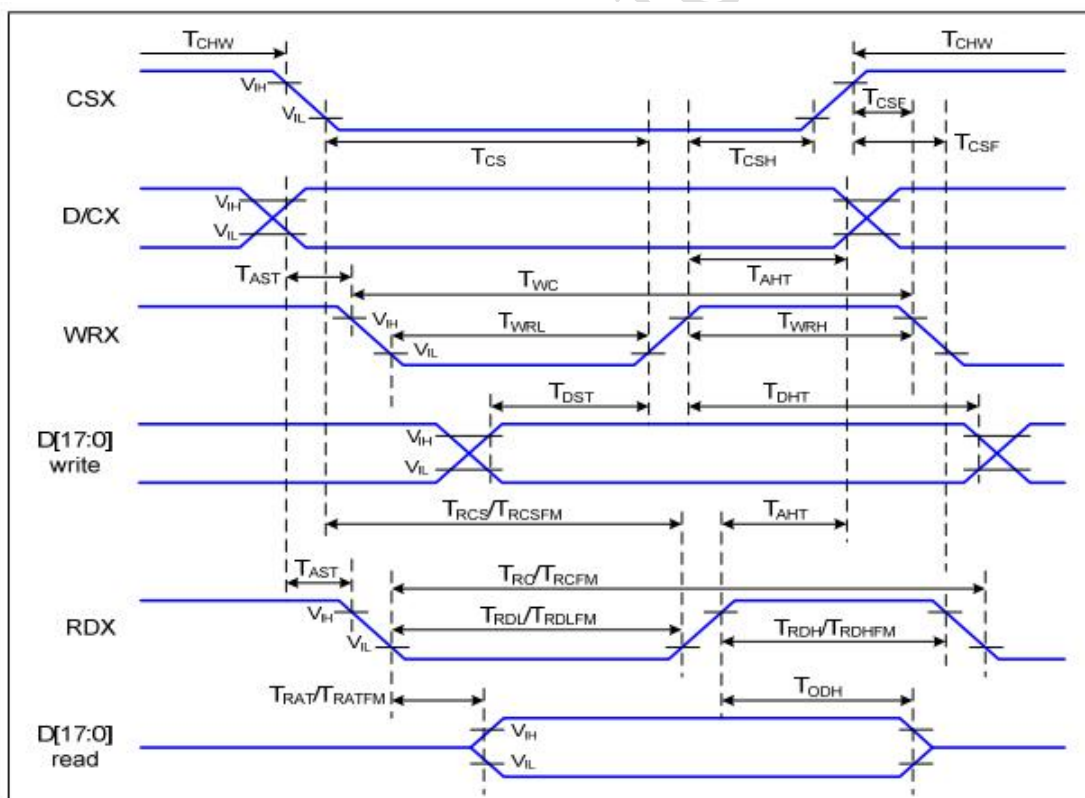
Item	syb	Min	Typ	Max	Unit	Condition
Voltage	Vf	10.8	12.8	13.6	V	IF=40mA
Number of LED	-	8			pcs	-
Power Consumption	PWF	-	320	-	mW	-
Connection mode	-	4P2S			-	-
LED life-span	-	-	(20000)	-	Hrs	-

9. Timing Characteristics

9.1. AC Characteristics

8080 Series MCU Parallel Interface Characteristics: 18/16/9/8-bit

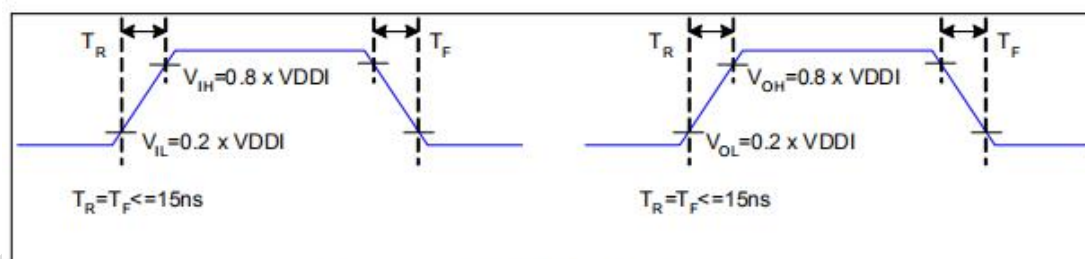
Bus



VDDI=1.8V, VDDA=2.8V, AGND=DGND=0V, Ta=25 °C

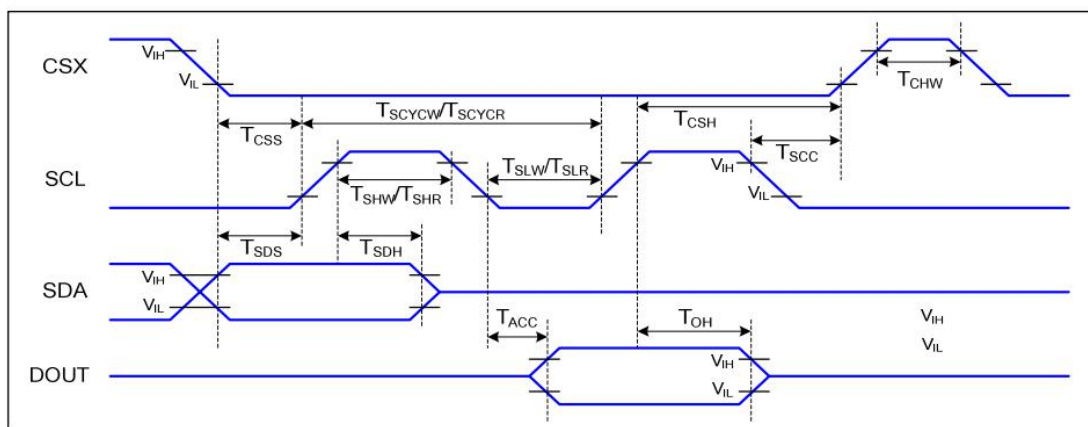
Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	T _{AST}	Address setup time	0		ns	-
	T _{AHT}	Address hold time (Write/Read)	10		ns	
CSX	T _{CHW}	Chip select "H" pulse width	0		ns	-
	T _{CS}	Chip select setup time (Write)	15		ns	
	T _{RCS}	Chip select setup time (Read ID)	45		ns	
	T _{RCSFM}	Chip select setup time (Read FM)	355		ns	
	T _{CSF}	Chip select wait time (Write/Read)	10		ns	
	T _{CSH}	Chip select hold time	10		ns	
WRX	T _{WC}	Write cycle	66		ns	
	T _{WRH}	Control pulse "H" duration	15		ns	

	T _{WRL}	Control pulse "L" duration	15		ns	
RDX (ID)	T _{RC}	Read cycle (ID)	160		ns	When read ID data
	T _{RDH}	Control pulse "H" duration (ID)	90		ns	
	T _{RDL}	Control pulse "L" duration (ID)	45		ns	
RDX (FM)	T _{RCFM}	Read cycle (FM)	450		ns	When read from frame memory
	T _{RDHFM}	Control pulse "H" duration (FM)	90		ns	
	T _{RDLFM}	Control pulse "L" duration (FM)	355		ns	
D[17:0]	T _{DST}	Data setup time	10		ns	For CL=30pF
	T _{DHT}	Data hold time	10		ns	
	T _{RAT}	Read access time (ID)	-	40	ns	
	T _{RATFM}	Read access time (FM)	-	340	ns	
	T _{ODH}	Output disable time	20	80	ns	



Rising and Falling Timing for I/O Signal

9.2. 3-SPI Serial Data Transfer Interface Characteristics:



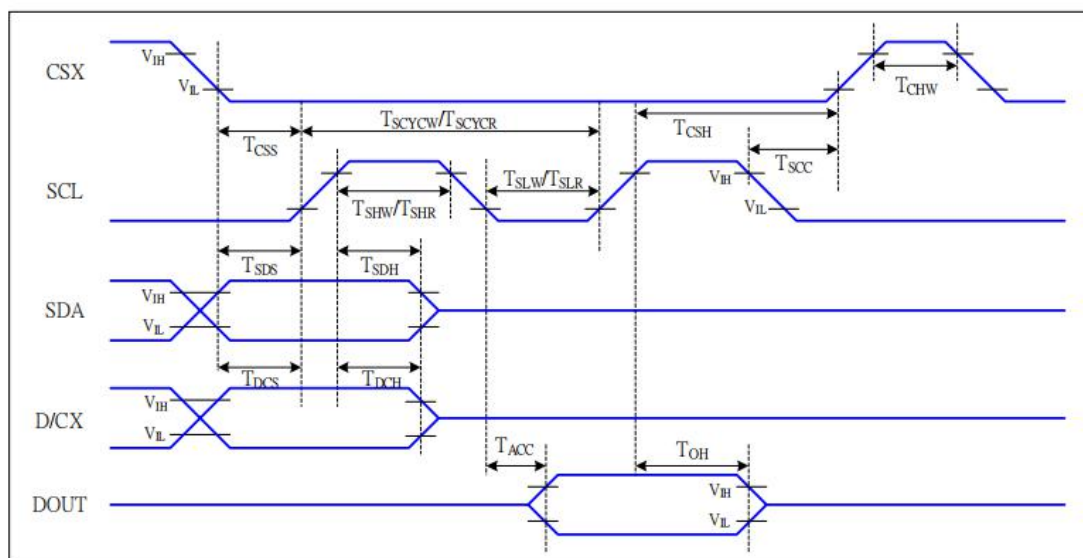
3-SPI Interface Timing Characteristics

VDDI=1.8V, VDDA=2.8V, AGND=DGND=0V, Ta=25 °C

Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	65		ns	
	T_{CHW}	Chip select "H" pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	66		ns	
	T_{SHW}	SCL "H" pulse width (Write)	15		ns	
	T_{SLW}	SCL "L" pulse width (Write)	15		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	
	T_{SHR}	SCL "H" pulse width (Read)	60		ns	
	T_{SLR}	SCL "L" pulse width (Read)	60		ns	
SDA (DIN)	T_{SDS}	Data setup time	10		ns	
	T_{SDH}	Data hold time	10		ns	
DOUT	T_{ACC}	Access time	10	50	ns	For maximum CL=30pF
	T_{OH}	Output disable time	15	50	ns	For minimum CL=8pF

3-SPI Interface Characteristics

9.3. 4-SPI Serial Data Transfer Interface Characteristics:

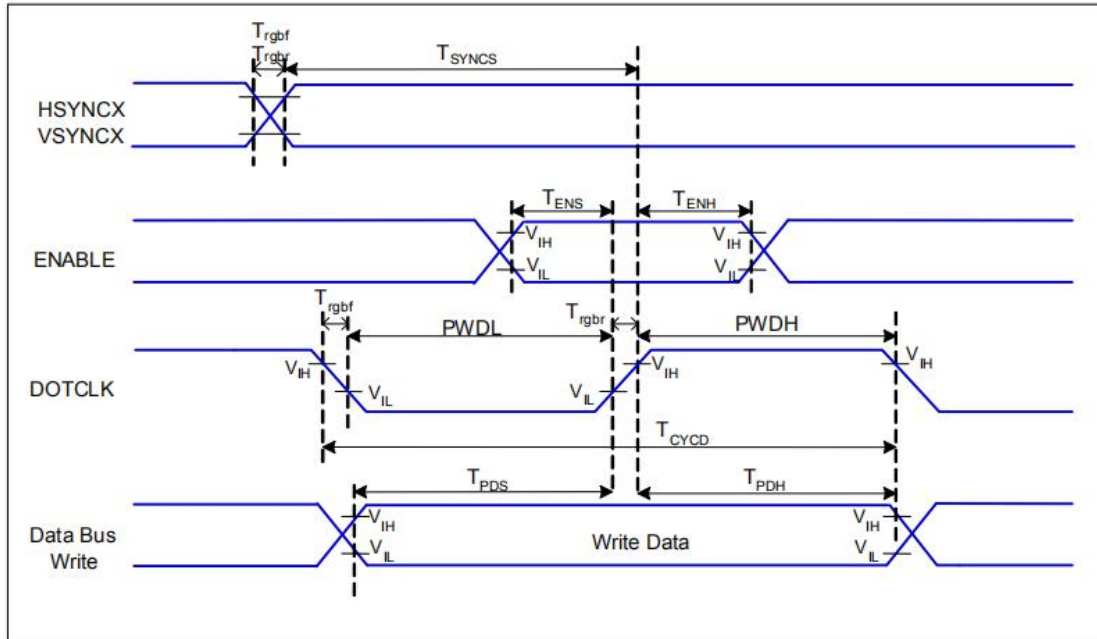


4-SPI Interface Timing Characteristics

VDDI=1.8V, VDDA=2.8V, AGND=DGND=0V, Ta=25 °C

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	T_{CSS}	Chip select setup time (write)	15		ns	
	T_{CSH}	Chip select hold time (write)	15		ns	
	T_{CSS}	Chip select setup time (read)	60		ns	
	T_{SCC}	Chip select hold time (read)	65		ns	
	T_{CHW}	Chip select "H" pulse width	40		ns	
SCL	T_{SCYCW}	Serial clock cycle (Write)	66		ns	-write command & data ram
	T_{SHW}	SCL "H" pulse width (Write)	15		ns	
	T_{SLW}	SCL "L" pulse width (Write)	15		ns	
	T_{SCYCR}	Serial clock cycle (Read)	150		ns	-read command & data ram
	T_{SHR}	SCL "H" pulse width (Read)	60		ns	
	T_{SLR}	SCL "L" pulse width (Read)	60		ns	
D/CX	T_{DCS}	D/CX setup time	10		ns	
	T_{DCH}	D/CX hold time	10		ns	
SDA (DIN)	T_{SDS}	Data setup time	10		ns	
	T_{SDH}	Data hold time	10		ns	
DOUT	T_{ACC}	Access time	10	50	ns	For maximum CL=30pF
	T_{OH}	Output disable time	15	50	ns	For minimum CL=8pF

9.4. RGB Interface Characteristics:



$V_{\text{DDI}}=1.8\text{V}, V_{\text{DDA}}=2.8\text{V}, \text{AGND}=\text{DGND}=0\text{V}, T_a=25^\circ\text{C}$

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
HSYNC, VSYNC	T_{SYNCX}	VSYNC, HSYNC Setup Time	15	-	ns	
ENABLE	T_{ENS}	Enable Setup Time	15	-	ns	
	T_{ENH}	Enable Hold Time	15	-	ns	
DOTCLK	PWDH	DOTCLK High-level Pulse Width	30	-	ns	
	PWDL	DOTCLK Low-level Pulse Width	30	-	ns	
	T_{CYCD}	DOTCLK Cycle Time	66	-	ns	
	$T_{\text{rghr}}, T_{\text{rgbf}}$	DOTCLK Rise/Fall time	-	15	ns	
DB	T_{PDS}	PD Data Setup Time	15	-	ns	
	T_{PDH}	PD Data Hold Time	15	-	ns	

RGB Interface Timing Characteristics

10. Application Circuit

Please consult our technical department for detail information.

11. QUALITY LEVEL

Please consult our technical department for detail information.

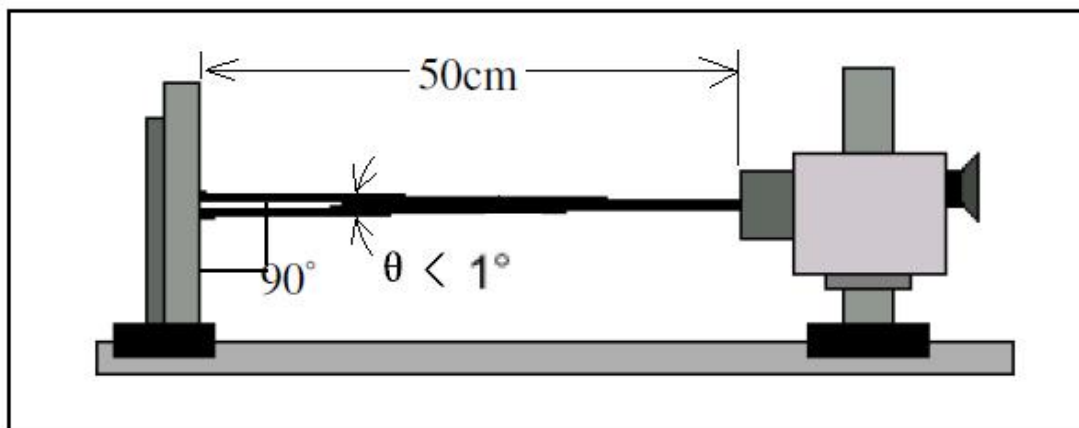
12. Electro-Optical Characteristics

Item		Symbol	Condition	Min	Typ	Max	Unit	Note
Response time		Tr+Tf	$\theta = 0$	-	30	40	ms	5
Uniformity (Five point)		δ WHITE	$=0$ $T_a=25^\circ\text{C}$	70	75	-	%	1, 2, 3 4, 5
Contrast ratio		Cr		-	1000	-	-	3, 5
LCM LUMINANCE		Lv		-	500	-	-	5
Viewing angle range		θ	$\varnothing = 90^\circ$	75	80	-	deg	5
			$\varnothing = 270^\circ$	75	80	-	deg	
			$\varnothing = 0^\circ$	75	80	-	deg	
			$\varnothing = 180^\circ$	75	80	-	deg	
Color filter chromaticity (x, y)	White	X	$\theta = \phi = 0^\circ$	-0.04	TBD	+0.04		5
		Y			TBD			
	Red	X			TBD			
		Y			TBD			
	Green	X			TBD			
		Y			TBD			
	Blue	X			TBD			
		Y			TBD			

Note 1: Ambient temperature= $25^\circ\text{C} \pm 2^\circ\text{C}$

Note 2: To be measured in the dark room with backlight unit.

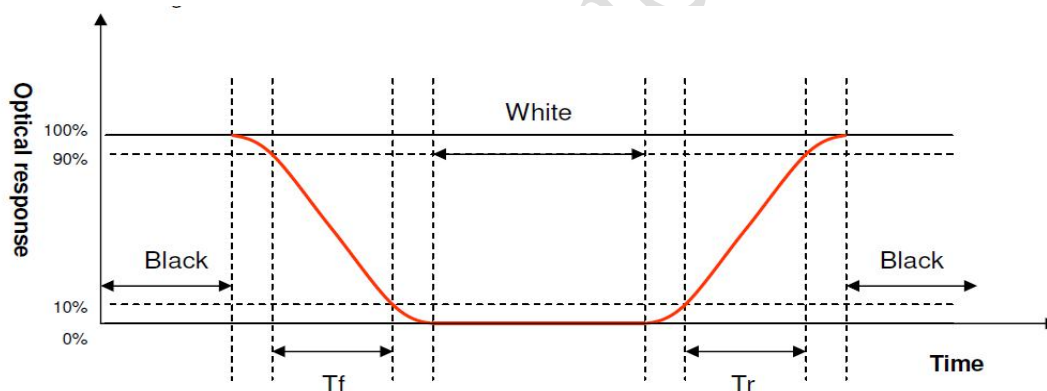
Note 3: To be measured at the center area of panel with a viewing cone of 1 by Topcon luminance meter BM-7A, after 10 minutes operation (module).



Note 4: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “black” to “white” (rising time) and from “white” to “black” (falling time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.

Refer to figure as below.



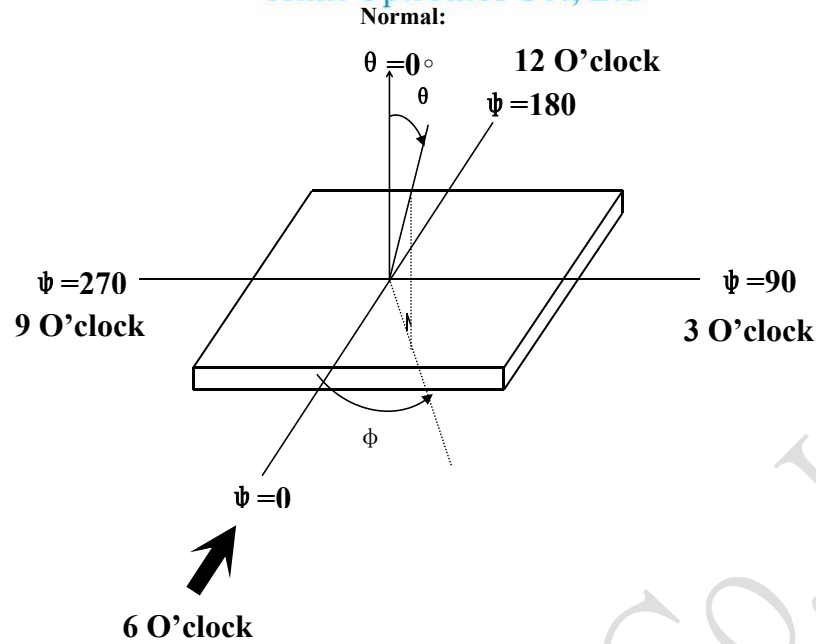
Note 5. Definition of contrast ratio:

Contrast ratio is calculated with the following formula:

$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector output when LCD is at "Black" state}}$$

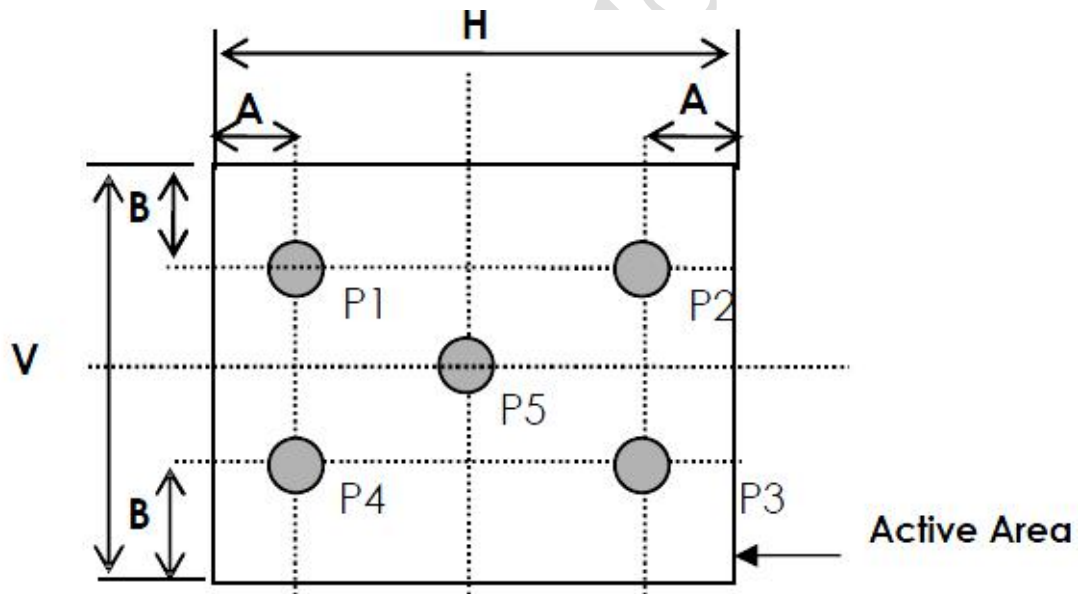
Note 6. Definition of viewing angle

Viewing angle is the angle at which the contrast ratio is greater than 10 for TFT module. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface.



Note 7. Surface luminance is the LCD surface from the surface with all pixels displaying white. Refer to figure as below.

Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity



A : 5 mm B : 5 mm H, V : Active Area

Light spot size $\varnothing=7\text{mm}$, 500mm distance from the LCD surface to detector lens
 measurement instrument is TOPCON's luminance meter BM-7A

Uniformity definition= [min of 5point/max of 5points]x100%

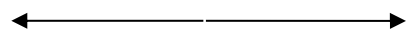
L_v = Average Surface Luminance with all white pixels (P₁, P₂, P₃, P₄, P₅)

13. Reliability Test

This standard reliability test is done only for the first lot of MP products.

Customer and supplier must hold a discussion if other reliability test is requested by customer.

If there is any abnormality, the normal temperature and humidity recovery 2 hours after the display is OK, the result is judged as OK.

NO.	Test Item	Description	Test Condition
1	High temperature storage	Endurance test applying the high storage temperature for a long time	80℃,240 H
2	Low temperature storage	Endurance test applying the low storage temperature for a long time	-30℃,240H
3	High temperature operation	Endurance test applying the electric stress under high temperature for a long time	70℃,240H
4	Low temperature operation	Endurance test applying the electric stress under low temperature for a long time	-20℃,240H
5	High temperature /humidity operation	Endurance test applying the high temperature and high humidity storage for a long time	60℃,90% RH, 240H
6	Temperature Cycle (Non operation)	Endurance test applying the low and high temperature cycle $-30^{\circ}\text{C} \leftarrow \rightarrow 25^{\circ}\text{C} \leftarrow \rightarrow 80^{\circ}\text{C}$ $30\text{min} \leftarrow \rightarrow 5\text{min} \leftarrow \rightarrow 30\text{min}$  one cycle	-30℃/80℃, 100 cycles

14. Quality Assurance

TBD

15. Precautions for Operation and Storage

1. Precautions for Operation

- (1) Since LCD panel made of glass, in order to prevent from glass broken or color tone change, please do not apply any mechanical shock or impact or excessive force to it when installing the LCD module.
- (2) If LCD panel is broken and liquid crystal substance leaks out and contact your skin or clothes, please immediately wash it off by using soap and water.
- (3) The polarizer on the LCD surface is soft and easily scratched. Please be careful when handling.
- (4) If LCD surface becomes contaminated, please wipe it off gently by using moisten soft cloth with normal hexane, do not use acetone, ketone, ethanol, alcohol or water. If there is saliva or water on the LCD surface, please wipe it off immediately.
- (5) When handling LCD module, please be sure that the body and the tools are properly grounded. And do not touch I/F pins with bare hands or contaminate I/F pins.
- (6) Do not attempt to disassemble or process the LCD module.
- (7) LCD module should be used under recommended operating conditions shown in chapter 6 and 7.
- (8) Response time will be extremely slower at lower temperature than at specified temperature and LCD will show different color when at higher temperature. The phenomenon will disappear when returning to specified condition.
- (9) Foggy dew, moisture condensation or water droplets deposited on surface and contact terminals will cause polarizer stain or damage, the deteriorated display quality and electrochemical reaction then leads to the shorter life time and permanent damage to the module probably. Please pay attention to the environmental temperature and humidity.

1. Precautions for Storage

- (1) Please store LCD module in a dark place, avoid exposure to sunlight, the light of fluorescent lamp or any ultraviolet ray.
- (2) Keep the environment temperature at between 10°C and 35 °C and at normal humidity. Avoid high temperature, high humidity or temperature

below 0°C.

(3) That keeps the LCD modules stored in the container shipped from supplier before using them is recommended.

(4) Do not leave any article on the LCD module surface for an extended period of time.

2. Warranty period

Warrants for a period of 12 Months from the shipping date when stored or used under normal condition.

16. Package Specification

TBD