Japan Display Inc.

FOR MESSRS : _____ DATE : Jan. 03rd ,2023

CUSTOMER'S ACCEPTANCE SPECIFICATIONS

TX38D203VM0BAA

Contents

No.	ITEM	SHEET No.	PAGE
1	COVER	7B64PS 2701-TX38D203VM0BAA-2	1-1/1
2	RECORD OF REVISION	7B64PS 2702-TX38D203VM0BAA-2	2-1/1
3	GENERAL DATA	7B64PS 2703-TX38D203VM0BAA-2	3-1/1
4	ABSOLUTE MAXIMUM RATINGS	7B64PS 2704-TX38D203VM0BAA-2	4-1/1
5	ELECTRICAL CHARACTERISTICS	7B64PS 2705-TX38D203VM0BAA-2	5-1/2~2/2
6	OPTICAL CHARACTERISTICS	7B64PS 2706-TX38D203VM0BAA-2	6-1/2~2/2
7	BLOCK DIAGRAM	7B64PS 2707-TX38D203VM0BAA-2	7-1/1
8	RELIABILITY TESTS	7B64PS 2708-TX38D203VM0BAA-2	8-1/1
9	LCD INTERFACE	7B64PS 2709-TX38D203VM0BAA-2	9-1/11~11/11
10	OUTLINE DIMENSIONS	7B64PS 2710-TX38D203VM0BAA-2	10-1/2~2/2
11	APPEARANCE STANDARD	7B64PS 2711-TX38D203VM0BAA-2	11-1/3~3/3
12	PRECAUTIONS	7B64PS 2712-TX38D203VM0BAA-2	12-1/2~2/2
13	DESIGNATION OF LOT MARK	7B64PS 2713-TX38D203VM0BAA-2	13-1/1

ACCEPTED BY: _____

PROPOSED BY: Oblack Tsai

2. RECORD OF REVISION

<table>
<tr><th>DATE</th><th>SHEET No.</th><th>SUMMARY</th></tr>
<tr><td rowspan="4">Jan.3,'23</td><td>7B64PS 2707-
TX38D203VM2BAA-2
Page-7-1/1</td><td>7. BLOCK DIAGRAM
Revised typo correction G-IC conerol → G-IC control</td></tr>
<tr><td>7B64PS 2709-
TX38D203VM2BAA-2
Page-9-8/11</td><td>9.6 LVDS RECEIVER TIMING
Revised
<table>
<tr><th colspan="2">Item</th><th>Symbol</th><th>Max.</th><th></th><th>Max.</th></tr>
<tr><td>RinX
(X=0,1,2,3)</td><td>1st data
position</td><td>tRP1</td><td>-0.65</td><td>→</td><td>+0.65</td></tr>
</table></td></tr>
<tr><td>7B64PS 2701-
TX38D203VM2BAA-2
Page-1-1/1</td><td rowspan="2">Company logo changed :
KOE
JDI Taiwan Inc.
→
JDI
Japan Display Inc.</td></tr>
<tr><td>7B64PS 2713-
TX38D203VM2BAA-2
Page-13-1/1</td></tr>
<tr><td></td><td></td><td></td></tr>
</table>

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 15” WHD of 13:5 format amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R (red), G (green), B (blue) sequentially. This display is RoHS compliant, COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX38D203VM0BAA
Module Dimensions	374.5(W) mm x 154.5(H) mm x 18.15 (D) mm
LCD Active Area	355.68(W) mm x 133.38(H) mm
Pixel Pitch	0.18525(W) mm x 0.18525 (H) mm
Resolution	1920 x 3(RGB)(W) x 720(H) Dots
Color Pixel Arrangement	R, G, B Vertical Stripe
LCD Type	Transmissive Color TFT; Normally Black
Display Type	Active Matrix
Number of Colors	16.7M Colors
Backlight	Light Emitting Diode (LED)
Weight	680 g
Interface	1ch LVDS; 60 pins
Power Supply Voltage	3.3V for LCD; 23.2V for Backlight
Power Consumption	2.24 W for LCD; 11.13 W for Backlight
Viewing Direction	Super Wide Version (In-Plane Switching)

4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V_{DD}	-0.3	4.0	V	-
Input Voltage of Logic	V_I	-0.3	V_{DD}+0.3	V	Note 1
Operating Temperature	Top	-40	85	°C	Note 2
Storage Temperature	Tst	-40	90	°C	Note 2

Note 1: The rating is defined for the signal voltages of the interface such as CLK and pixel data pairs.

Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:

- Background color, contrast and response time would be different in temperatures other than 25 °C.
- Operating under high temperature will shorten LED lifetime.

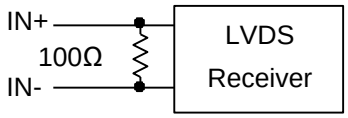
5. ELECTRICAL CHARACTERISTICS

5.1 LCD CHARACTERISTICS

$T_a = 25\ ^{\circ}C,\ V_{SS} = 0V$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-	3.0	3.3	3.6	V	-
Differential Input Voltage for LVDS Receiver Threshold	V_I	"H" level	-	-	+50	mV	Note 1
		"L" level	-50	-	-		
Power Supply Current	I_{DD}	V_{DD}=3.3V,white	570	700	850	mA	Note 2
Frame Frequency	f_{Frame}	-	59	60	71	Hz	
CLK Frequency	f_{CLK}	-	85	-	104	MHz	

Note 1: VCM 1.2V is common mode voltage of LVDS transmitter and receiver. The input terminal of LVDS receiver is terminated with 100Ω.



Note 2: An all white check pattern is used when measuring I_{DD}. f_{Frame} is set to 60 Hz. Moreover, 2A fuse is applied in the module for I_{DD}. For display activation and protection purpose, power supply is recommended larger than 5A to start the display and break fuse once any short circuit occurred.

5.2 BACKLIGHT CHARACTERISTICS

$T_a = 25\,^{\circ}C$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
LED Input Voltage	V_{LED}	-	-	23.2	27.2	V	Note1
LED Forward Current	I_{LED}	-	-	80	-	mA	
LED lifetime	-	I_{LED}= 480 mA	-	100K	-	hrs	Note 2
Thermistor type	-	-	NTC 10K			-	Note 3

Note 1: As Fig. 5.1 shown, LED current is constant, 80 mA, controlled by the LED Input Voltage when applying 23.2 V.

Note 2: The estimated lifetime is specified as the time to reduce 50% brightness by applying 80 mA at 25°C.

Note 3. Position : near LED.

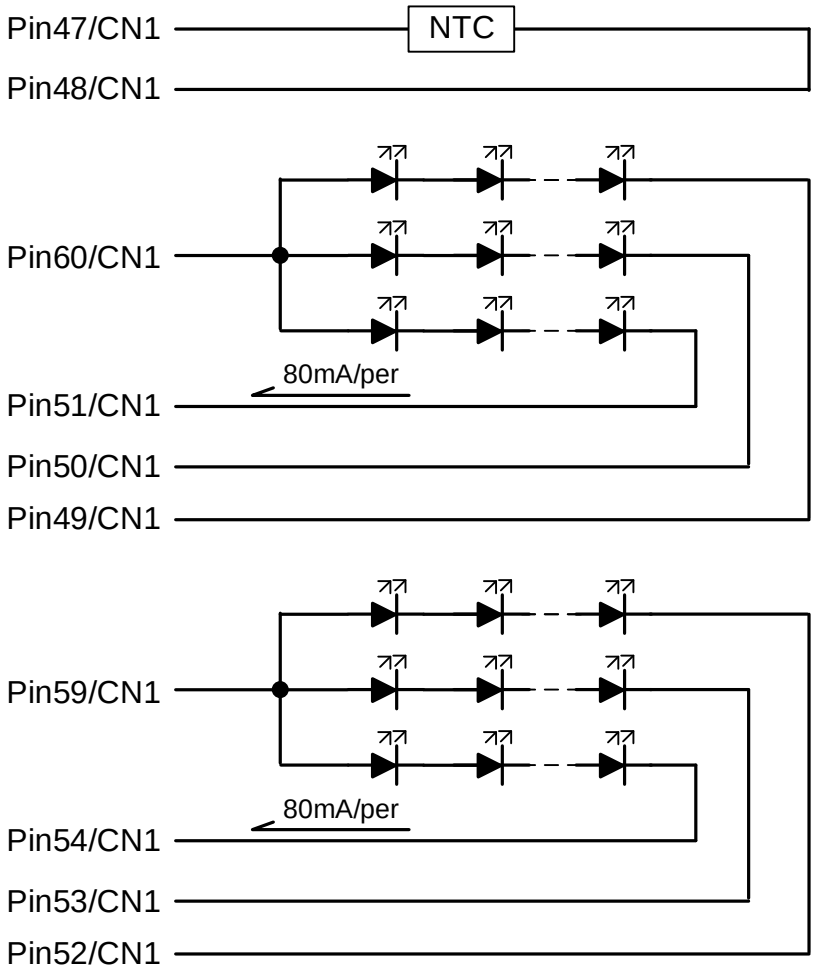


Fig 5.1

6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on for 30 minutes.
- The ambient temperature is 25°C.
- In the dark room less than 100 lx, the equipment has been set for the measurements as shown in Fig 6.1.

$T_a = 25\ ^{\circ}C,\ f_{Frame} = 60\,\text{Hz}, V_{DD} = 3.3\text{V}$

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Brightness of White		-	$\phi = 0^{\circ}, \theta = 0^{\circ}$, I_{LED}= 480 mA	850	1000	-	cd/m^2	Note 1
Brightness Uniformity		-		80	-	-	%	Note 2
Contrast Ratio		CR		800	1000	-	-	Note 3
Response Time		$T_r + T_f$	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	-	30	ms	Note 4
NTSC Ratio		-	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	70	-	%	-
Viewing Angle		θ x	$\phi = 0^{\circ}$, CR ≥ 10	-	85	-	Degree	Note 5
		θ x′	$\phi = 180^{\circ}$, CR ≥ 10	-	85	-		
		θ y	$\phi = 90^{\circ}$, CR ≥ 10	-	85	-		
		θ y′	$\phi = 270^{\circ}$, CR ≥ 10	-	85	-		
Color Chromaticity	Red	X	$\phi = 0^{\circ}, \theta = 0^{\circ}$	0.590	0.640	0.690	-	Note 6
		Y		0.280	0.330	0.380		
	Green	X		0.250	0.300	0.350		
		Y		0.550	0.600	0.650		
	Blue	X		0.100	0.150	0.200		
		Y		0.010	0.060	0.110		
	White	X		0.263	0.313	0.363		
		Y		0.279	0.329	0.379		

Note 1: The brightness is measured from the center point of the panel, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

$$\text{Brightness uniformity} = \frac{\text{Min. Brightness}}{\text{Max. Brightness}} \text{ X100\%}$$

which is based on the brightness values of the 9 points in active area measured by BM-5 as shown in Fig. 6.2.

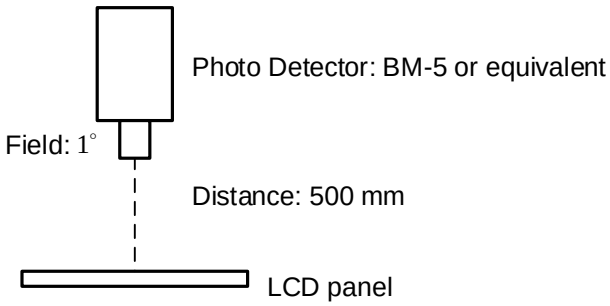


Fig 6.1

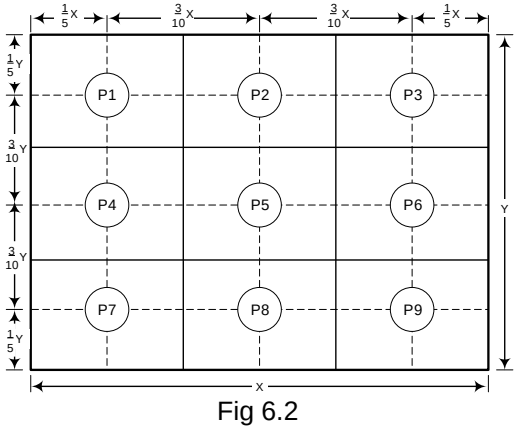


Fig 6.2

Note 3: The Contrast Ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{\text{Brightness of White}}{\text{Brightness of Black}}$$

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 10% brightness to 90% brightness when the data is from black to white. Oppositely, Falling time is the period from 90% brightness falling to 10% brightness.

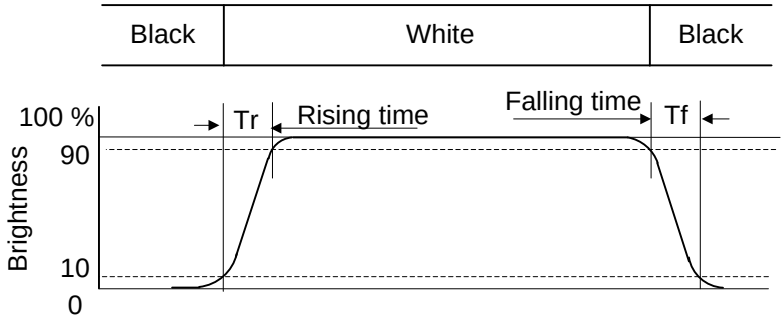


Fig.6.3

Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270^\circ$ means 6 o'clock, and $\phi = 0^\circ$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.

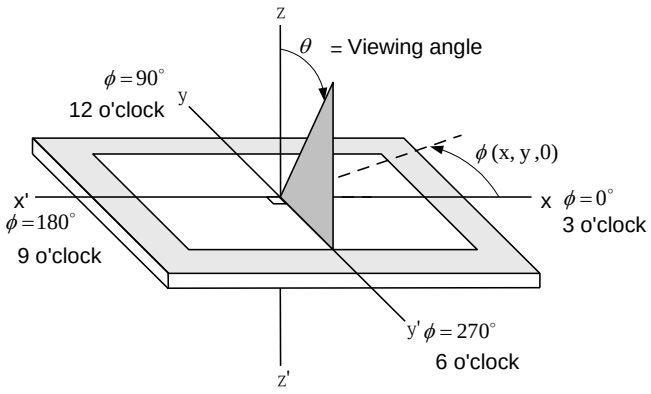
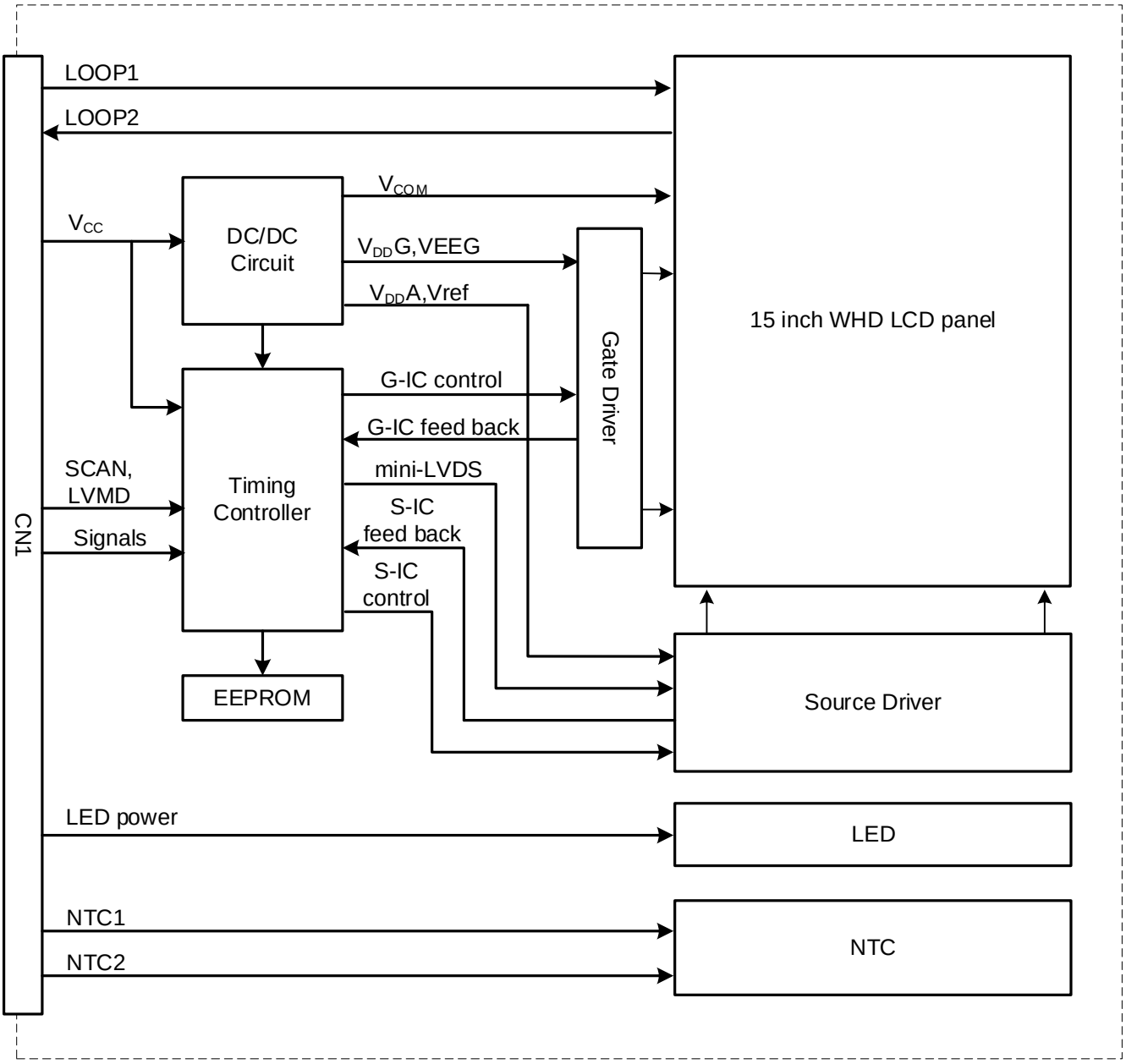


Fig 6.4

Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

7. BLOCK DIAGRAM



Note 1: Signals are CLK and pixel data pairs.

8. RELIABILITY TESTS

Test Item	Condition	
High Temperature	1) Operating 2) 85°C	500 hrs
Low Temperature	1) Operating 2) -40°C	500 hrs
High Temperature	1) Storage 2) 90°C	500 hrs
Low Temperature	1) Storage 2) -40°C	500 hrs
Heat Cycle	1) Operating 2) –40°C ~85°C 3) 3hrs~1hr~3hrs	500 hrs
Thermal Shock	1) Non-Operating 2) -40°C ↔ 85°C 3) 0.5 hr ↔ 0.5 hr	500 hrs
High Temperature & Humidity	1) Operating 2) 65°C & 85%RH 3) Without condensation	500 hrs (Note 3)
Vibration	1) Non-Operating 2) 10～200 Hz 3) 5G 4) X, Y, and Z directions	1 hr for each direction
Mechanical Shock	1) Non-Operating 2) 10 ms 3) 80G 4) ±X, ±Y and ±Z directions	Once for each direction
ESD	1) Operating 2) Tip: 150 pF, 330Ω 3) Air discharge for glass: ± 15KV 4) Contact discharge for metal frame: ± 8KV	1) Glass: 9 points 2) Metal frame: 8 points (Note4)

Note 1: Display functionalities are inspected under the conditions defined in the specification after the reliability tests.

Note 2: The display is not guaranteed for use in corrosive gas environments.

Note 3: Under the condition of high temperature & humidity, if the temperature is higher than 60℃, the humidity needs to be reduced as Fig. 8.1 shown.

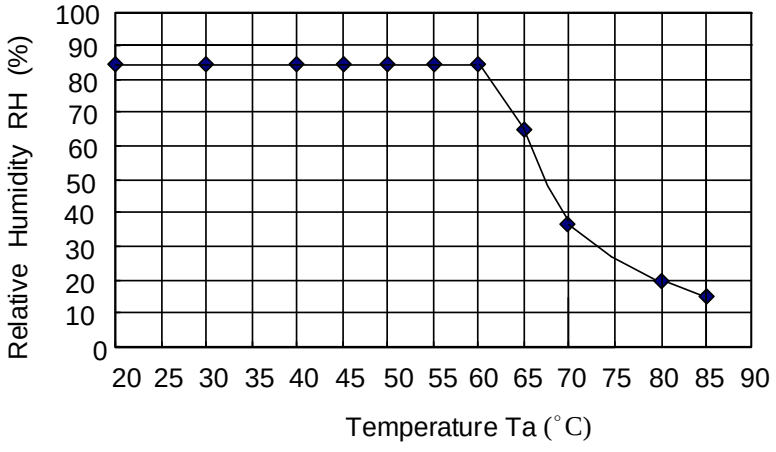


Fig. 8.1

Note 4: All pins of LCD interface (CN1) have been tested by ± 200V contact discharge of ESD under non-operating condition.

9. LCD INTERFACE

9.1 INTERFACE PIN CONNECTIONS

The display interface connector (CN1) is 505110-6091 made by MOLEX and pin assignment is as below:

Pin No.	Symbol	Signal	Pin No.	Symbol	Signal
1	LOOP1	Loop back1 → LOOP2(*2)	31	RX3-	LVDS Data3-
2	V_{CC}	Power Supply (3.3V)	32	GND	Ground
3	V_{CC}	Power Supply (3.3V)	33	RXC+	LVDS Clock+
4	V_{CC}	Power Supply (3.3V)	34	RXC-	LVDS Clock-
5	V_{CC}	Power Supply (3.3V)	35	GND	Ground
6	NC	No connecting	36	RX2+	LVDS Data2+
7	GND	Ground	37	RX2-	LVDS Data2-
8	GND	Ground	38	GND	Ground
9	GND	Ground	39	RX1+	LVDS Data1+
10	GND	Ground	40	RX1-	LVDS Data1-
11	LVMD	Input:：LVDS MAP (Low：JEIDA, High：VESA)	41	GND	Ground
12	SCAN	Input:：Scan direction (Low：Normal, High：Reverse)	42	RX0+	LVDS Data0+
13	NC	No connecting	43	RX0-	LVDS Data0-
14	GND	Ground	44	NC	No connecting
15	Reserved	Keep to V_{CC} level	45	NC	No connecting
16	Reserved	Keep to V_{CC} level	46	LOOP2	Loop back2 → LOOP1(*2)
17	GND	Ground	47	NTC1	Backlight temperature Sensor pin 1 (*3)
18	Reserved	Keep to V_{CC} level	48	NTC2	Backlight temperature Sensor pin 2 (*3)
19	Reserved	Keep to V_{CC} level	49	BL_C6	Backlight cathode 6
20	GND	Ground	50	BL_C5	Backlight cathode 5
21	Reserved	Keep to V_{CC} level	51	BL_C4	Backlight cathode 4
22	Reserved	Keep to V_{CC} level	52	BL_C3	Backlight cathode 3
23	GND	Ground	53	BL_C2	Backlight cathode 2
24	Reserved	Keep to V_{CC} level	54	BL_C1	Backlight cathode 1
25	Reserved	Keep to V_{CC} level	55	NC	No connecting
26	GND	Ground	56	NC	No connecting
27	Reserved	Keep to V_{CC} level	57	NC	No connecting
28	Reserved	Keep to V_{CC} level	58	NC	No connecting
29	GND	Ground	59	BL_A1	Backlight anode1 common (For cathode 1,2,3)
30	RX3+	LVDS Data3+	60	BL_A2	Backlight anode1 common (For cathode 4,5,6)

Note 1 : LOOP1, LOOP2

LOOP1 and LOOP2 are connected in the LCD module through PCB and panel(PCB→FPC→Glass→ FPC→PCB). This can be used for internal broken wire detection.

Note 2 : NTC1, NTC2

Thermistor characteristics (Material：TD05-3H103FR)

Item	Specification	Condition
Resistance	10k ohm ±1%	zero-power resistance at 25℃
B-constant	3370K ±1%	B-value between 25 to 50℃
Maximum power dissipation	300mW	-
Heat dissipation	2.4mW/℃	at 25℃
Operating temperature range	-40 ～150℃	-
Rohs	Compliant	--

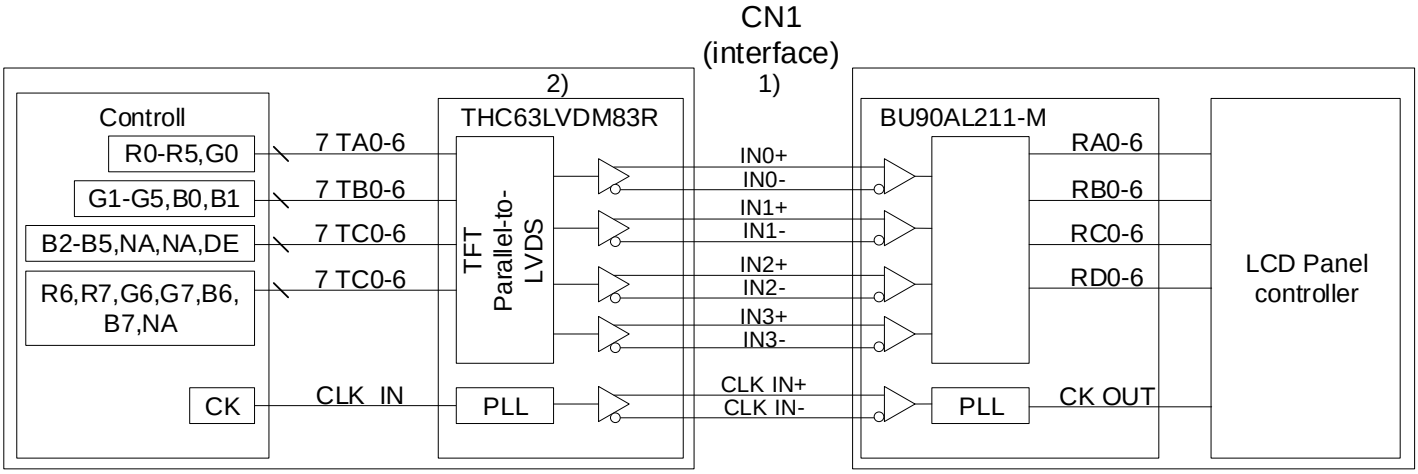
Placed in the edge of Backlight close to LED.

Note 3 : Please refer to 9.9 SCAN DIRECTION for the setting methods of scan function.

9.2 LVDS INTERFACE

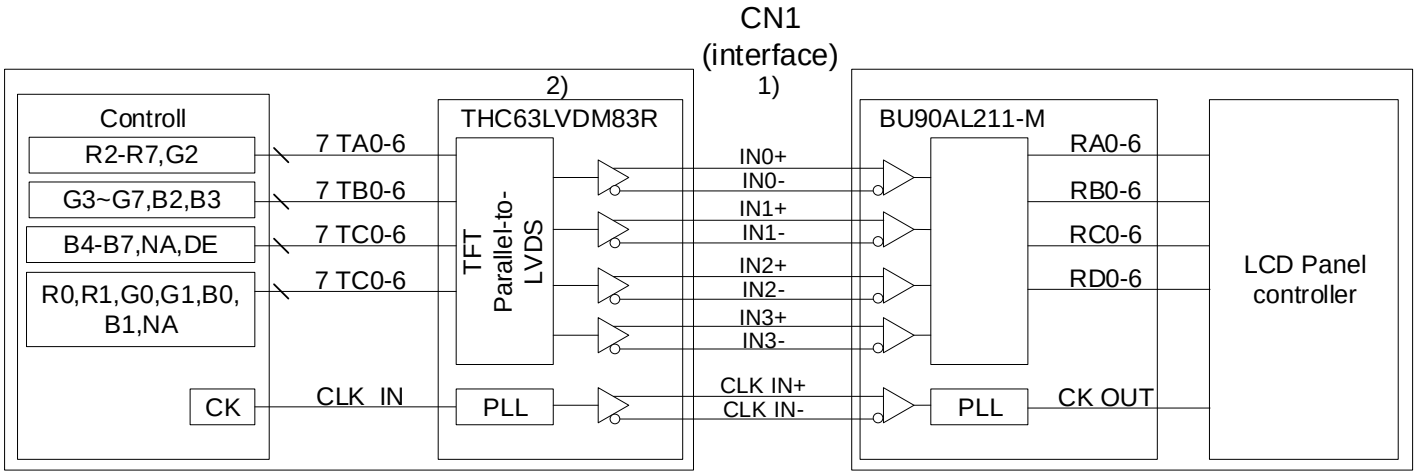
1) VESA Mapping Mode

8Bit Mode (LVMD = H)

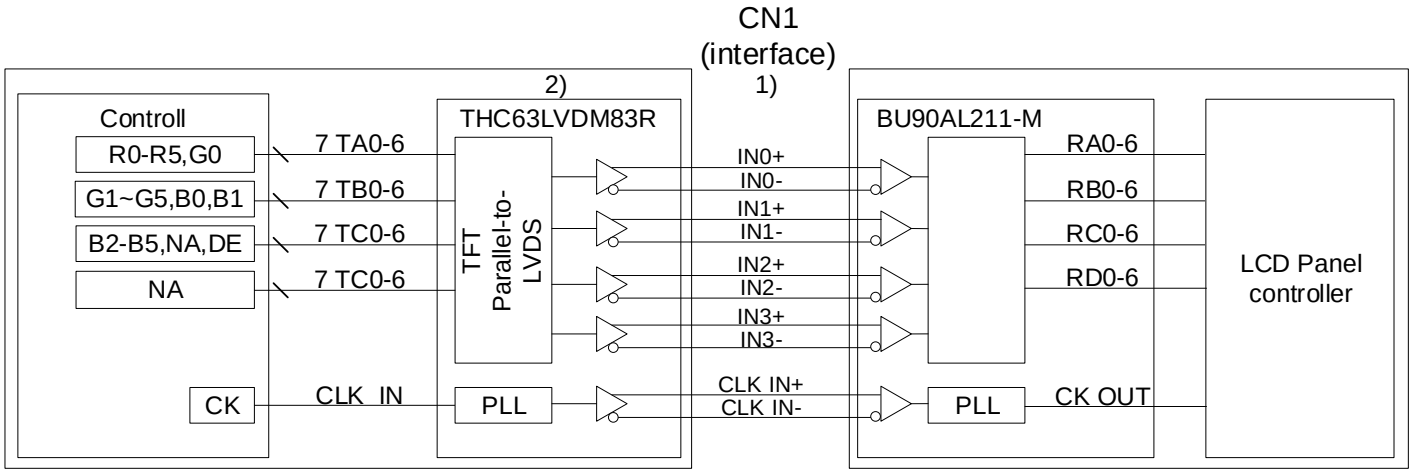


2) JEIDA Mapping Mode

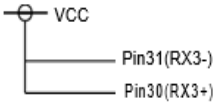
① 8Bit Mode(LVMD = L)



② 6Bit Mode(LVMD = L)



When LVMD is “L” and 6bit input, the following connection is recommended.



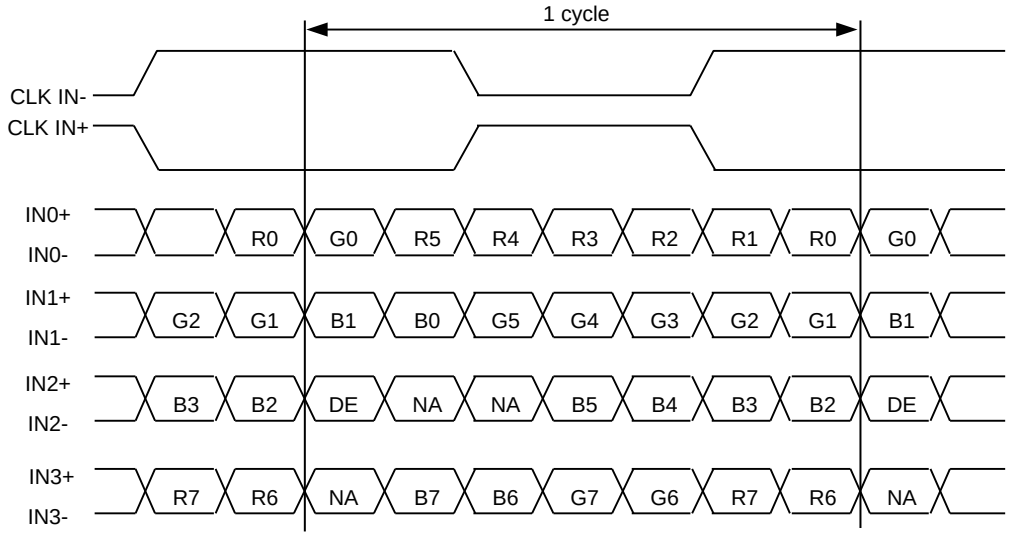
Note 1: Transmitter Made by Thine : THC63LVDM83R or equivalent.

Note 2: 100Ω impedance of LVDS cable is recommended for best optical performance.

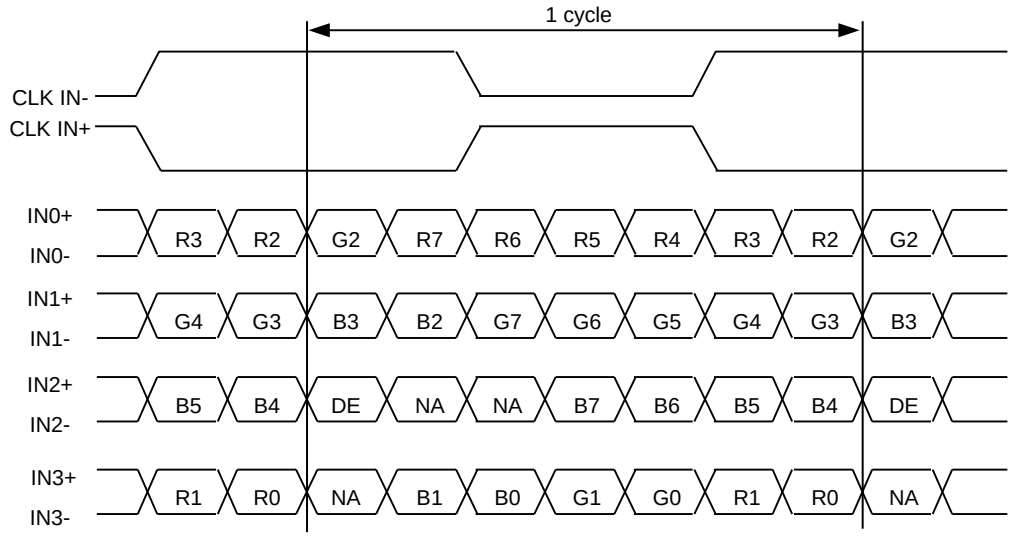
9.3 DATA MAPPING

Transmitter		VESA Mode	JEIDA Mode	
		8Bit Mode	8Bit Mode	6Bit Mode
Pin No.	Pin name	LVMD		
		HIGH	LOW	
51	TA0	R0(LSB)	R2	R0(LSB)
52	TA1	R1	R3	R1
54	TA2	R2	R4	R2
55	TA3	R3	R5	R3
56	TA4	R4	R6	R4
3	TA5	R5	R7(MSB)	R5(MSB)
4	TA6	G0(LSB)	G2	G0(LSB)
6	TB0	G1	G3	G1
7	TB1	G2	G4	G2
11	TB2	G3	G5	G3
12	TB3	G4	G6	G4
14	TB4	G5	G7(MSB)	G5(MSB)
15	TB5	B0(LSB)	B2	B0(LSB)
19	TB6	B1	B3	B1
20	TC0	B2	B4	B2
22	TC1	B3	B5	B3
23	TC2	B4	B6	B4
24	TC3	B5	B7(MSB)	B5(MSB)
27	TC4	(NA)	(NA)	(NA)
28	TC5	(NA)	(NA)	(NA)
30	TC6	DE	DE	DE
50	TD0	R6	R0(LSB)	(NA)
2	TD1	R7(MSB)	R1	(NA)
8	TD2	G6	G0(LSB)	(NA)
10	TD3	G7(MSB)	G1	(NA)
16	TD4	B6	B0(LSB)	(NA)
18	TD5	B7(MSB)	B1	(NA)
25	TD6	(NA)	(NA)	(NA)

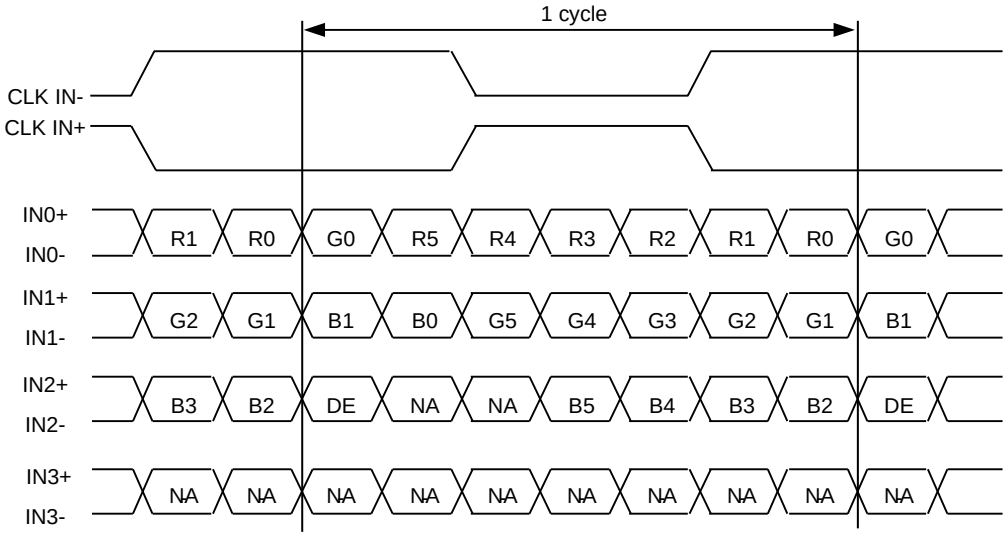
(1) 8Bit Mode (LVMD=H)



(2) 8Bit Mode (LVMD=L)



(3) 6Bit Mode (LVMD=L)



DE : Display Enable

NA : Not Available

9.4 TIMING CHART

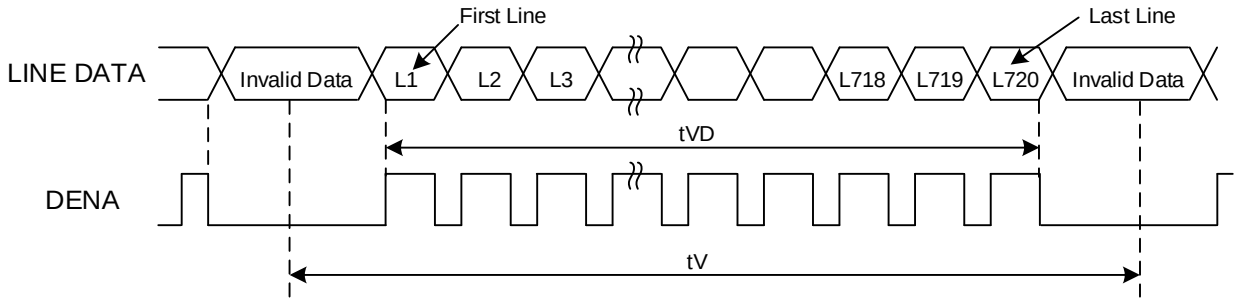


Fig. 9.1 Vertical Time

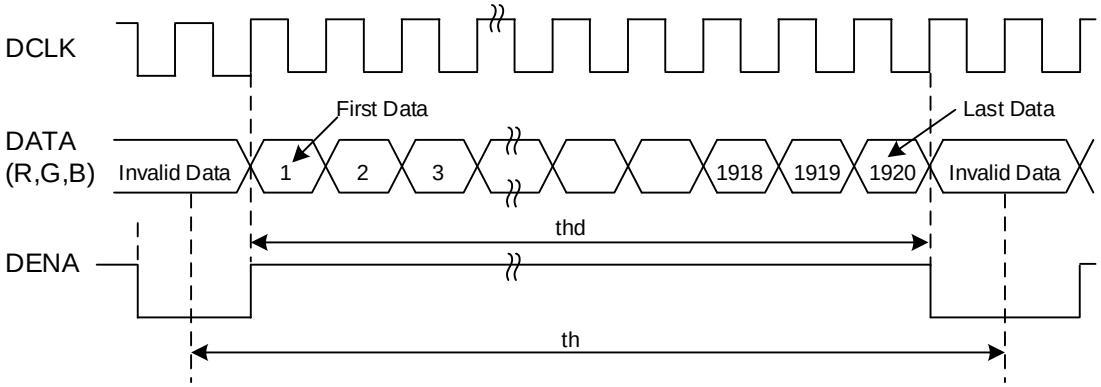


Fig. 9.2 Horizontal Timing

9.5 TIME TABLE

The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (f_{Frame}) = 60 Hz to define. If 60 Hz is not the aim to set, less than 62 Hz for f_{Frame} is recommended to apply for better performance by other parameter combination as the definitions in section 5.1.

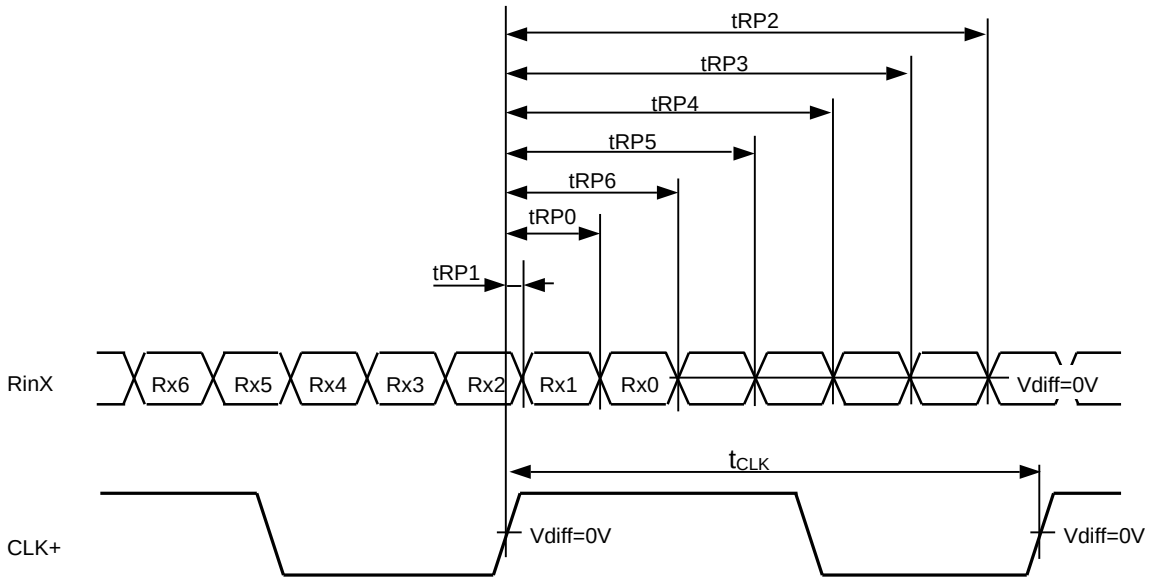
A. Horizontal and Vertical Timing

Item		Symbol	Min.	Typ.	Max.	Unit
Horizontal	CLK Frequency	fclk	86.6	87.1	104	M Hz
	Display Data	thd	1920			CLK
	Cycle Time	th	1990	1996	2166	
Vertical	Display Data	tvd	720			H
	Cycle Time	tv	725	727	800	

B. Setup and Hold Time

Item		Symbol	Min.	Typ.	Max.	Unit
CLK	Duty	Tcwh	40	50	60	%
	Cycle Time	Tcph	-	30	-	ns
Data	Setup Time	Tdsu	5	-	-	
	Hold Time	Tdhd	5	-	-	
DE	Setup Time	Tesu	5	-	-	
	Hold Time	Tehd	5	-	-	

9.6 LVDS RECEIVER TIMING



RinX= (RinX+)-(RinX-) (X=0, 1, 2, 3)

Item		Symbol	Min.	Typ.	Max.	Unit
CLK	Cycle frequency	1/tcLK	85	87.1	104	MHz
RinX (X=0,1,2,3)	0 data position	tRP0	$1/7t_{CLK}$-0.65	$1/7*t_{CLK}$	$1/7t_{CLK}$+0.65	ns
	1st data position	tRP1	-0.65	0	+0.65	
	2nd data position	tRP2	$6/7t_{CLK}$-0.65	$6/7*t_{CLK}$	$6/7t_{CLK}$+0.65	
	3rd data position	tRP3	$5/7t_{CLK}$-0.65	$5/7*t_{CLK}$	$5/7t_{CLK}$+0.65	
	4th data position	tRP4	$4/7t_{CLK}$-0.65	$4/7*t_{CLK}$	$4/7t_{CLK}$+0.65	
	5th data position	tRP5	$3/7t_{CLK}$-0.65	$3/7*t_{CLK}$	$3/7t_{CLK}$+0.65	
	6th data position	tRP6	$2/7t_{CLK}$-0.65	$2/7*t_{CLK}$	$2/7t_{CLK}$+0.65	

9.7 DATA INPUT for DISPLAY COLOR

(8BIT MODE)

Input color		Red Data								Green Data								Blue Data							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB							LSB	MSB							LSB	MSB							LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal : 1 : High, 0 : Low

(6BIT MODE)

Input color		Red Data						Green Data						Blue Data					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
		MSB					LSB	MSB					LSB	MSB					LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal : 1 : High, 0 : Low

9.8 POWER SEQUENCE

Interface signals are also shown in the chart. Signals from any system shall be Hi- resistance state or low level when V_{DD} voltage is off.

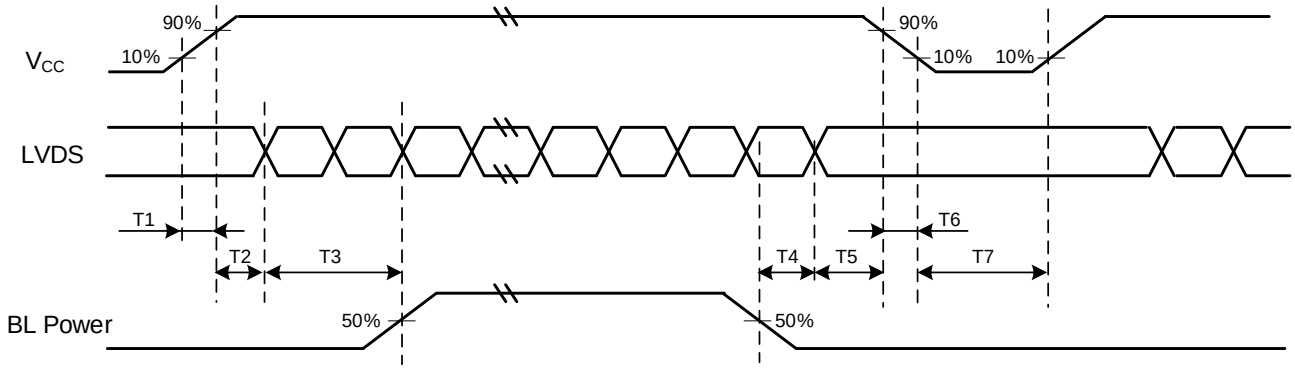
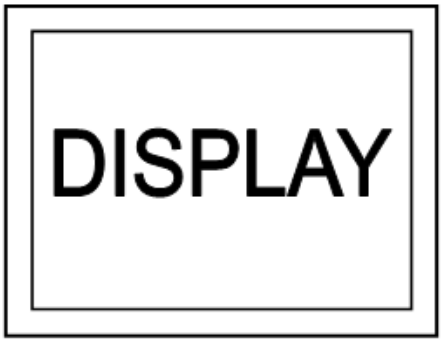


Fig 9.4 Power Sequence

Sequence	Parameter	Symbol	Min.	Max.	Unit
Power ON	VCC rising time	T1	(0.1)	(10)	ms
	VCC 90% to signal	T2	0	--	ms
	signal to Backlight ON (T2<100ms)	T3	(100)	--	ms
Power OFF	Backlight OFF to signal stop	T4	0	--	ms
	signal stop to VCC fall	T5	(60)	--	ms
	VCC falling time	T6	(0.1)	(10)	ms
Restart	restart VCC time	T7	(400)	--	ms

9.9 SCAN DIRECTION

LCD scan direction

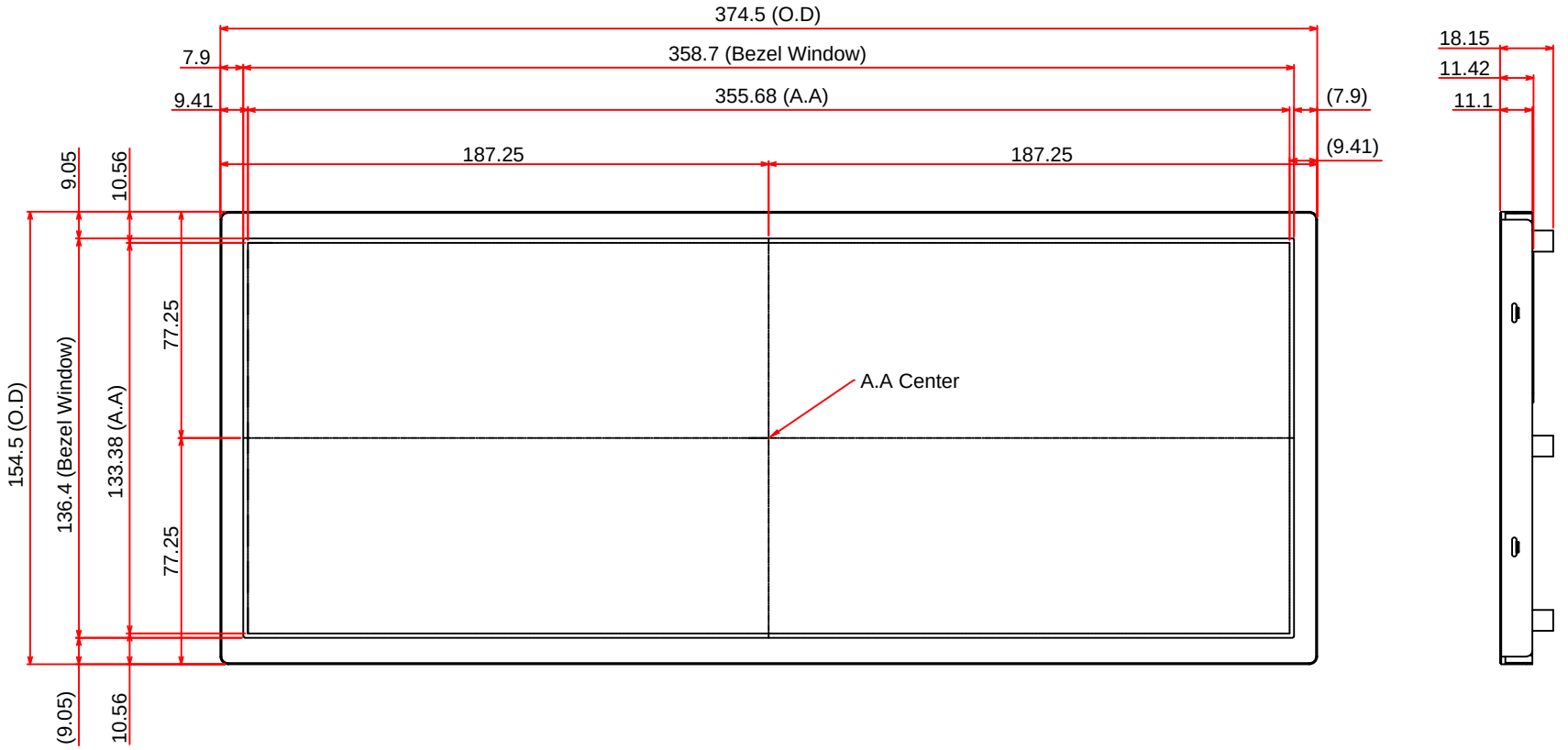


SCAN：Low

SCAN：High

10. OUTLINE DIMENSIONS

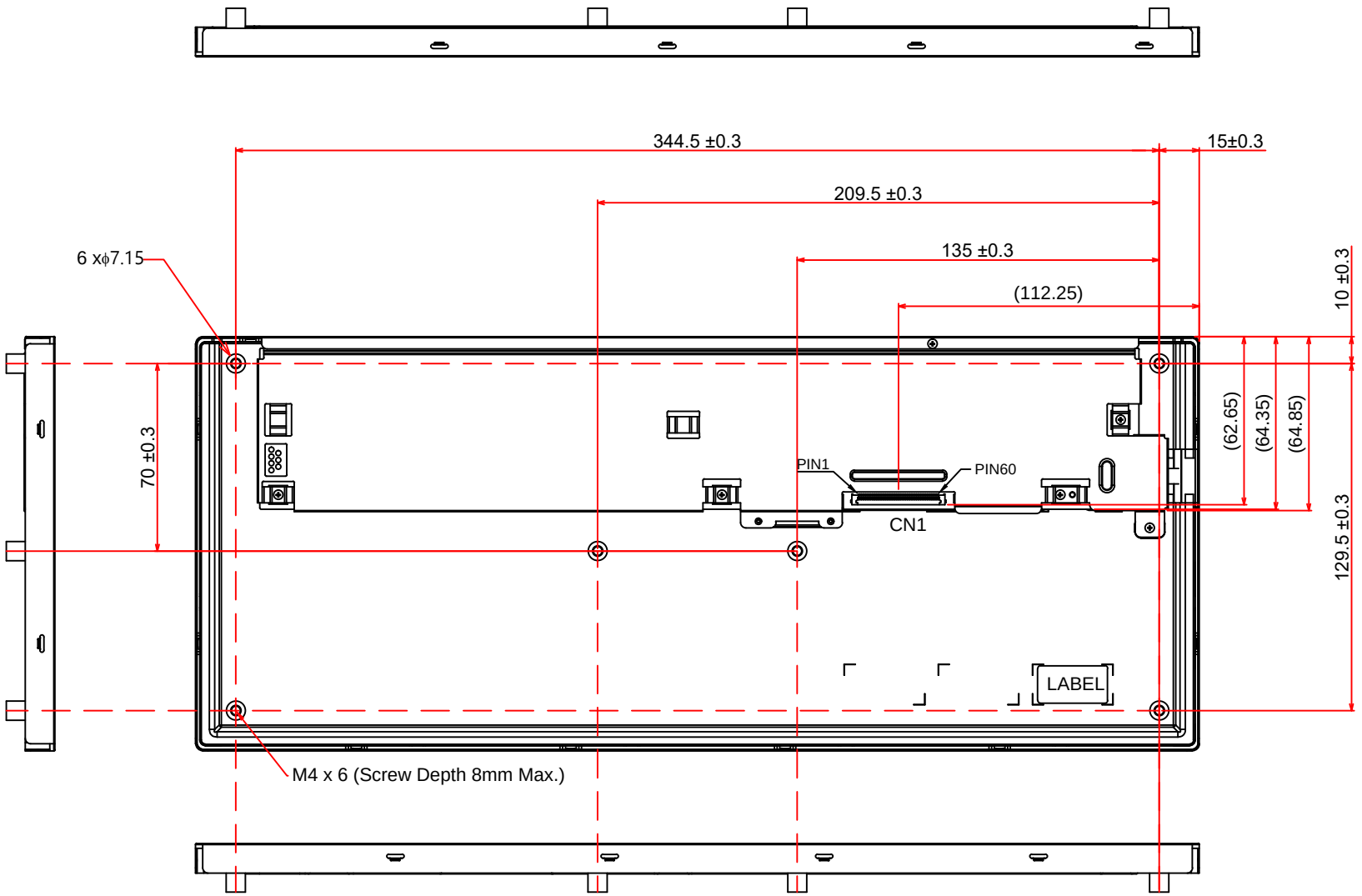
10.1 FRONT VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

JDI Taiwan Inc. Kaohsiung Branch	SHEET No.	7B64PS 2710-TX38D203VM0BAA-2	PAGE	10-1/2

10.2 RAER VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

JDI Taiwan Inc. Kaohsiung Branch	SHEET No.	7B64PS 2710-TX38D203VM0BAA-2	PAGE	10-2/2

11. APPEARANCE STANDARD

The appearance inspection is performed in a room around 500~1000 lx based on the conditions as below:

- The distance between inspector's eyes and display is 30 cm.
- The viewing zone is defined with angle θ shown in Fig. 11.1 The inspection should be performed within 45° when display is shut down. The inspection should be performed within 5° when display is power on.

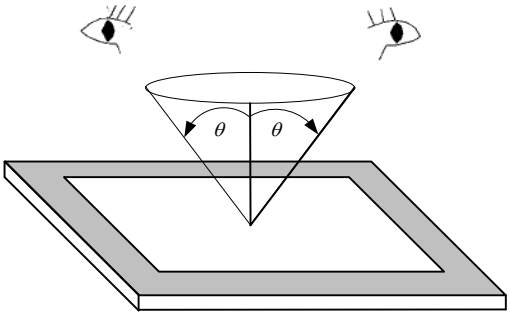


Fig. 11.1

11.1 THE DEFINITION OF LCD ZONE

LCD panel is divided into 3 areas as shown in Fig.11.2 for appearance specification in next section. A zone is the LCD active area (dot area); B zone is the area between A zone and metal frame.

In terms of housing design, B zone is the recommended window area customers' housing should be located in.

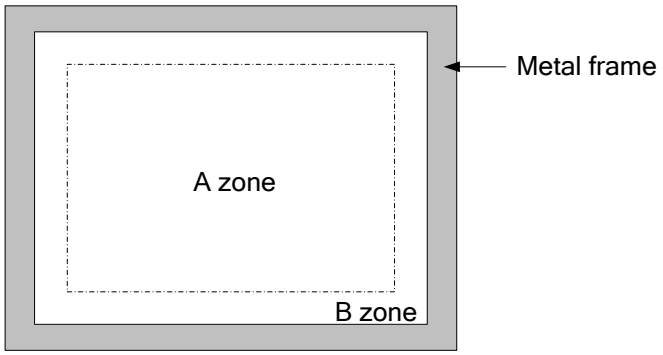


Fig. 11.2

11.2 LCD APPEARANCE SPECIFICATION

The specification as below is defined as the amount of unexpected phenomenon or material in different zones of LCD panel. The definitions of length, width and average diameter using in the table are shown in Fig. 11.3 and Fig. 11.4.

<table>
<tr><td>Item</td><td colspan="4">Criteria</td><td>Applied zone</td></tr>
<tr><td rowspan="4">Scratches</td><td>Length (mm)</td><td>Width (mm)</td><td>Maximum number</td><td>Minimum space</td><td rowspan="4">A, B</td></tr>
<tr><td>Ignored</td><td>W≦0.02</td><td>Ignored</td><td>-</td></tr>
<tr><td>L≦40</td><td>0.02＜W≦0.04</td><td>10</td><td>-</td></tr>
<tr><td>L≦20</td><td>W≦0.04</td><td>10</td><td>-</td></tr>
<tr><td>Dent</td><td colspan="4">Serious one is not allowed</td><td>A</td></tr>
<tr><td>Wrinkles in polarizer</td><td colspan="4">Serious one is not allowed</td><td>A</td></tr>
<tr><td rowspan="5">Bubbles on polarizer</td><td colspan="2">Average diameter (mm)</td><td colspan="2">Maximum number</td><td rowspan="5">A</td></tr>
<tr><td colspan="2">D≦0.2</td><td colspan="2">Ignored</td></tr>
<tr><td colspan="2">0.2＜D≦0.3</td><td colspan="2">12</td></tr>
<tr><td colspan="2">0.3＜D≦0.5</td><td colspan="2">3</td></tr>
<tr><td colspan="2">0.5＜D</td><td colspan="2">0</td></tr>
<tr><td rowspan="14">1) Stains
2) Foreign Materials
3) Dark Spot</td><td colspan="3">Filamentous (Line shape)</td><td rowspan="4">A, B</td></tr>
<tr><td>Length (mm)</td><td>Width (mm)</td><td>Maximum number</td></tr>
<tr><td>L≦2.0</td><td>W≦0.03</td><td>Ignored</td></tr>
<tr><td>L≦3.0</td><td>0.03＜W≦0.05</td><td>6</td></tr>
<tr><td>L≦2.5</td><td>0.05＜W≦0.1</td><td>1</td><td rowspan="9">A, B</td></tr>
<tr><td colspan="3">Round (Dot shape)</td></tr>
<tr><td>Average diameter (mm)</td><td>Maximum number</td><td>Minimum Space</td></tr>
<tr><td>D＜0.2</td><td>Ignored</td><td>-</td></tr>
<tr><td>0.2≦D＜0.3</td><td>10</td><td>10 mm</td></tr>
<tr><td>0.3≦D＜0.4</td><td>5</td><td>30 mm</td></tr>
<tr><td>0.4≦D</td><td>0</td><td>-</td></tr>
<tr><td colspan="3">Those wiped out easily are acceptable</td></tr>
<tr><td rowspan="6">Dot-Defect
(Note 1)</td><td></td><td>Type</td><td>Maximum number</td><td rowspan="6">A</td></tr>
<tr><td>Bright dot-defect</td><td>1 dot</td><td>0</td></tr>
<tr><td rowspan="3">Dark dot-defect</td><td>1 dot</td><td>5</td></tr>
<tr><td>2 adjacent dot</td><td>2</td></tr>
<tr><td>In total</td><td>5</td></tr>
<tr><td colspan="2">In total</td><td>5</td></tr>
</table>

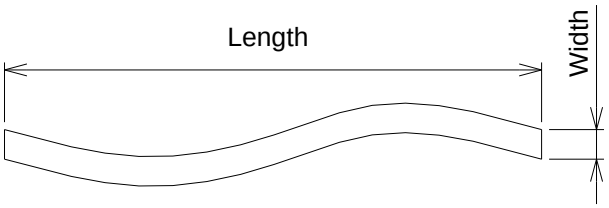


Fig 11.3

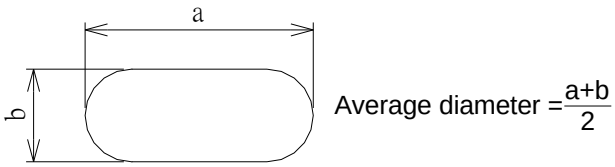


Fig 11.4

Note 1: The definitions of dot defect are as below:

- For bright dot-defect, showing black pattern, visible with defect size over 1/2 dot is defined.
- For dark dot-defect, showing white pattern, defect size over 1/2 dot area is defined.
- The definition of 1-dot-defect is the defect-dot, which is isolated and no adjacent defect-dot.
- The definition of adjacent dot is shown as Fig. 11.5.
- The Density of dot defect is defined in the area within diameter ϕ =10mm.

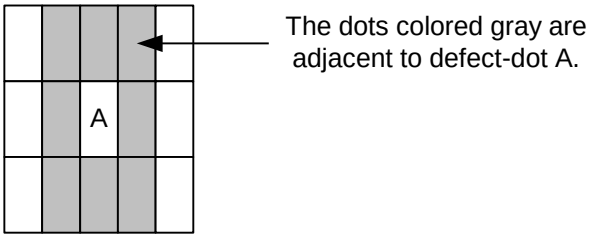


Fig. 11.5

12. PRECAUTIONS

12.1 PRECAUTIONS of ESD

1) Before handling the display, please ensure your body has been connected to ground to avoid any damages by ESD. Also, do not touch display's interface directly when assembling.

2) Please remove the protection film very slowly before turning on the display to avoid generating ESD.

12.2 PRECAUTIONS of HANDLING

1) In order to keep the appearance of display in good condition, please do not rub any surfaces of the displays by sharp tools harder than 3H, especially touch panel, metal frame and polarizer.

2) Please do not pile the displays in order to avoid any scars leaving on the display. In order to avoid any injuries, please pay more attention for the edges of glasses and metal frame, and wear finger cots to protect yourself and the display before working on it.

3) Touching the display area or the terminal pins with bare hand is prohibited. This is because it will stain the display area and cause poor insulation between terminal pins, and might affect display's electrical characteristics furthermore.

4) Do not use any harmful chemicals such as acetone, toluene, and isopropyl alcohol to clean display's surfaces.

5) Please use soft cloth or absorbent cotton with ethanol to clean the display by gently wiping. Moreover, when wiping the display, please wipe it by horizontal or vertical direction instead of circling to prevent leaving scars on the display's surface, especially polarizer.

6) Please wipe any unknown liquids immediately such as saliva, water or dew on the display to avoid color fading or any permanently damages.

7) Maximum pressure to the surface of the display must be less than 1.96×10^4 Pa. If the area of adding pressure is less than $1\,\mathrm{cm}^2$, the maximum pressure must be less than 1.96N.

12.3 PRECAUTIONS OF OPERATING

1) Please input signals and voltages to the displays according to the values defined in the section of electrical characteristics to obtain the best performance. Any voltages over than absolute maximum rating will cause permanent damages to this display. Also, any timing of the signals out of this specification would cause unexpected performance.

2) When the display is operating at significant low temperature, the response time will be slower than it at $25\,\mathrm{C}^\circ$. In high temperature, the color will be slightly dark and blue compared to original pattern. However, these are temperature-related phenomenon of LCD and it will not cause permanent damages to the display when used within the operating temperature.

3) The use of screen saver or sleep mode is recommended when static images are likely for long periods of time. This is to avoid the possibility of image sticking.

4) Spike noise can cause malfunction of the circuit. The recommended limitation of spike noise is no bigger than ± 100 mV.

12.4 PRECAUTIONS of STORAGE

If the displays are going to be stored for years, please be aware the following notices.

1) Please store the displays in a dark room to avoid any damages from sunlight and other sources of UV light.
2) The recommended long term storage temperature is between 10℃ ~35℃ and 55%~75% humidity to avoid causing bubbles between polarizer and LCD glasses, and polarizer peeling from LCD glasses.
3) It would be better to keep the displays in the container, which is shipped from JDI, and do not unpack it.
4) Please do not stick any labels on the display surface for a long time, especially on the polarizer.

13. DESIGNATION of LOT MARK

1) The lot mark is showing in Fig.13.1. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 6 digits are the serial number.

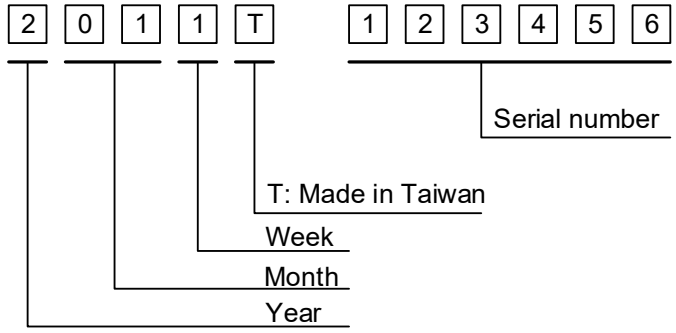


Fig. 13.1

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Mark
2022	2
2023	3
2024	4
2025	5
2026	6

Month	Mark	Month	Mark
1	01	7	07
2	02	8	08
3	03	9	09
4	04	10	10
5	05	11	11
6	06	12	12

Week (Days)	Mark
1~7	1
8~14	2
15~21	3
22~28	4
29~31	5

3) The location of the lot mark is on the back of the display shown in Fig. 13.2.

Label example:

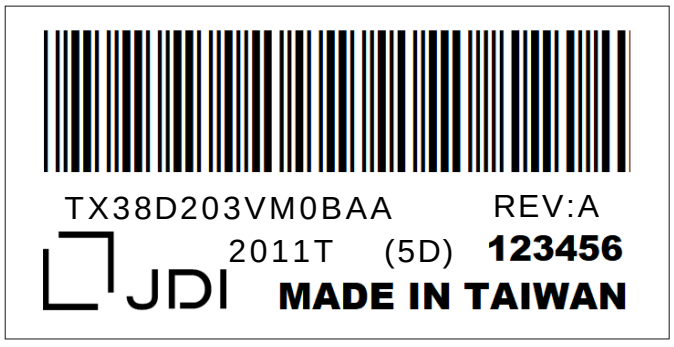


Fig 13.2