CONFIDENTIAL(A)

SPECIFICATION

OF

LIQUID CRYSTAL DISPLAY MODULE

UNITED RADIANT TECHNOLOGY CORP. 光聯科技
Jun-04-2025
品管部 QC DEPT.

CUSTOMER : URT-STD

Model No. : UMOH-9729FD-T

Model version : 0

Document Revision : 2

CUSTOMER APPROVED SIGNATURE			

This specification need to be signed by purchaser or customer as a specification of products production and delivery from URT. Without signature of this specification , any purchase order for this model no. will be treated and considered that this specification is automatically acknowledged and accepted by purchaser or customer.

U.R.T. UNITED RADIANT TECHNOLOGY CORPORATION

Joe Wu	Ashin Chiu	Ashin Chiu	Jun-04-2025
APPROVED	CHECKED	PREPARED	Date

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Revision record

Document Revision	Model No. Version No.	Description	Revision by
0	UMOH-9729MD-T Version No. 0	7”TFT.	William Don Emir Lu 04-Jan-2024
1	UMOH-9729MD-T Version No. 1	Update LCD Type Description..	William Don Emir Lu 02-Jun-2025
2	UMOH-9729FD-T Version No. 0	1. Update LCD Type Description. 2. Modify module number from UMOH-9729MD-T to UMOH-9729FD-T.	William Don Emir Lu 04-Jun-2025

CONTENTS:

1. BASIC SPECIFICATION

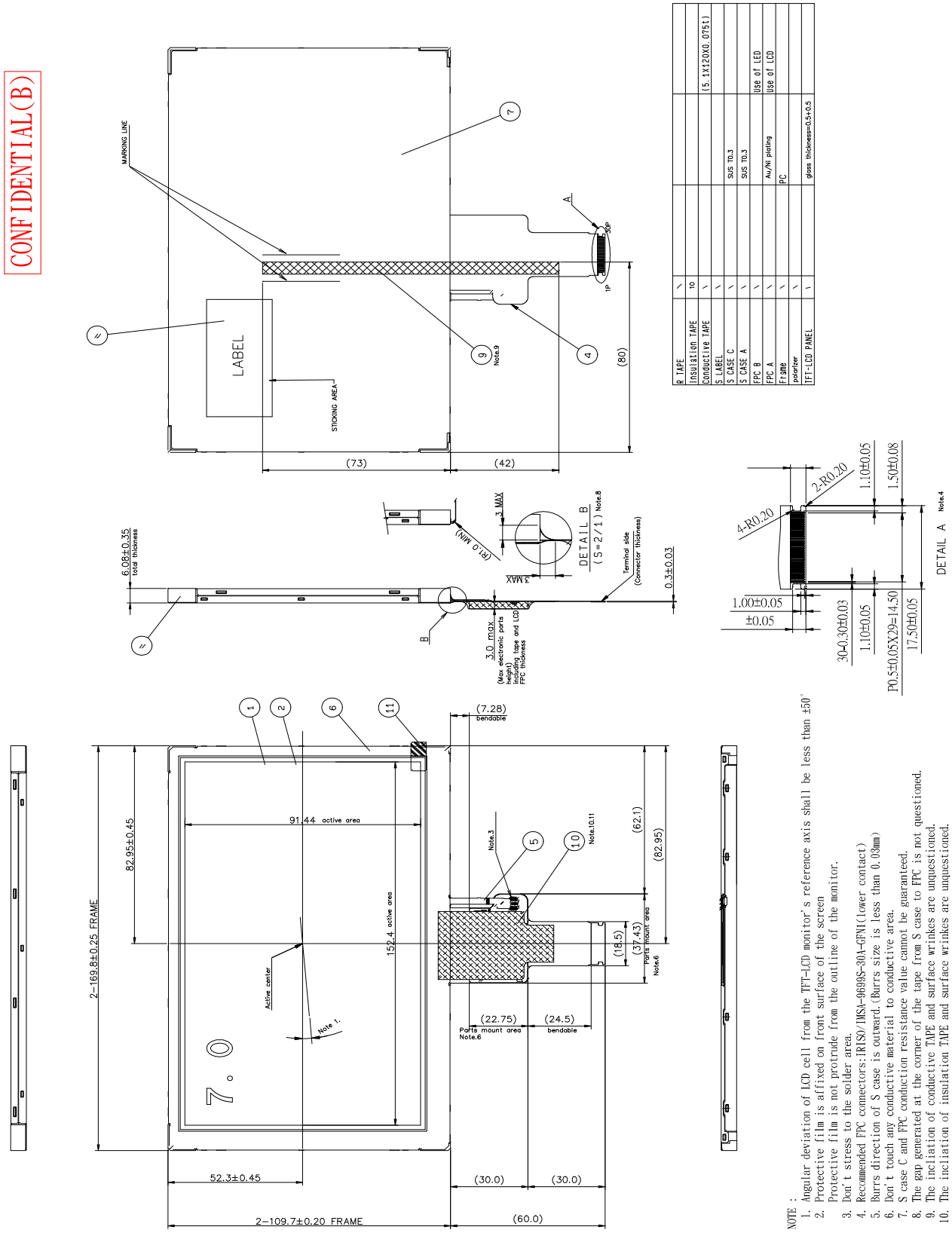
1.1 Mechanical specifications

Items	Nominal Dimension	Unit
Active screen size	7.0” Diagonal	-
Dot Matrix	800 x RGB x 480	Pixel
Module Size (W x H x T)	169.8 x 109.7 x 6.08	mm.
Active Area (W×H)	152.4 x 91.44	mm.
Pixel Size (W×H)	0.1905 x 0.1905	mm.
Color depth	16.7M	color
Interface	LVDS(6 bit/8 bit)	-
Driving IC Package	COG	-
Module weight	170± 10%	g

1.2 Display specification

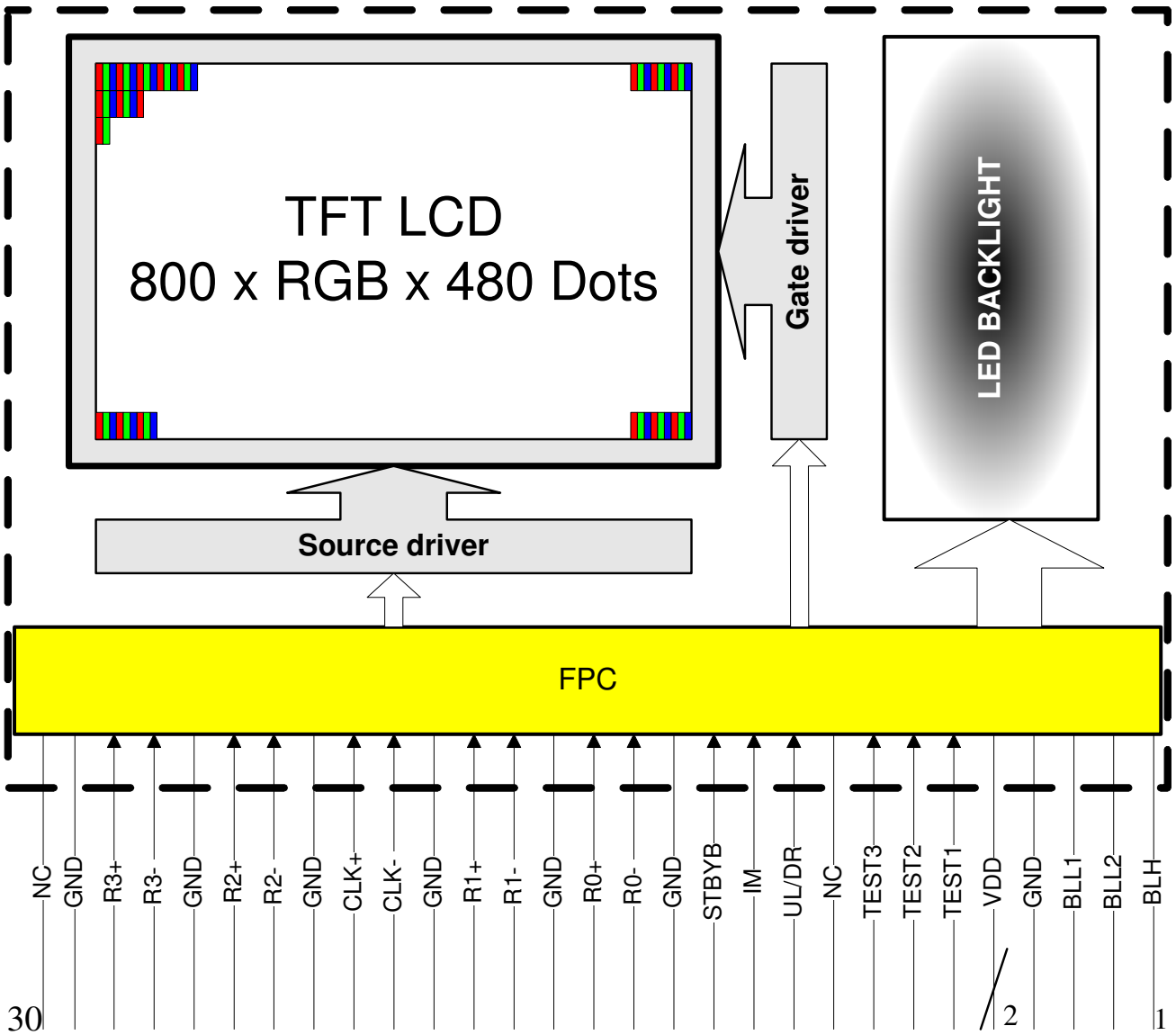
Display	Descriptions	Note
LCD Type	TFT transflective (Micro Reflective)	-
LCD Mode	Normally black	-
Pixel arrangement	RGB-stripe	-
Backlight Type	LED	-
Viewing Direction	All Viewing	-

1. O tline di ension



NOTE :
1. Angular deviation of LCD cell from the TFT-LCD monitor's reference axis shall be less than ±50'
2. Protective film is affixed on front surface of the screen
   Protective film is not protrude from the outline of the monitor.
3. Don't stress to the solder area.
4. Recommended FPC connectors: IRISO/IMSA-9698S-30A-GFN1(lower contact)
5. Burrs direction of S case is outward.(Burrs size is less than 0.03mm)
6. Don't touch any conductive material to conductive area.
7. S case C and FPC conduction resistance value cannot be guaranteed.
8. The gap generated at the corner of the tape from S case to FPC is not questioned.
9. The incliation of conductive TAPE and surface wrinkes are unquestioned.
10. The incliation of insulation TAPE and surface wrinkes are unquestioned.
11. Mounted parts may not be completely hidden by insulating tape.
12. Tolerance for not assigned ±0.5mm
13. LCD TYPE TFT transflective (Micro Reflective)

1.4 Block Diagram:



1.5 Interface Pin Connection :

Pin No.	Pin Symbol	I/O	Description	Remark
1	BLH	P	LED Drive power source . (Anode side)	
2	BLL2	P	LED Drive power source . (Cathode side 2)	
3	BLL1	P	LED Drive power source . (Cathode side 1)	
4	GND	I	Ground.	
5~6	VDD	P	Power supply input.	
7	TEST1	I	Test input (connect to VDD)	
8	TEST2	I	Test input (connect to GND)	
9	TEST3	I	Test input (connect to GND)	
10	NC	-		
11	UL/DR	I	Up & Left / Down & Right switching terminal.(Low : DR , High or NC :UL)	
12	IM	I	6 / 8 bit(based on VESA) switching terminal.(Low : 6 bit , High or NC : 8 bit)	
13	STBYB	I	Standby signal.(Low : standby operation ; High : normal operation.)	
14	GND	P	Ground.	
15~16	R0- / R0+	I	LVDS differential data input.	
17	GND	P	Ground.	
18~19	R1- / R1+	I	LVDS differential data input.	
20	GND	P	Ground.	
21~22	CLK- / CLK+	I	LVDS differential clock input.	
23	GND	P	Ground.	
24~25	R2- / R2+	I	LVDS differential data input.	
26	GND	P	Ground.	
27~28	R3- / R3+	I	LVDS differential data input.	
29	GND	P	Ground.	
30	NC	-	No connection.	

I: input, O: output, P: Power

- Recommended connector : IRISO ELECTRONICS 9699 series [IMSA-9699S-30A-GFN1]
- Please be sure to check a consistency between pin assignment in "3.2 Outward Form" and your connector pin assignment when designing your circuit.
  Inconsistency in input signal assignment may cause a malfunction.
- Since FPC cable has gold plated terminals, gilt finish contact shoe connector is recommended.

2. ELECTRICAL CHARACTERISTICS

2.1 Absolute Maximum Ratings

Items	Symbol	Min.	Max.	Unit
Power supply voltage	VDD	-0.3	3.9	V
Input voltage for logic	Vi	-0.3	VDD+0.3	V
Forward current	IL1,IL2	-	40	mA
Operate temperature range	TOP	-30	85	℃
Stor ge temperature range	TST	-40	95	℃
Storage Humidity	HD	20	90	% H

2.2 DC Characteristics:

Typical Operation Conditions

Items	Symbol	Min.	Typ.	Max.	Unit	Condition
Digital Supply voltage	VDD	3.0	3.3	3.6	V	
Input Voltage for logic	VI	0	-	VDD	V	

Current Consumption

Items	Symbol	Min.	Typ.	Max.	Unit	Condition
High Level input voltage	VIH	0.7VDD	-	VDD		
Low Level input voltage	VIL	0		0.3VDD		
Pull up / down resistor	RI	200	350	850	KΩ	IM,STBYB
		100	175	425	KΩ	UL/DR
Current for Driver	I_{VDD}	-	38	76	mA	VDD = 3.3V

2.3 Back-light only Specification :

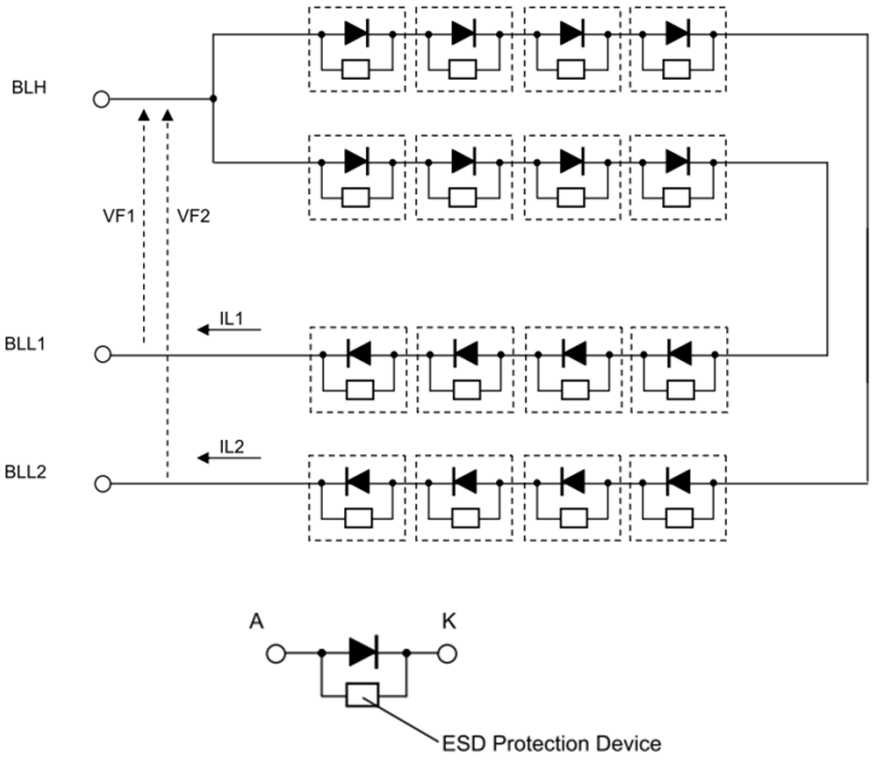
PARAMETER	SYMBOL	MIN	TYP	MAX	Unit	Test Condition	Remark
Voltage for LED backlight	VF1	-	18.5	19.8	V	BLH,BLL1	
	VF2		18.5	19.8	V	BLH,BLL2	
Current for LED backlight	IL1	-	20	40	mA	BLH,BLL1	
	IL2					BLH,BLL2	

Note1: - Please control so that each current does not vary (IL1 = IL2).

Note2: - The lifetime of the LED is defined as a period till the brightness of the LED decreases to the half of its initial value.

- This figure is given as a reference purpose only, and not as a guarantee.
- This figure is estimated for an LED operating alone.
  As the performance of an LED may differ when assembled as a monitor together with a TFT panel due to different environmental temperature.
- Estimated lifetime could vary on a different temperature and usually higher temperature could reduce the life significantly.

LED Circuit

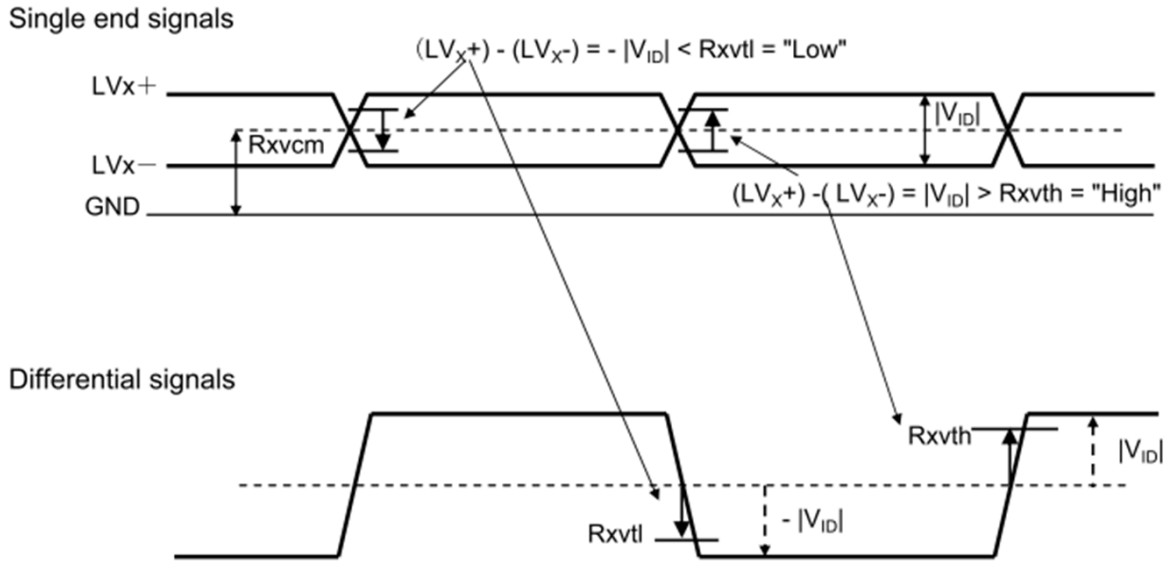


* It is recommended to control currents of BLL1 / BLL2 to equal current values (IL1 = IL2).

2.4 LVDS DC Characteristics

(Unless otherwise noted, Ta=25 °C,VDD=3.3V,GND=0V)

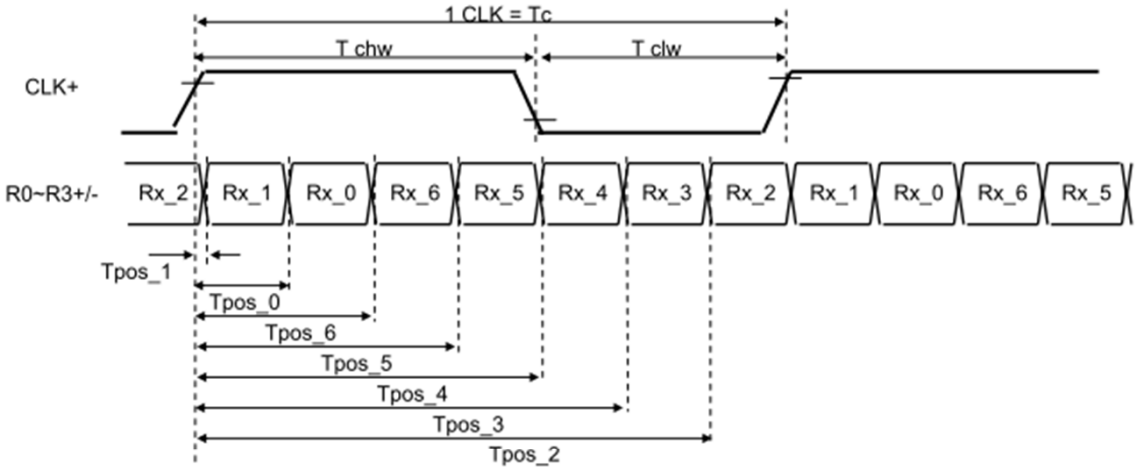
Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
Differential input high threshold voltage	Rxvth	Rxvcm=1.2V	-	-	0.1	V	CLK+、CLK- R0+、R0-、R1+、R1- R2+、R2-、R3+、R3-
Differential input low threshold voltage	Rxvtl		-0.1	-	-	V	
Differential input common Mode voltage	Rxvcm		1.0	1.2	1.8-|V_{ID}|/2	V	
Differential input voltage	|V_{ID}|		0.2	-	0.6	V	
Differential input leakage current	RV_{leak}		-10	-	+10	μA	

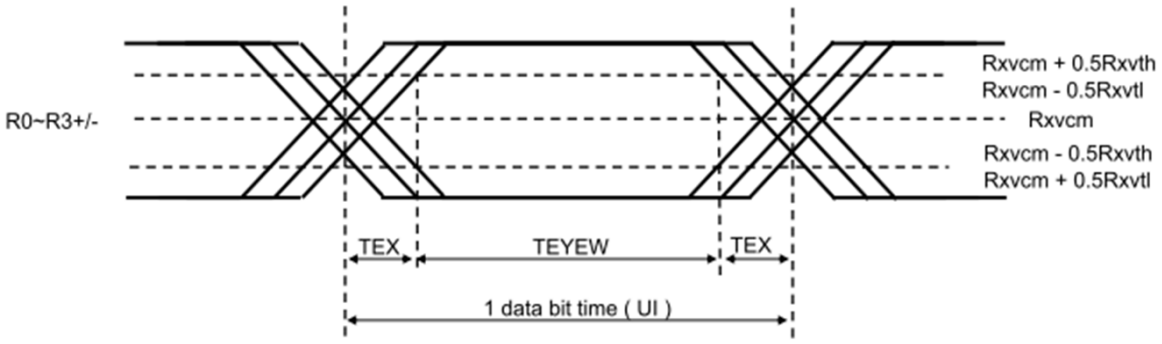


2.5 LVDS AC Characteristics

(Unless otherwise noted, Ta=25 °C,VDD=3.3V,GND=0V)

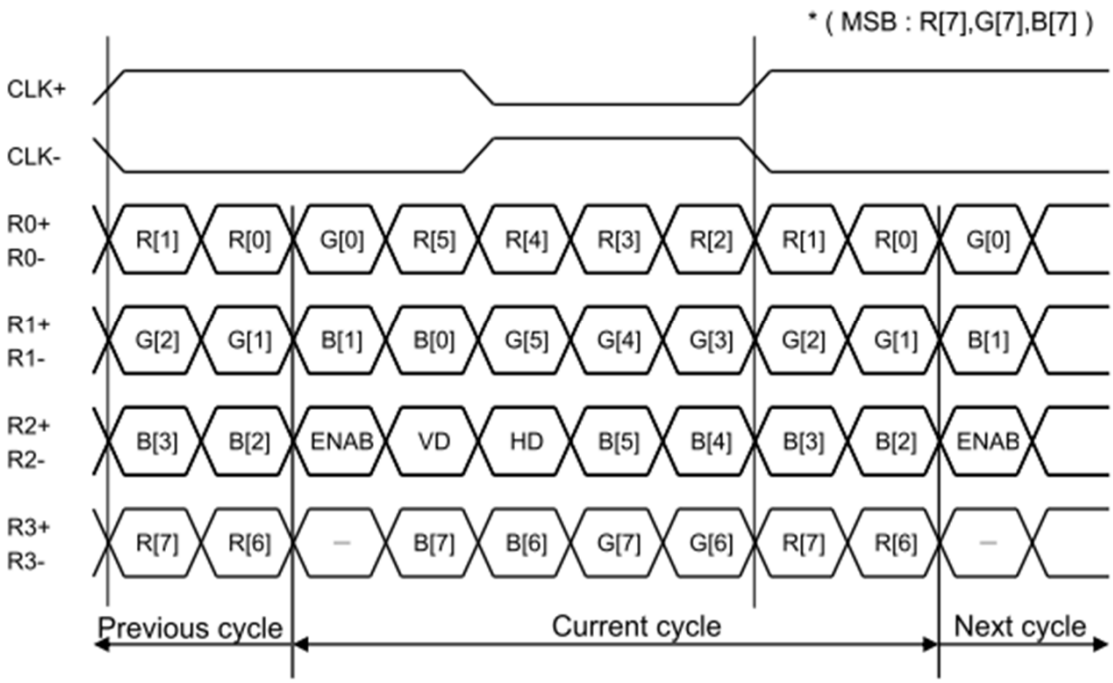
Item	Symbol	Rating			Unit	
		MIN	TYP	MAX		
CLK Frequency	f clk	25.2	27.2	30.5	MHz	
Clock period	Tc	32.8	36.8	39.7	ns	
1 data bit time	UI	-	1/7	-	Tc	
CLK High level Width	T chw	2.9	4	4.1	UI	
CLK Low level Width	T clw	2.9	3	4.1	UI	
Position 1	Tpos_1	-0.2	0	0.2	UI	
Position 0	Tpos_0	0.8	1	1.2	UI	
Position 6	Tpos_6	1.8	2	2.2	UI	
Position 5	Tpos_5	2.8	3	3.2	UI	
Position 4	Tpos_4	3.8	4	4.2	UI	
Position 3	Tpos_3	4.8	5	5.2	UI	
Position 2	Tpos_2	5.8	6	6.2	UI	
Reciever Strobe Position 7	TEYEW	0.6	-	-	UI	
Reciever Strobe Position 8	TEX	-	-	0.2	UI	



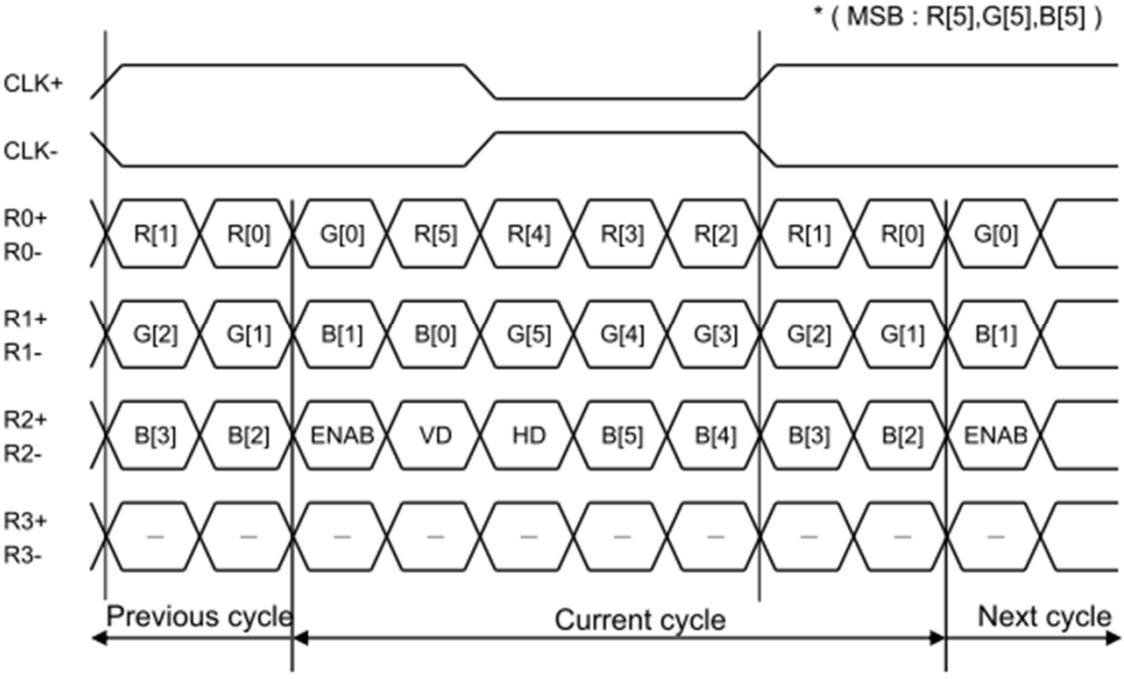


2.6 LVDS Data Format

VESA Format 8bit



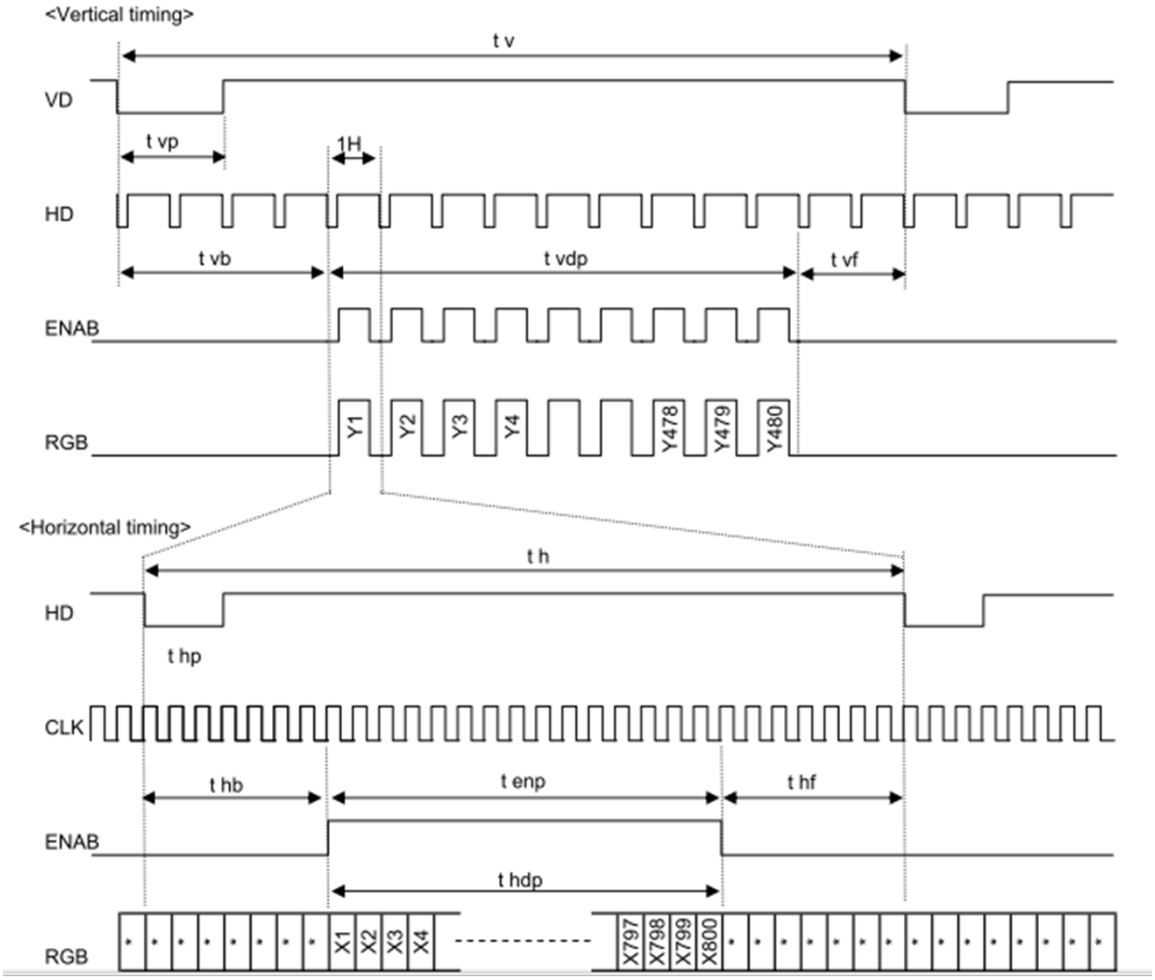
VESA Format 6bit



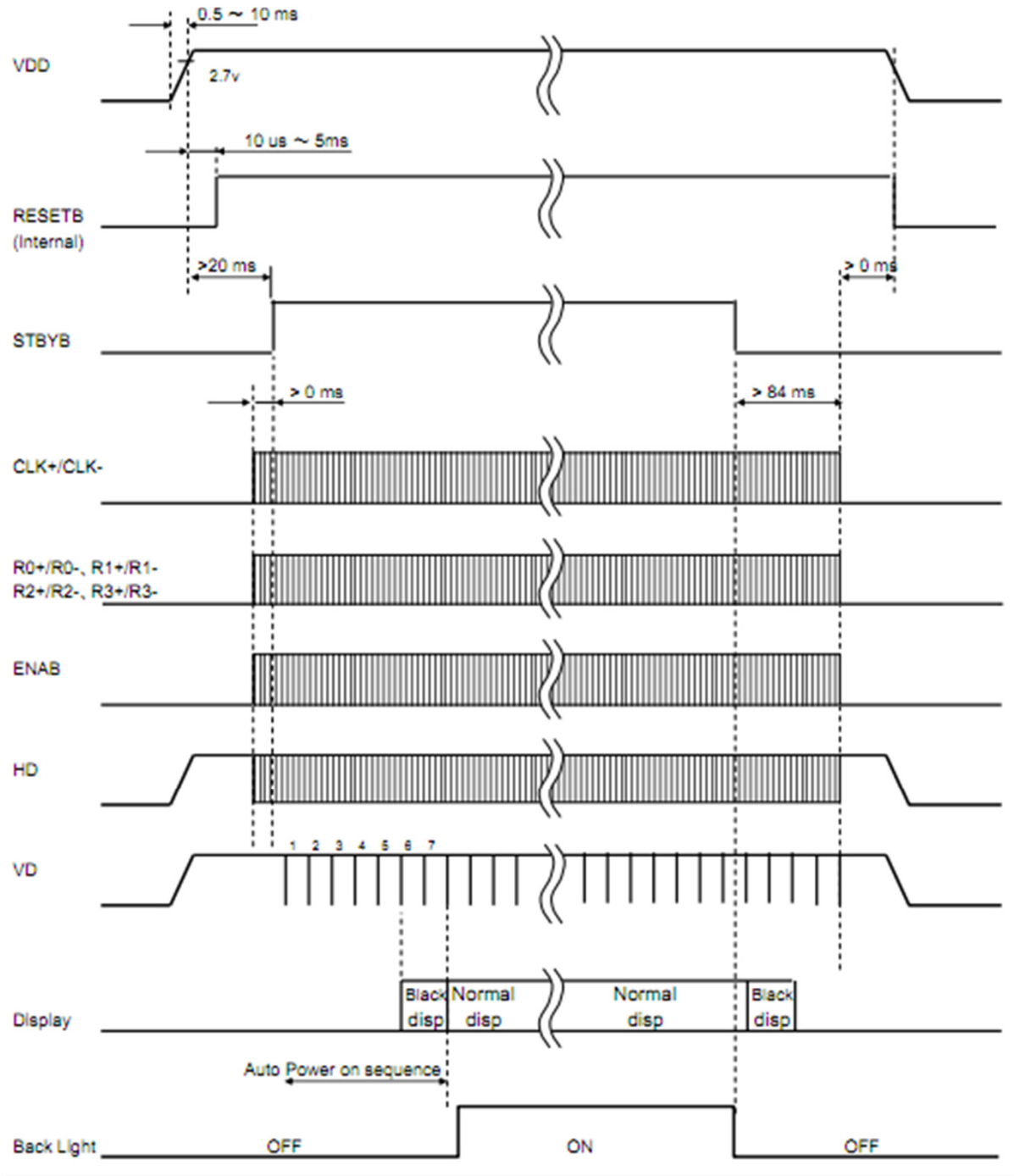
2.7 Input timing

Item	Symbol	Rating			Unit	Signal (*)
		MIN	TYP	MAX		
CLK frequency	fCLK	25.2	27.2	30.5	MHz	CLK
VD frequency	fVD	-	60	-	Hz	VD
1 vertical field	tv	490	528	552	H	
VD pulse width	tvp	1	2	66	H	
VD back porch	tvb	5	10	67	H	VD,HD,ENAB
VD front porch	tvf	5	38	67	H	R[7:0],G[7:0],B[7:0]
Vertical valid data	tvdp		480		H	
HD frequency	fHD	-	28.8	-	kHz	HD
1 horizontal field	th	856	860	920	CLK	
HD pulse width	thp	1	2	100	CLK	
HD back porch	thb	5	16	101	CLK	CLK,HD,ENAB
HD front porch	thf	19	44	115	CLK	R[7:0],G[7:0],B[7:0]
ENAB pulse width	tenp		800		CLK	
Horizontal valid data	thdp		800		CLK	

(*) Input terminals are (R0+/- , R1+/- , R2+/- , R3+/- , CLK+/-).



2.8 Power On/OFF sequence



Note : ENAB , HD and VD are include in the R2+ / R2- terminals.

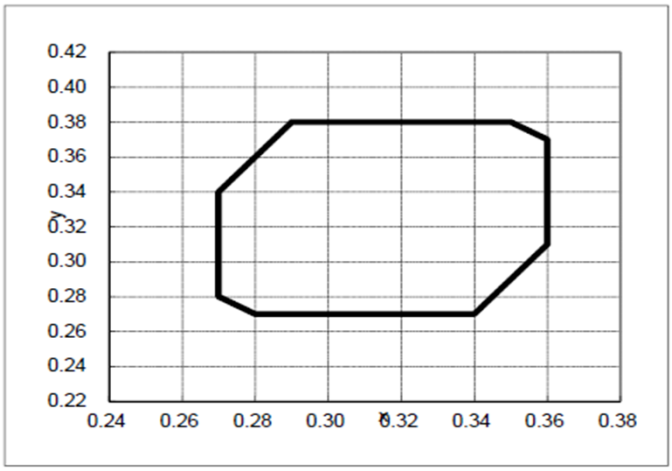
3. OPTICAL CHARACTERISTICS

3.1 Characteristics

Electrical and Optical Characteristics

Measured temperature: Ta=25° C

No.	Item			symbol / temp.		Min.	Typ.	Max.	Unit	Note
1	Response Time			Tr+Tf	25 ℃	-	50	100	ms	2
2	Viewing Angle	Hor.	Cr≧10	θ_{2+}	Φ = 0°	80	-	-	degree	3
				θ_{2-}	Φ = 180°	80	-	-		
		Ver.		θ_{1+}	Φ = 270°	80	-	-		
				θ_{1-}	Φ = 90°	80	-	-		
3	Contrast Ratio			Cr	25 ℃	350	700	-	-	4
4	White x-code			Wx	25 ℃	White Chromaticity Range			-	5
	White y-code			Wy						
	Brightness			Y		240	400	-	cd/m^2	
5	Brightness Uniformity				25 ℃	70	-	-	%	6



White Chromaticity Range

【White Chromaticity Range】

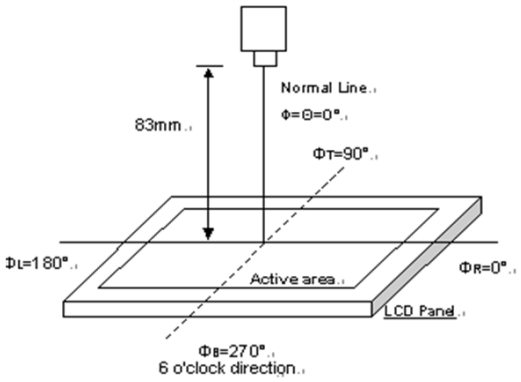
x	y
0.29	0.38
0.27	0.34
0.27	0.28
0.28	0.27
0.34	0.27
0.36	0.31
0.36	0.37
0.35	0.38

3.2 Temperature Characteristics

Item		Symbol	Specification			Remark
			MIN	TYP	MAX	
Contrast ratio		CR	200	-	-	Ta=-30°C Backlight ON
			200	-	-	Ta=85°C Backlight ON
Response time	Rise time + Fall time	TON + TOFF	-	980ms	1500ms	Ta=-30°C
			-	40ms	80ms	Ta=85°C
Display Quality			No noticeable display defect or ununiformity should be observed.			

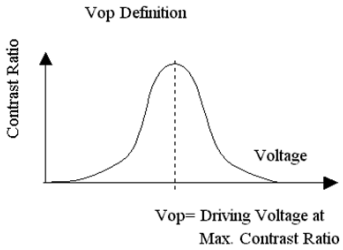
3.3 Definition of optical characteristics

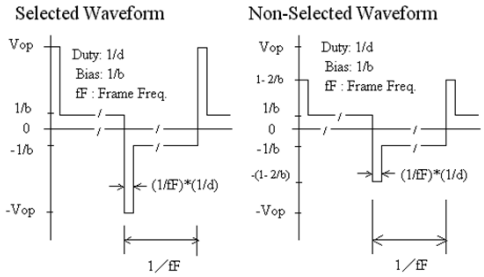
Measurement condition : Transmissive mode optical measurement system



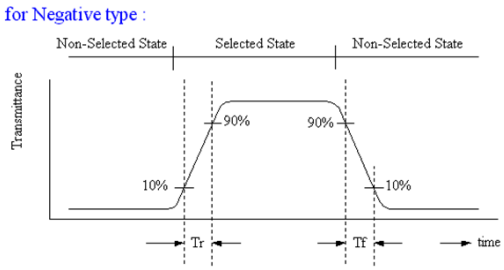
LCD Evaluation System : DMS-803
Light Source : Halogen Lamp.

[Note 1] Definition of LCD Driving Vop and Waveform :

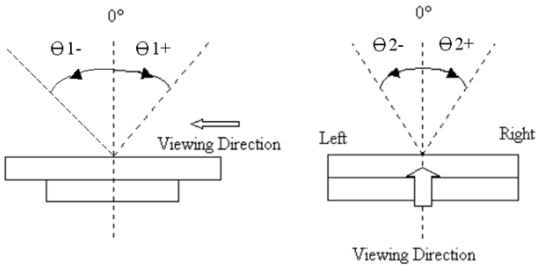




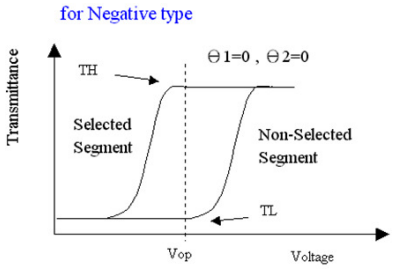
[Note 2] Definition of Response Time



[Note 3] Definition of Viewing Angle :

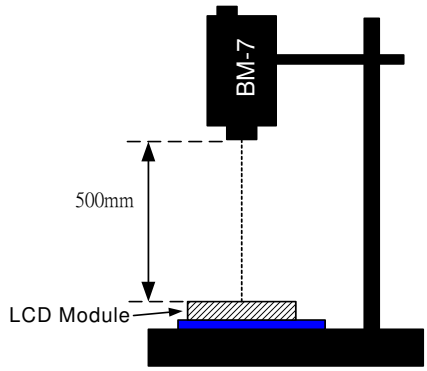


[Note 4] Definition of Contrast Ratio :

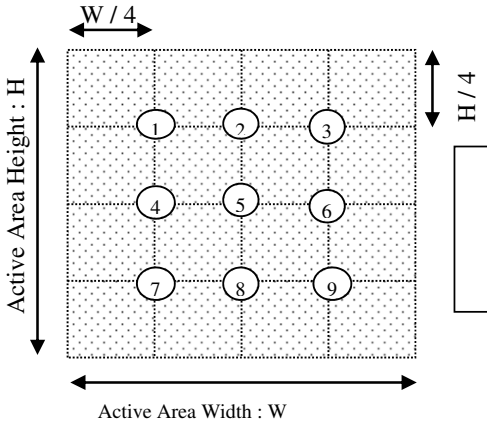


$$\text{Contrast Ratio} = \frac{TH}{TL}$$

[Note 5] Definition of measurement of Color Chromaticity and Brightness

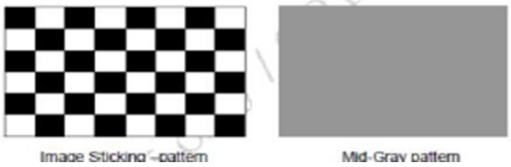


[Note 6] Definition of Brightness Uniformity



$$\text{Brightness Uniformity} = \frac{\text{Minimum Brightness of Point 1~9}}{\text{Maximum Brightness of Point 1~9}}$$

4. RELIABILITY :

Item No	Items	Condition	Note
1	High temperature operating	85 ℃ , 240 hours	1
2	Low temperature operating	-30 ℃ , 240 hours	1
3	High temperature storage	95 ℃ , 240 hours	1
4	Low temperature storage	-40 ℃ , 240 hours	1
5	High temperature & humidity storage	60℃, 90%RH, 240 hours	2
6	Thermal Shock storage	-40℃, 30min.<=> 95℃, 30min. 10 Cycles	
7	Vibration test	Total amplitude 1.5mm，f：10~55Hz X，Y，Z directions for each 30mins.	
8	Electrostatic discharge test (Non operation)	C：200pF，R：0Ω，V：±200V Each 3 times of discharge on and power supply and other terminals.	
9	Surface discharge test (Non operation)	C：250pF，R：100Ω，V：±12KV Each 3 times of discharge on and power supply and other terminals.	
10	Packing drop test	Drop from 75cm high 1 time to each 6 surfaces，3 edges，1 corner.	
11	Image Sticking Test	25 ± 2°C Operation with test pattern sustained for 2 hrs, then change to gray pattern immediately. After 5 mins, the mura must be disappeared completely  Image Sticking pattern　Mid-Gray pattern	

Note 2：The product move into the room temperature for at least 24 hours with no condensation.

Note 3 : Please change the display picture (autorun) during operating mode. Avoid displaying static images to avoid image sticking , and the image sticking is accelerated by temperature.

Note 4: In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.

* One single product test for only one item.

* Judgment after test : keep in room temperature for more than 2 hours.

- Current consumption < 2 times of initial value
- Function : work normally

5. PRODUCT HANDLING AND APPLICATION

5.1 PRECAUTION FOR HANDLING LCM

- The LCD module contains a C-MOS LSI. People who operate the LCM should wear ESD protection equipment to prevent ESD hurt on products.
- Do not input any signal before power is turned on.
- Do not take LCM from its packaging bag until it is assembled.
- Peel off the LCM protective film slowly since static electricity may be generated.
- Hand Soldering : Soldering temperature less than 260℃,within 5 sec, at 5 mm. Away from pin connection.
- Do not touch the display surface or connection terminals area with bare hands. Smudges on the display surface reduce the insulation between terminals.
- Do not twist or bend the modules and also avoid any inappropriate external force on display surface during assembly.
- Do not expose LCM to organic solvent. IF clean the surface , wipe it gently with soft cloth dampened by alcohol.
- Do not attempt to wiped off the contact pads.
- Keep LCM panels away from direct sunlight or fluorescent light, , also avoid them in high-temperature & high humidity environment for a long period.
- It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.
- Do not drive LCM by DC voltage & avoid displaying at certain pattern for a long time otherwise it might cause image sticking.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Never use the LCD , LCM under 45 Hz , the liquid crystal will decomposition and cause perfectly damage on display !!
- Liquid in LCM is hazardous substance. In case a contact with liquid crystal material is occurred, be sure to immediately wash such material away by soap and water.
- The polarizer is easily damaged and should be handle with special care. Don't press or rub it with hard objects.

5.2 PRECAUTION FOR STORING

- Store the module in a dark room where must keep at 25±10℃ and 65%RH or less.
- Do not store the module in surroundings containing organic solvent or corrosive gas
- Store the module in an anti-electrostatic container or bag.

5.3 USING ON MEDICAL CARE , SAFETY OR HAZARDOUS APPLICATION OR SYSTEM

- For the application in medical care, safety and hazardous products or systems, an authorization from URT is required. URT will not responsible for any damage or loss which caused by the products without any authorization given by URT.
- This product is not allowed to be designed and used for military application and/or purpose.
- The delivery of this product to the countries and/or regions where the embargoes are imposed by U.N. is prohibited.
- The application and delivery of this product must comply with Strategic High-Tech Commodities (SHTC) export control and the sales to the embargoed and/or sanctioned countries or regions are strictly prohibited.

6. DATE CODE OF PRODUCTS

- Date code will be shown on each product :

- **YY MM DD - XXXX**

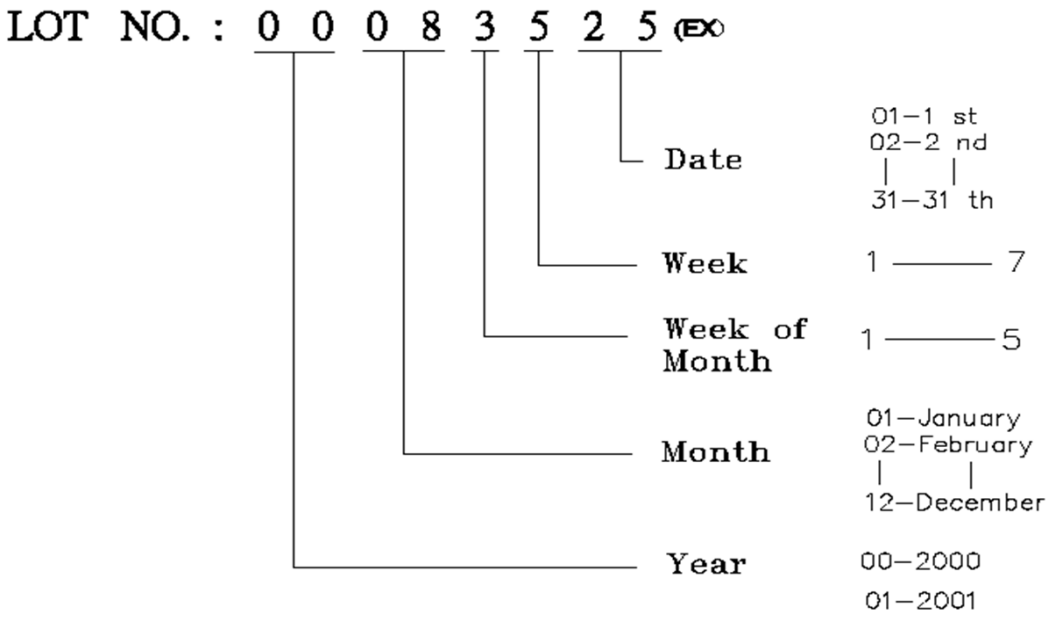
  Year Month Day - Production lots no.

- Example: 241108 - 0003 ==> Year 2024, November,8th ,
  Production lots no. 0003

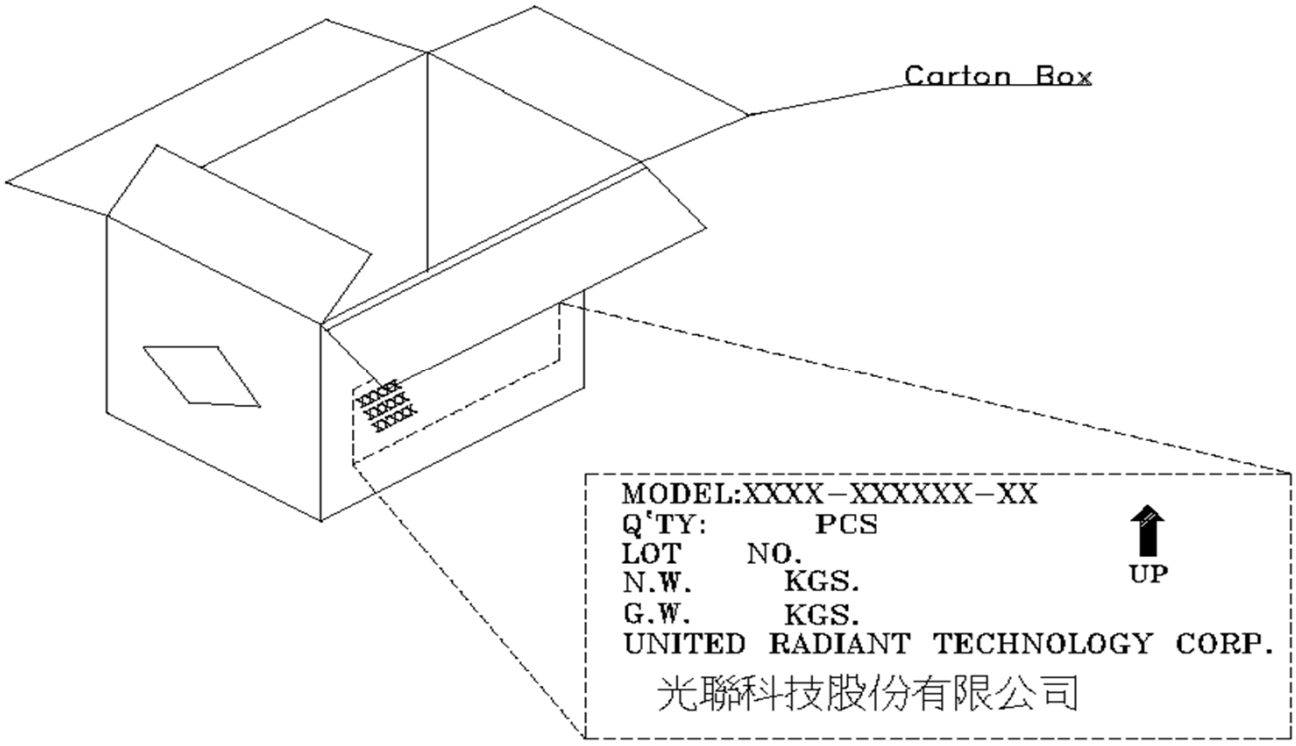
Note ： The lot no. attached on the packing box will be used for tracking once the part is too small to print the date code.

7. LOT NO

Instruction of lot number:



Lable of carton:



8. INSPECTION STANDARD

8.1. QUALITY :

THE QUALITY OF GOODS SUPPLIED TO PURCHASER SHALL COME UP TO THE FOLLOWING STANDARD.

8.1.1. THE METHOD OF PRESERVING GOODS

AFTER DELIVERY OF GOODS FROM U.R.T. TO PURCHASER. PURCHASER SHALL CONTROL THE LCM AT -10 ℃ ~40 ℃ ,AND IT MIGHT BE DESIRABLE TO KEEP AT THE NORMAL ROOM TEMPERATURE AND HUMIDITY UNTIL INCOMING INSPECTION OR THROWING INTO PROCESS LINE.

8.1.2. INCOMING INSPECTION

(A) THE METHOD OF INSPECTION

IF PURCHASER MAKE AN INCOMING INSPECTION , A SAMPLING PLAN SHALL BE APPLIED ON THE CONDITION THAT QUALITY OF ONE DELIVERY SHALL BE REGARDED AS ONE LOT.

(B) THE STANDARD OF QUALITY

ISO-2859-1 (SAME AS MIL-STD-105E) , LEVEL Ⅱ SINGLE PLAN.

(C) MEASURE

IF AS THE RESULT OF ABOVE RECEIVING INSPECTION , A LOT OUT IS DISCOVERED. PURCHASER SHALL BE INFORM SELLER OF IT WITHIN SEVEN DAYS. BUT FIRST SHIPMENT WITHIN FOURTEEN DAYS.

8.1.3. WARRANTY POLICY

U.R.T. WILL PROVIDE ONE-YEAR WARRANTY FOR THE PRODUCTS ONLY IF UNDER SPECIFICATION OPERATING CONDITIONS. U.R.T. WILL REPLACE GOOD PRODUCTS FOR THESE DEFECT PRODUCT WHICH UNDER WARRANTY PERIOD AND BELONG TO THE RESPONSIBILITY OF U.R.T.

8.1.4 Illustrate of The Inspection

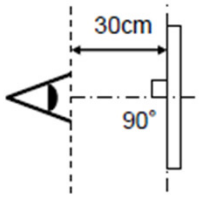
Samples are used to assist and verify compliance with specifications, not as inspection standards.
If a physical sample is required as the inspection standard, both parties shall jointly determine and sign "Inspection sample or Limited sample (with upper limit and lower limit) " .

8.2 Standard of Visual Inspection：

8.2.1 Defective Display and Screen Quailty

Test Condition: Observed TFT-LCD monitor from front during operation with the following conditions
Driving Signal: Raster Patter (RGB, white, black)

Observation distance: 30 cm
Illuminance: 200 to 350 lx



Defect item		Defect content		Criteria
Display Quality	Line defect	Black, white or color line, 3 or more neighboring defective dots		Not exists
	Dot defect	Uneven brightness on dot-by-dot base due to defective TFT or CF, or dust is counted as dot defect (brighter dot, darker dot) Dark dot: Appear dark through white display		Refer to table 1 *To be discussed and determined by both parties if any matters related to this item occurred.
Screen Quality	Dirt	Uneven brightness (white stain, black stain etc)		Invisible through 5% ND filter at Black screen. Invisible through 1% ND filter at other screen.
	Foreign particle	Point-like	0.25mm< φ	N=0
			0.20mm< φ ≦0.25mm	N≦2
			φ ≦0.20mm	Acceptable
		Liner	3.0mm<length and 0.08mm<width	N=0
			length≦3.0mm or width≦0.08mm	Acceptable
	Others			Use boundary sample for judgment when necessary

φ(mm): Average diameter = (major axis + minor axis)/2
Permissible number: N

Table 1

Judgment screen	black	gray	red	green	blue	Total
Bright dot	○	don't judge	○	○	○	0
Dark dot	don't judge	○	○	○	○	1 or less

Definition of bright dot and dark dot

List of screens	R	G	B
black	00h	00h	00h
gray	94h	94h	94h
red	94h	00h	00h
green	00h	94h	00h
blue	00h	00h	94h

8.2.2 Screen and Other Appearance

Testing conditions

Observation distance	30cm
Illuminance	1200～2000 lx

Item		Criteria	Remark
Polarizer	Flaw Stain Bubble Dust Dent	Ignore invisible defect when the backlight is on.	Applicable area: Active area only (Refer to the section 1.3 3.2 "Outward form")
S-case		No functional defect occurs	
FPC cable		No functional defect occurs	