



晶采光電科技股份有限公司  
AMPIRE CO., LTD.

# Specifications for LCD module

Customer	
Customer part no.	
Ampire part no.	AM-640480VSTZQW-06H
Approved by	
Date	

☐ Preliminary Specification

☒ Formal Specification

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Approved by	Checked by	Organized by
Patrick	Mark	Tank

\*This specification is subject to change without notice.

## RECORD OF REVISION

Revision Date	Page	Contents	Editor
2020/12/17	---	New Release	Tank
2021/01/19	1	Update Ampire Part No.	Tank
	3, 5	Update Luminance	
	10	Update Electrical Characteristic Of LED Backlight	
2021/02/02	3, 5	Update Luminance	Tank

## 1. Features

5.7 inch Amorphous-TFT-LCD (Thin Film Transistor Liquid Crystal Display) module. This TFT LCD has a 5.7 (4:3) inch diagonally measured active display area with 640x480 (640 horizontal by 480 vertical pixels) resolution. This module is composed of a 5.7" TFT-LCD panel and backlight unit.

- (1) Construction: a-Si TFT-LCD with driving system, White LED Backlight.
- (2) LCD type : IPS
- (3) Number of the Colors : 262K colors (R,G,B 6bit digital each)
- (4) Interface: 18 Bit TTL RGB interfaces. 33 pin.
- (5) LCD Power Supply Voltage: 3.3V single power input, built-in power supply circuit.

## 2. Physical Specifications

Item	Specifications	unit
Display size (diagonal)	5.7	inch
Resolution	640 (W) x RGB x 480 (H)	dot
Pixel pitch	0.18 (W) x 0.18 (H)	mm
Active Area	115.2 (W) x 86.4 (H)	mm
Color configuration	R.G.B Vertical stripe	
Overall Dimension	144.0 (W) x 104.6 (H) x 12.3 (D)	mm
Display Mode	Normally Black	
Brightness	750	cd/m <sup>2</sup>
Contrast ratio	1200 : 1	
Backlight unit	LED	
Display color	262,144	colors

### 3. Absolute Maximum Ratings

#### 3.1 Electrical Absolute max. ratings

Item	Symbol	Condition	Min.	Max.	Unit	Remark
Power voltage	VCC	GND=0	-0.3	3.96	V	
Logic Input Voltage Range	VIN	GND=0	-0.3	VCC+0.3	V	Note(1)

Note(1) HSYNC, VSYNC, ENAB, CK, R0~R5, G0~G5, B0~B5.

Note(2) The following are maximum values which, if exceeded, may cause operation or damage to the unit.

#### 3.2 Environmental Absolute Maximum Ratings

Item	Operating		Storage		Remark
	Min.	Max.	Min.	Max.	
Temperature	-30	85	-30	85	Note(2),(3), (4),(5),(6),(7)
Humidity	Note(1)		Note(1)		
Corrosive Gas	Not Acceptable		Not Acceptable		

Note(1) Ambient temperature Temp.  $\leq 60^{\circ}\text{C}$  : 90% RH max

Note(2) For storage condition Ta at  $-30^{\circ}\text{C} < 240\text{h}$  , at  $85^{\circ}\text{C} < 240\text{h}$

Note(3) For operating condition Ta at  $-30^{\circ}\text{C} < 240\text{h}$  , at  $85^{\circ}\text{C} < 240\text{h}$

Note(4) Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note(5) The response time will be slower at low temperature.

Note(6) Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at  $+25^{\circ}\text{C}$

Note(7) When LCM panel is operated over  $60^{\circ}\text{C}$  ( center of the panel surface temperature), the IAK of the LED back-light should be adjusted to 105 mA

Note(8) This is center of the panel surface temperature, not ambient temperature.

Note(9) At  $25^{\circ}\text{C}$

## 4. Optical Characteristics

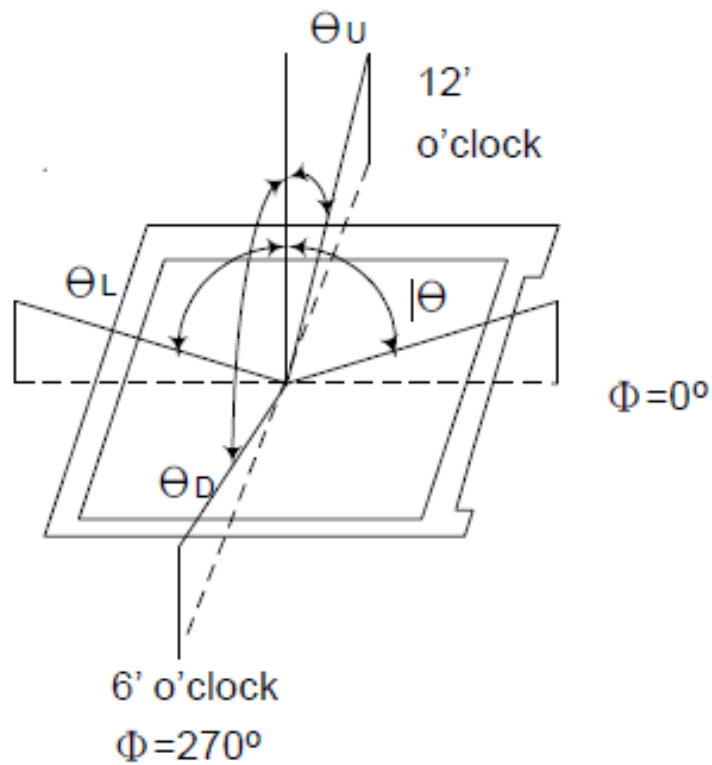
Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Viewing Angle	Hor.	$\theta$ U	$CR \geq 10$	75	85	--	deg.	(1),(4)
		$\theta$ D		75	85	--		
	Ver.	$\theta$ L		75	85	--		
		$\theta$ R		75	85	--		
Contrast ratio		CR	$\Theta = \Phi = 0^\circ$	800	1200	--	--	(1),(2)
Response Time		$T_R + T_F$	$\Theta = \Phi = 0^\circ$	--	30	45	msec	(1),(3)
NTSC		(%)		55	60	--	%	
Color chromaticity	Red	R <sub>x</sub>	$\Theta = \Phi = 0^\circ$	Typ. -0.05	0.630	Typ. +0.05	--	(1),(4),(5)
		R <sub>y</sub>			0.312			
	Green	G <sub>x</sub>			0.298			
		G <sub>y</sub>			0.593			
	Blue	B <sub>x</sub>			0.147			
		B <sub>y</sub>			0.115			
	White	W <sub>x</sub>			0.312			
		W <sub>y</sub>			0.356			
Luminance (I <sub>AK</sub> =140mA)		L	$\Theta = \Phi = 0^\circ$	600	750	--	cd/m <sup>2</sup>	(1),(6)
Luminance Uniformity		$\Delta L$	$\Theta = \Phi = 0^\circ$	70	-	-	%	(7)

### Measuring Condition

Ta=25°C. To be measured on the center area of panel after 10 minutes operation. LED Back-light  $I_{AK} = 140mA$

- Measuring surrounding : Dark room
- Ambient temperature :  $25 \pm 2^\circ C$
- 15min. Warm-up time.

### Note(1) Definition of Viewing Angle

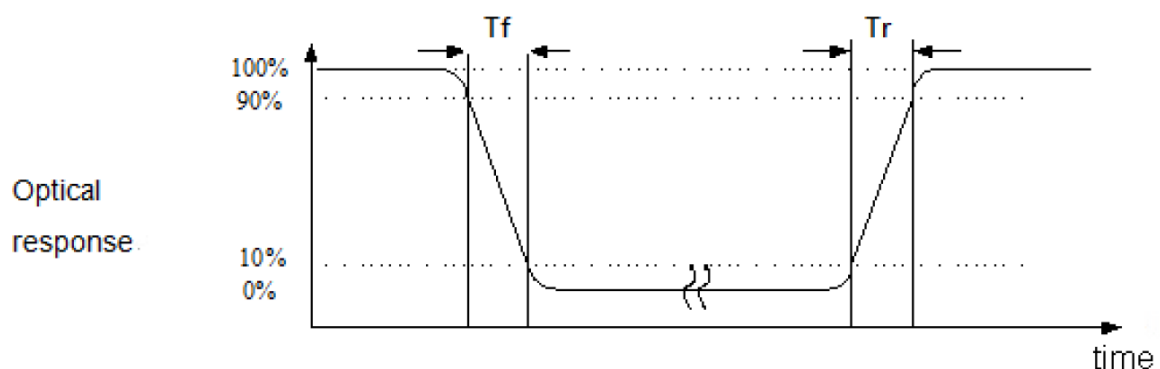


Note(2) Definition of Contrast Ratio (CR) :

Contrast ratio is calculated with the following formula.

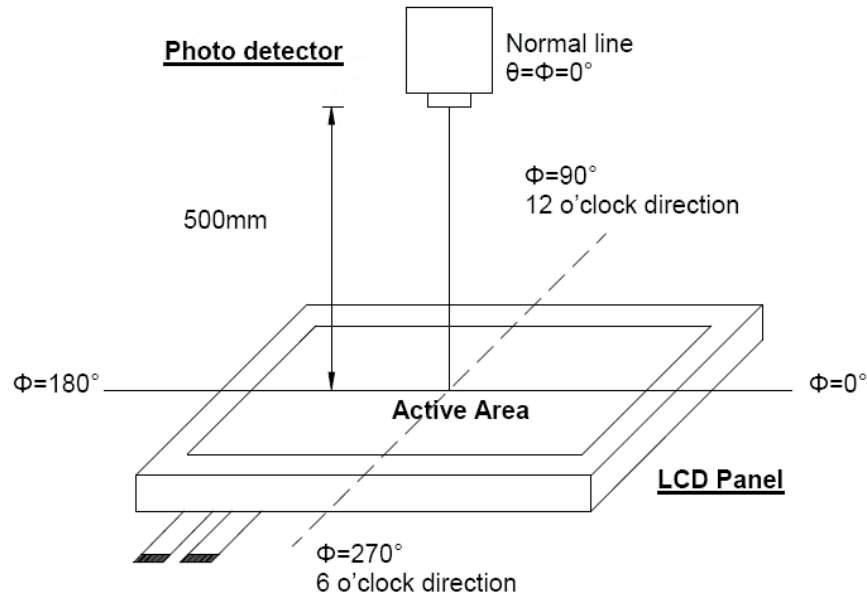
$$\text{Contrast ratio (CR)} = \frac{\text{Photo detector output when LCD is at "White" state}}{\text{Photo detector Output when LCD is at "Black" state}}$$

Note(3) Definition of Response Time : Sum of TR and TF



Note(4) Definition of optical measurement setup

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view:  $1^\circ$  / Height: 500mm.)

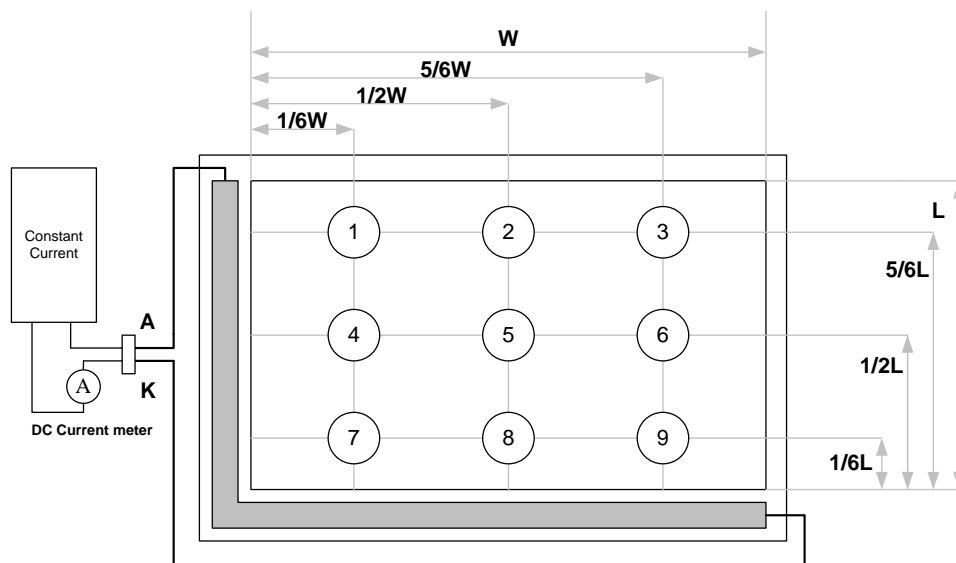


Note(5) Definition of color chromaticity (CIE1931)

Color coordinated measured at center point of LCD.

All input terminals LCD panel must be ground when measuring the center area of the panel.

Note(6) Luminance is measured at point 5 of the display.



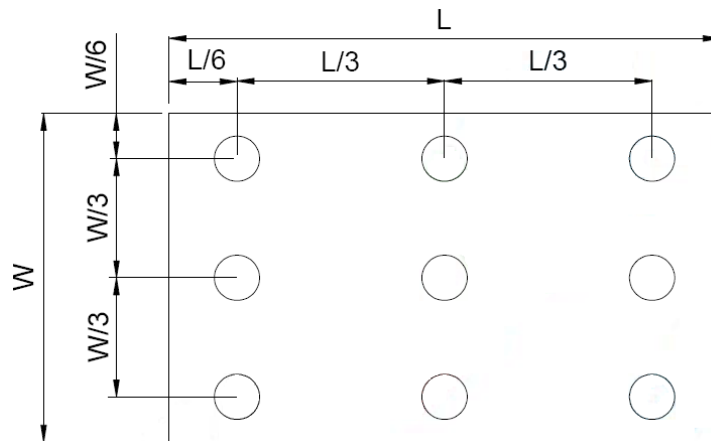
Note(7) Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to bellow figure).

Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{\min}}{B_{\max}}$$

L ----- Active area length      W ----- Active area width



B<sub>max</sub>: The measured maximum luminance of all measurement position.

B<sub>min</sub>: The measured minimum luminance of all measurement position.



## 5. Electrical Characteristics

### 5.1 DC Characteristics

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Power supply voltage		VCC	3.0	--	3.6	V	
Input voltage for logic	H Level	V <sub>IH</sub>	0.7* VCC	--	VCC	V	(1)
	L Level	V <sub>IL</sub>	GND	--	0.3* VCC	V	

Note(1) HSYNC, VSYNC, ENAB, CK, R0~R5, G0~G5, B0~B5.

Note(2) fV =60Hz , Ta=25°C , Display pattern : All White.

Note(3) \*:Will be reference only

## 5.2 Electrical Characteristic Of LED Backlight

Item	Symbol	Min.	Typ.	Max.	Unit	Note
LED Forward Voltage	$V_{AK}$	8.5	--	10.8	V	$I_{AK}=140\text{mA}$ , $T_a=25^\circ\text{C}$
LED Forward Current	$I_{AK}$	--	140	--	mA	$T_a=25^\circ\text{C}$
LED life time			50K	-	Hrs.	$I_{AK}=140\text{mA}$ , $T_a=25^\circ\text{C}$

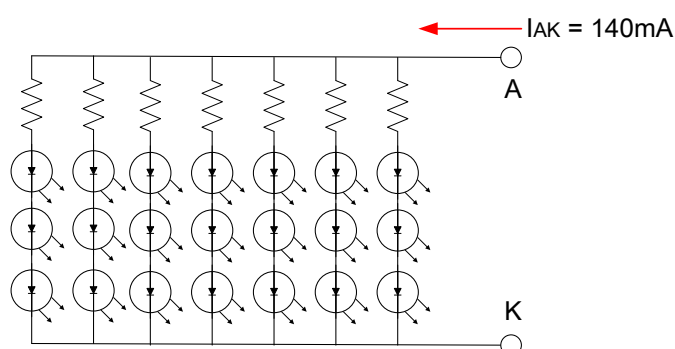
Note(1)  $T_a$  means ambient temperature of TFT-LCD module.

Note(2) If the module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

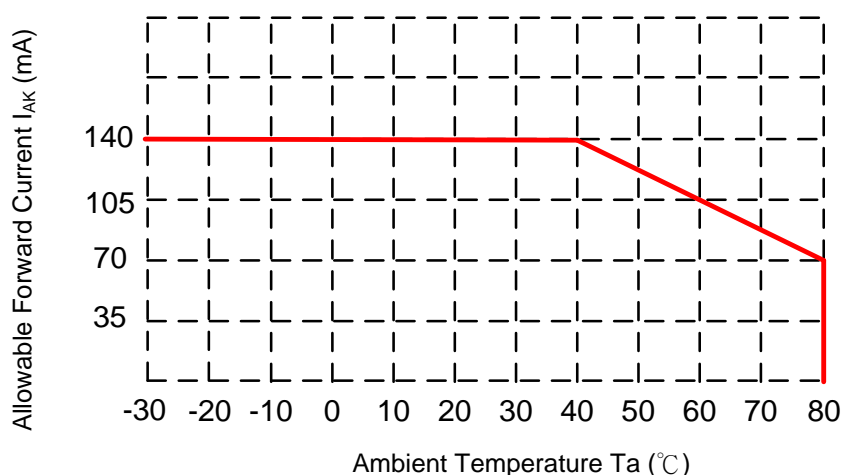
Note(3) The constant current source is needed for LED back-light driving.

Note(4) Operating life means brightness goes down to 50% minimum brightness.  
LED life time is estimated data.  $T_a=25^\circ\text{C}$

Note(5) The structure of LED B/L shows as below.



Note(6) When LCM is operated over  $60^\circ\text{C}$  ambient temperature, the  $I_{AK}$  of the LED backlight should be adjusted to 105 mA max



## 6. Interface Pin Assignment

### CN1: TTL Interface

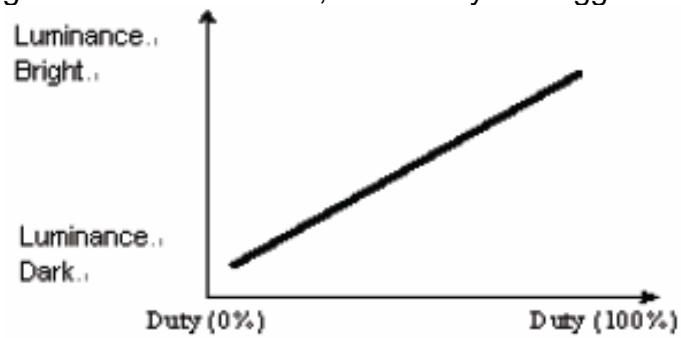
Pin no	Symbol	I/O	Description	Remark
1	GND	I	Ground	
2	CK	I	Clock signal. Latching data at the rising edge	
3	Hsync	I	Horizontal sync input in digital RGB mode	
4	Vsync	I	Vertical sync input in digital RGB mode	
5	GND	I	Ground	
6	R0	I	Red data	
7	R1	I		
8	R2	I		
9	R3	I		
10	R4	I		
11	R5	I		
12	GND	I	Ground	
13	G0	I	Green data	
14	G1	I		
15	G2	I		
16	G3	I		
17	G4	I		
18	G5	I		
19	GND	I	Ground	
20	B0	I	Blue data	
21	B1	I		
22	B2	I		
23	B3	I		
24	B4	I		
25	B5	I		
26	GND	I	Ground	
27	ENAB	I	Input data enable control	
28	VCC	I	+3.3V Power Supply	
29	VCC	I		
30	R/L	I	Right / Left Reverses Mode	
31	U/D	I	Up / Down Reverses Mode	
32	NC	I	No Connection	
33	GND	I	Ground	

### CN2: Backlight interface

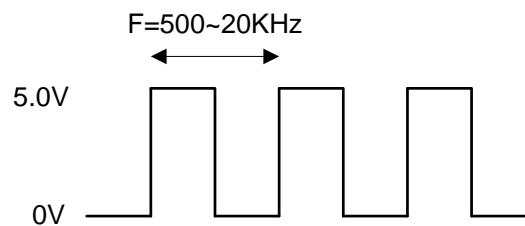
1	12V	LED drive circuit power supply (12V)
2	12V	LED drive circuit power supply (12V)
3	GND	Ground
4	GND	Ground
5	PWM	PWM Dimmer
6	LEDO	LED OPEN [normal: High(5V), open error: Low]

NOTE:

1. ADJ adjust brightness to control Pin , Pulse duty the bigger the brighter.



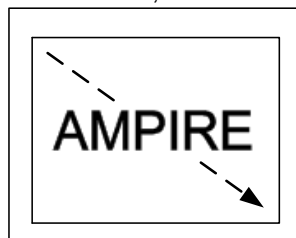
2. ADJ signal = 0 ~ 5.0V , operation frequency : 500Hz~20KHz



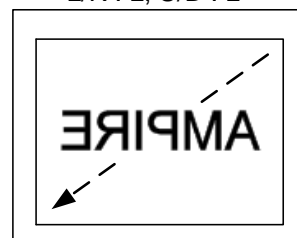
3. U/D and L/R are controlled function

L/R	U/D	Function
1	0	Normally display
0	0	Left and Right opposite
1	1	Up and Down opposite
0	1	Left and Right opposite , Up and Down opposite

L/R : H, U/D : L



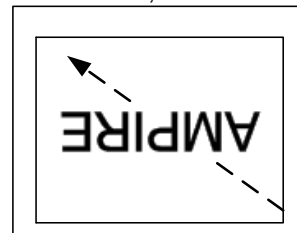
L/R : L, U/D : L



L/R : H, U/D : H



L/R : L, U/D : H



## 7. Interface Timing

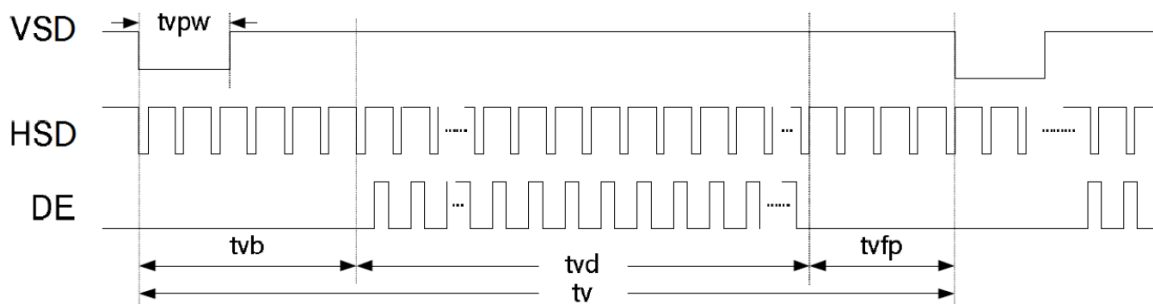
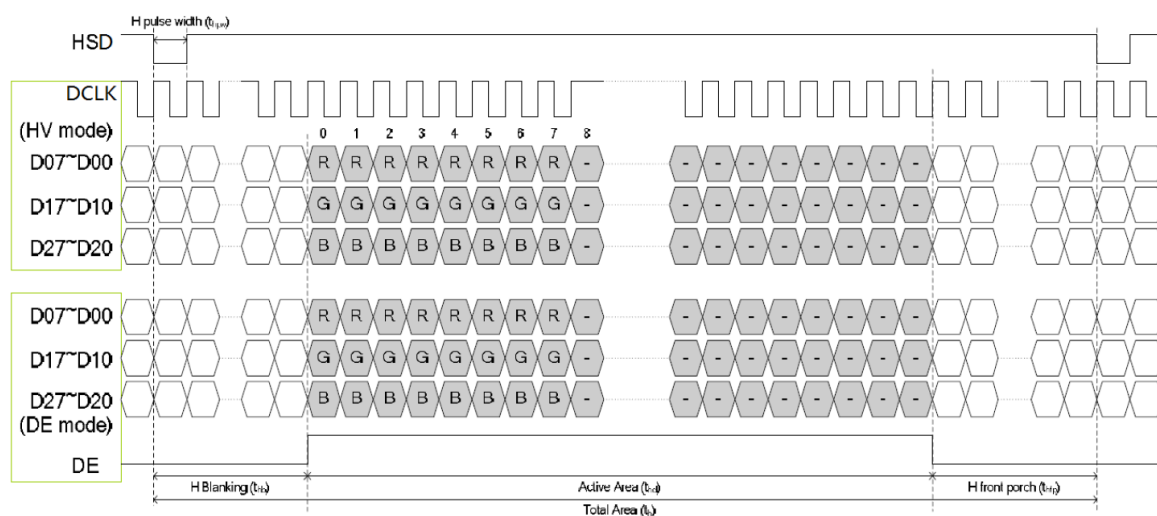
### AC electrical characteristics

Parameter Symbol		SPEC			Unit
		Min	Typ	Max	
HSD setup time	T <sub>hst</sub>	8	-	-	ns
HSD hold time	T <sub>hhd</sub>	8	-	-	ns
VSD setup time	T <sub>vst</sub>	8	-	-	ns
VSD hold time	T <sub>vhd</sub>	8	-	-	ns
Data setup time	T <sub>dsu</sub>	8	-	-	ns
Data hold time	T <sub>dhd</sub>	8	-	-	ns
DE setup time	T <sub>esu</sub>	8	-	-	ns
DE hold time	T <sub>ehd</sub>	8	-	-	ns
VDD Power On Slew rate	T <sub>POR</sub>	-	-	20	ms
RSTB pulse width	T <sub>Rst</sub>	10	-	-	us
CLKIN cycle time	T <sub>cph</sub>	20	-	-	ns
CLKIN pulse duty	T <sub>cwh</sub>	40	50	60	%
Output stable time	T <sub>sst</sub>	-	-	6	us

## Timing for RGB Interface

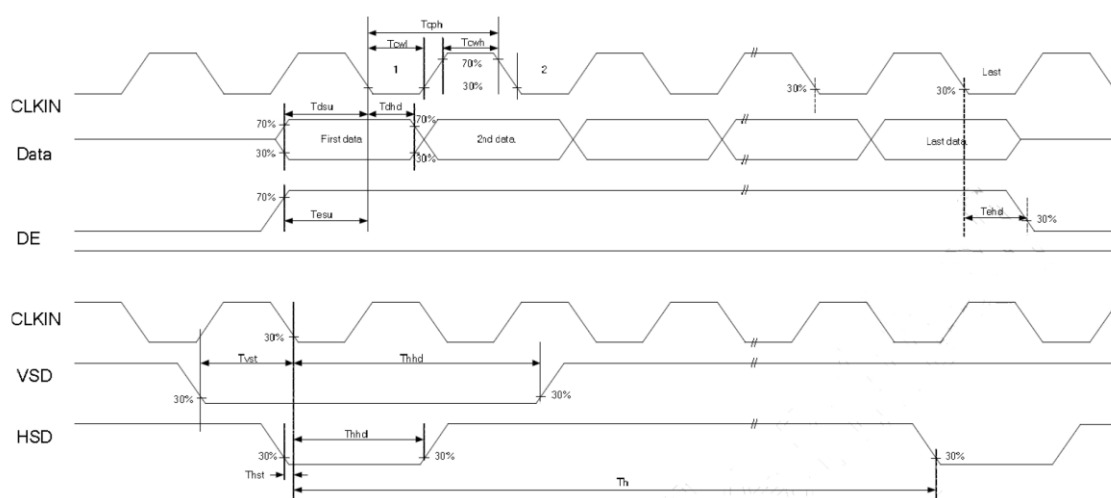
VDD=3.3V, VSS=0V, Ta=25°C

Parameter	Symbol	Min	Typ	Max	Unit	Remark
DCLK frequency	fclk	--	24	50	MHz	
Horizontal display area	thd	640			DCLK	
One Horizontal Line	th	--	760	--	DCLK	
HSD pulse width	thpw	1	48	255	DCLK	
HSD Back Porch(blanking)	thb	88			DCLK	
HSD Front Porch	thfp	1	32	255	DCLK	
DE Mode Blanking	th-thd	85	120	512	DCLK	
Vertical display area	tvd	480			TH	
VSD period time	tv	513	525	767	TH	
VSD pulse width	tpw	3	3	255		
VSD Back Porch(blanking)	tvb	32			TH	
VSD Front Porch	tvfp	1	13	255	TH	
DE Mode Blanking	tv-tvd	4	45	255	TH	



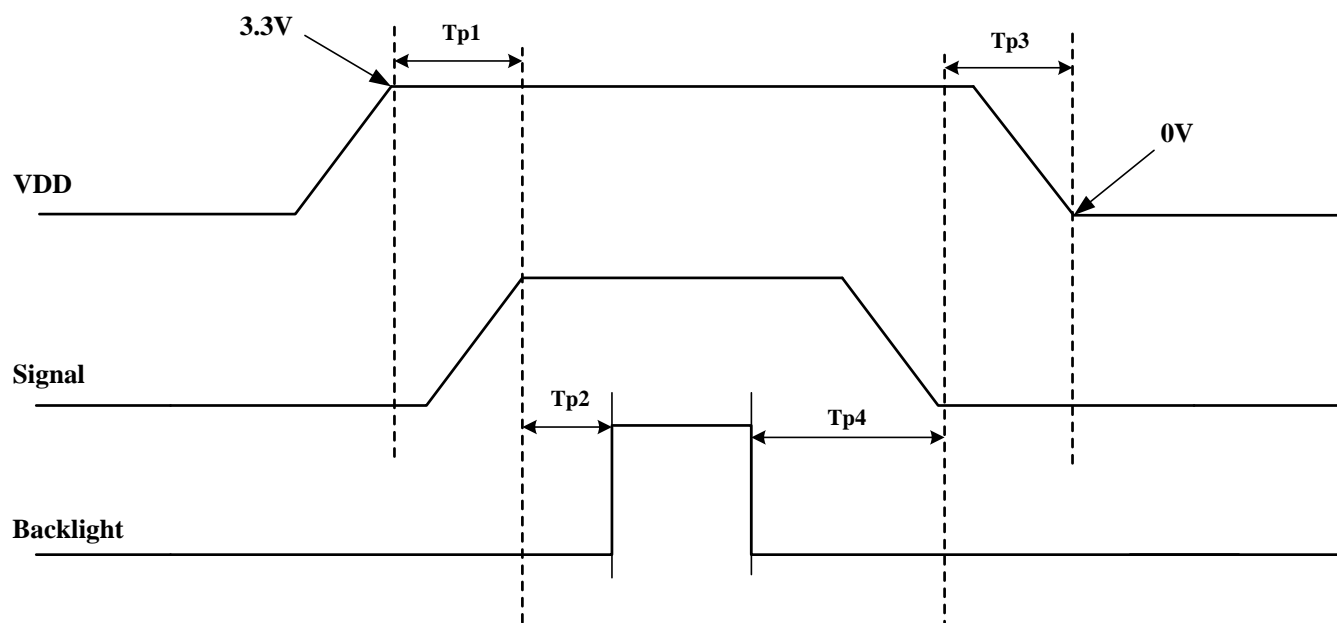
## Input Clock and Data timing Diagram

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLKIN Frequency	Fclk	-	40	50	MHz	VDD=3.0V~3.6V
CLKIN Cycle Time	Tclk	20	25	-	ns	-
CLKIN Pulse Duty	Tcwh	40	50	60	%	Tclk
Time from HSD to Source Output	Thso		64		CLKIN	-
Time from HSD to LD	Thld		64		CLKIN	-
Time from HSD to STV	Thstv		2		CLKIN	-
Time from HSD to CKV	Thckv		20		CLKIN	-
Time from HSD to OEV	Thoev		4		CLKIN	-
LD Pulse Width	Twld		10		CLKIN	-
CKV Pulse Width	Twckv		66		CLKIN	-
OEV Pulse Width	Twoev		74		CLKIN	-



Input Clock and Data Timing Diagram

## 8. Power On/Off Sequence



Item	Symbol	Value			Units	Remark
		Min.	Typ.	Max.		
VDD on to signal starting	Tp1	5	-	50	ms	
Signal starting to backlight on	Tp2	150	-	-	ms	
Signal off to VDD off	Tp3	5	-	50	ms	
Backlight off to signal off	Tp4	150	-	-	ms	



## 9. Displayed Color and Input Data

### Data Signal

COLOR		INPUT DATA																							
		R DATA								G DATA								B DATA							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB							LSB	MSB							LSB	MSB							LSB
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
RED	RED(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
GREEN	GREEN(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	GREEN(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	GREEN(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
BLUE	BLUE(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	BLUE(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0
	BLUE(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

## 10. Reliability Test Conditions

Test Item	Test Conditions	Note
High Temperature Operation	85±3°C , t=240 hrs	
Low Temperature Operation	-30±3°C , t=240 hrs	
High Temperature Storage	85±3°C , t=240 hrs	(1),(2)
Low Temperature Storage	-30±3°C , t=240 hrs	(1),(2)
Storage Humidity Test	60 °C, Humidity 90%, 240 hrs	(1),(2)
Vibration Test (Packing)	Sweep frequency : 10 ~ 50 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	(2)

Note(1) Condensation of water is not permitted on the module.

Note(2) The module should be inspired after 1 hour storage in normal conditions (15~35°C , 45~65%RH).

Note(3) The module shouldn't be tested over one condition, and all the tests are independent.

Note(4) All reliability tests should be done without the protective film.

Definitions of life end point:

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of initial value.

## **11. Use Precautions**

### **11.1 Handling precautions**

- (1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- (2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- (3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- (4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

### **11.2 Installing precautions**

- (1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx.  $1\text{M}\Omega$  and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- (2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- (3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- (4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

### 11.3 Storage precautions

- (1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- (2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- (3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

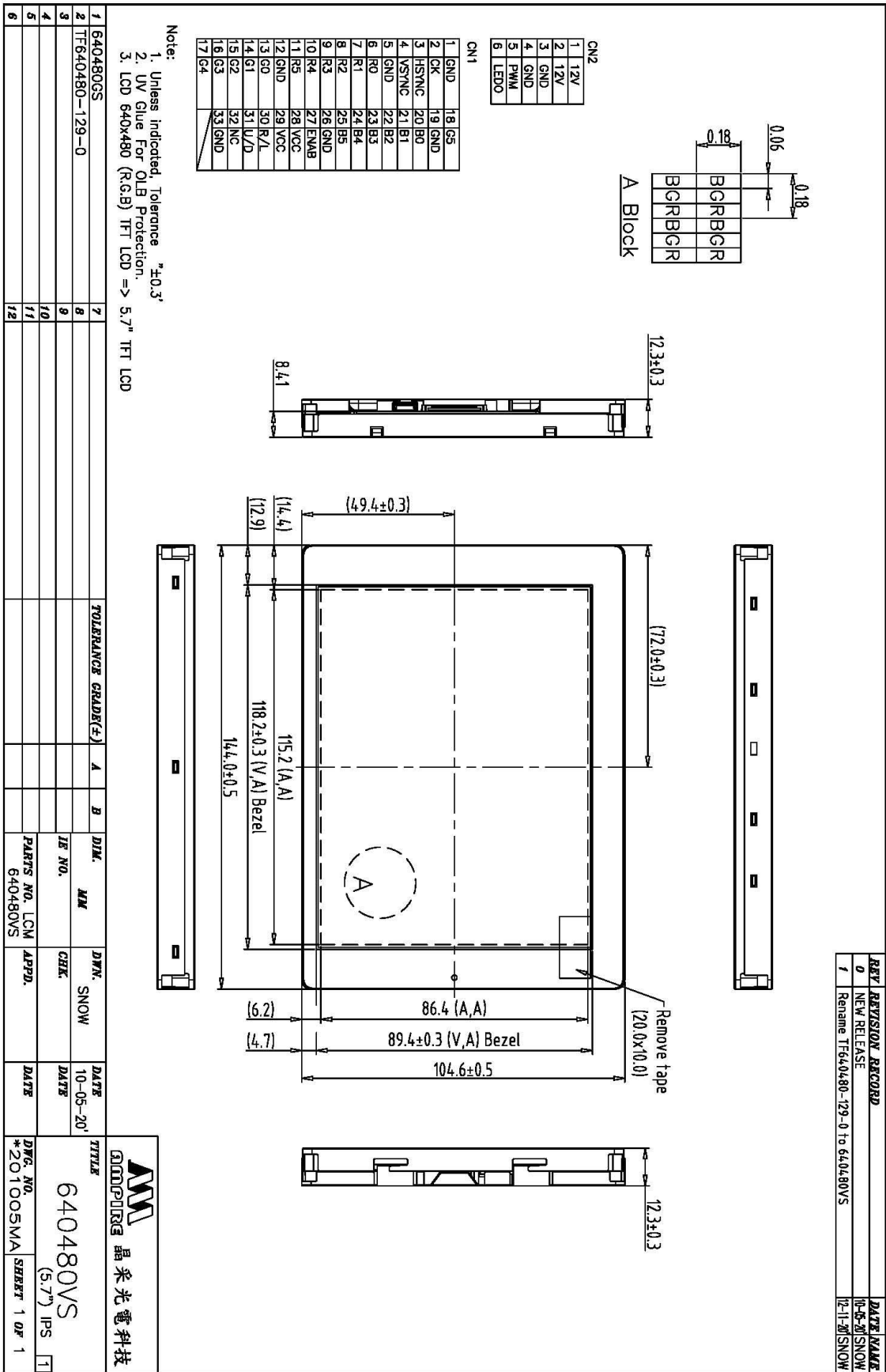
### 11.4 Operating precautions

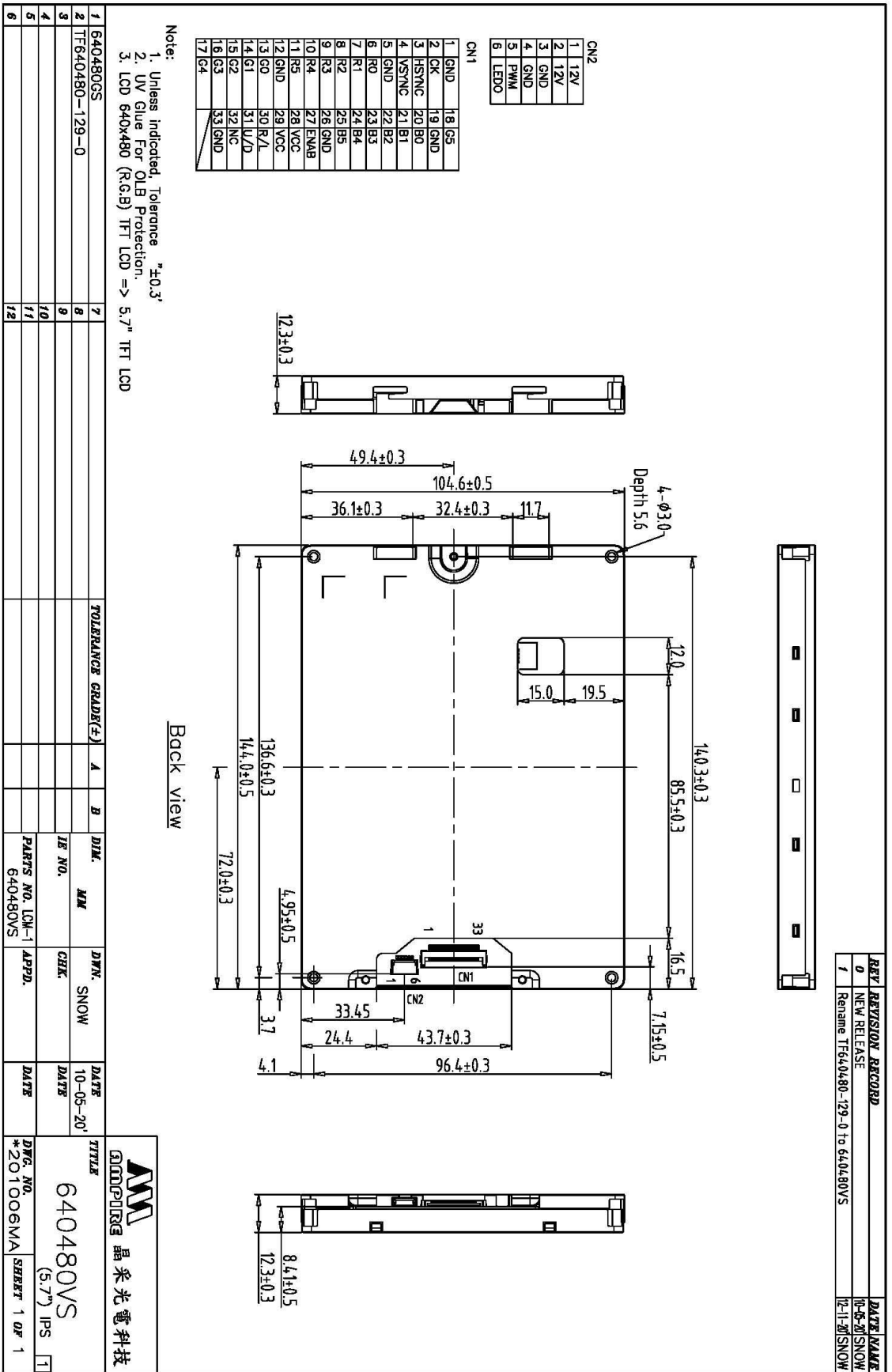
- (1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- (2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- (3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC drive voltage.
- (4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- (5) Make certain that each signal noise level is within the standard (L level: 0.2Vdd or less and H level: 0.8Vdd or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- (6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- (7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
- (8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

### 11.5 Other

- (1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- (2) Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver.
- (3) AMIPRE will provide one year warrantee for all products and three months warrantee for all repairing products.

# 12. Outline Dimension





## 13. Package

