



晶采光電科技股份有限公司
AMPIRE CO., LTD.

Specifications for LCD module

Customer	
Customer part no.	
Ampire part no.	AM-800600MYVZQW-51H
Approved by	
Date	

☐ Preliminary Specification

☒ Formal Specification

AMPIRE CO., LTD.

4F., No.116, Sec. 1, Xintai 5th Rd., Xizhi Dist., New Taipei City 221, Taiwan (R.O.C.)

新北市汐止區新台五路一段 116 號 4 樓(東方科學園區 A 棟)

TEL:886-2-26967269 , FAX:886-2-26967196 or 26967270

Approved by	Checked by	Organized by
Patrick	Simon	Jessica

This Specification is subject to change without notice.

RECORD OF REVISION

Revision Date	Page	Contents	Editor
2022/03/10	--	New Release	Jessica

1. Instruction

Ampire 8.4" Display Module is a color active matrix TFT-LCD that uses amorphous silicon TFT as a switching device. This model is composed of a TFT-LCD panel, a driving circuit. This TFT-LCD has a high resolution (800(R.G.B) X 600) and can display up to 16.2M colors.

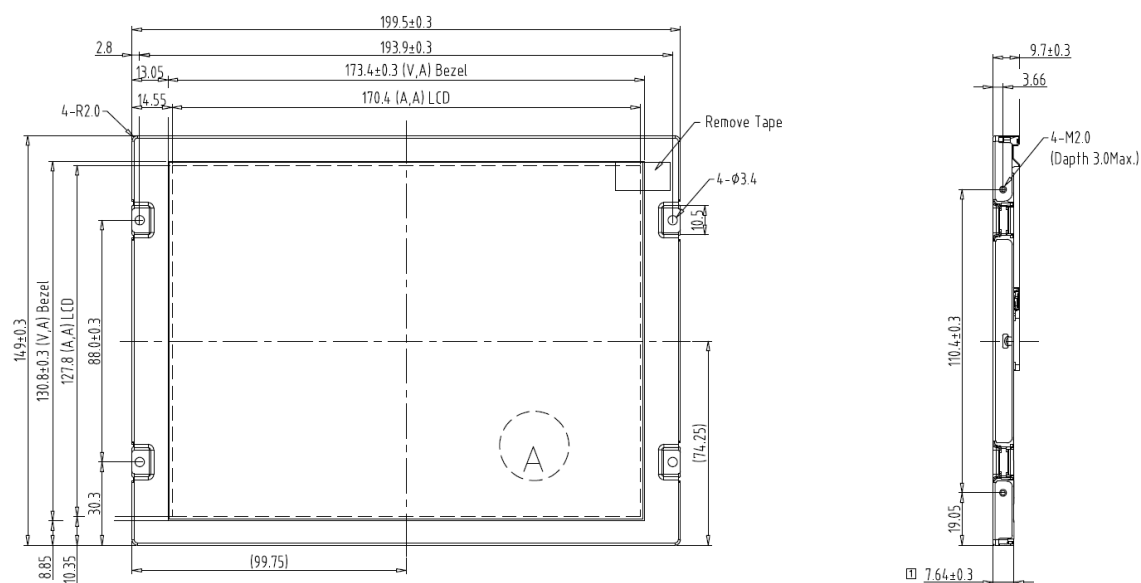
1.1 Features

- (1) Construction: a-Si TFT-LCD with driving system, White LED Backlight.
- (2) LCD type : Transmissive , Normally White
- (3) Number of the Colors : 16.2M colors
- (4) LVDS Interface 8 bit.
- (5) LCD Power Supply Voltage: 3.3V single power input, built-in power supply circuit.
- (6) Build-in LED Driver IC (VL=12V).
- (7) ROHS compliant.

2. Physical Specifications

Item	Specifications	unit
Display resolution(dot)	800RGB (W) x 600(H)	dots
Pixel pitch	213 (W) x 213 (H)	um
Color configuration	R.G.B -stripe	
Backlight unit	LED	
Display color	16.2M	colors
Driver IC	source IC: HX8282-A01DPD300-C Gate IC: HX8696-A00DPD300-E	

If user wants to change the default setting for mass production, please contact with Ampire. We'll apply a new P/N for you.



3. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit	Note
Supply voltage range	VCC	-0.3	4	V	(1)
Voltage range at any terminal	VI	-0.3	VCC + 0.3	V	
Operating Temperature	Top	-20	70	°C	
Storage Temperature	Tstg	-30	80	°C	

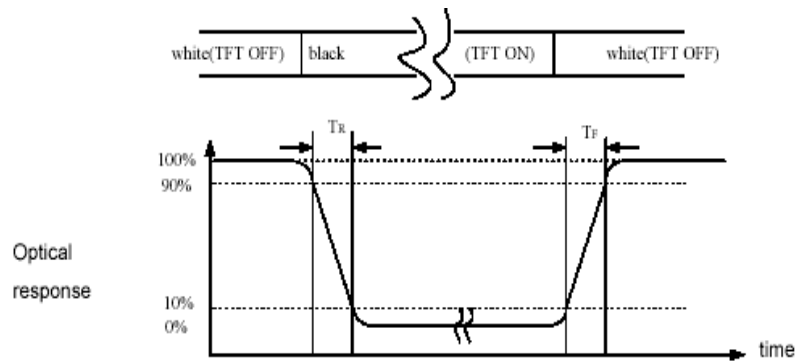
Note: All voltage values are with respect to the GND terminals unless otherwise noted.

4. Optical Characteristics

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Response Time		T _r +T _f	Θ=Φ=0°	-	16	25	ms	(1)
Contrast ratio		CR		480	600	-	-	(2)(3)
Viewing Angle	Horizontal	Θ _L	CR≥ 10	-	85	-	Deg.	(5)
		Θ _R		-	85	-		
	Vertical	Θ _U		-	85	-		
		Θ _D		-	85	-		
Luminance (Center)		L	Θ=Φ=0°	452	565	--	cd/m ²	(3)(4) Ta=25°C
Luminance Uniformity		ΔL		-	70	-	%	(3)(4)
Color chromaticity	White	W _x			T.B.D			
		W _y			T.B.D			

These items are measured by BM-5A (TOPCON) or CA-1000(MINOLTA) in the dark room (no ambient light).

Note(1) Definition of Response Time (White-Black)



Note(2) Definition of Contrast Ratio

Measure contrast ratio on the below 5 points (refer to figure1, #1~#5point) and take the average value

Contrast ratio is calculated with the following formula:

$$\text{Contrast Ratio (CR)} = (\text{White}) \text{ Luminance of ON} \div (\text{Black}) \text{ Luminance of OFF}$$

Note(3) Definition of Luminance :

Measure white luminance on the center point (point 5) and take the value.

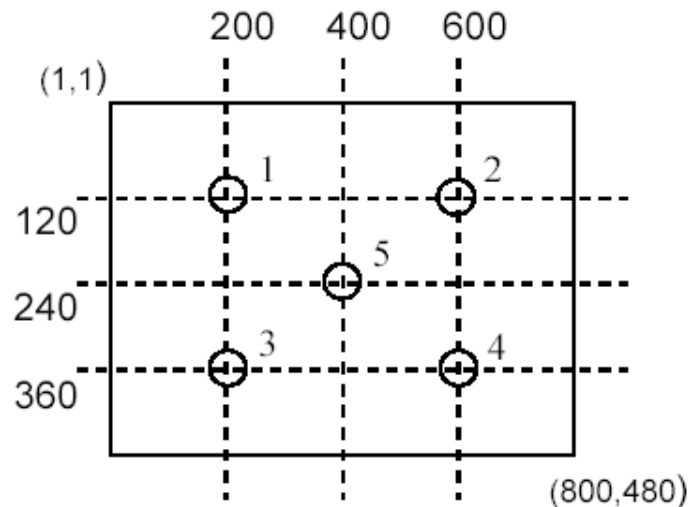


Fig.1 Measuring point

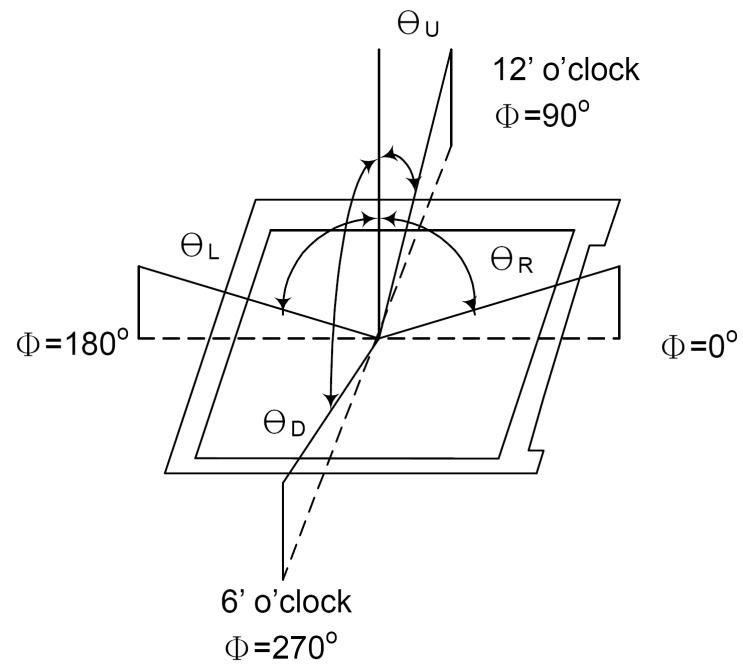
Note(4) Definition of Luminance Uniformity :

Measured Maximum luminance [L (MAX)] and Minimum luminance [L (MIN)] on the 5 points

Luminance Uniformity is calculated with the following formula:

$$\Delta L = [L (\text{MIN}) / L (\text{MAX})] \times 100\%$$

Note(5) Definition of Viewing Angle



5. Electrical Characteristics

5.1 Power Specification

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Logic / LCD Drive Voltage	VCC	3.0	3.3	3.6	V	
VCC Current	ICC		120		mA	(1)

Note(1) fV =60Hz , Ta=25℃ , Display pattern : All Black

5.2 LVDS electrical Specification

Vcc = 3.0 - 3.6V, Ta = -10 - +70 °C

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
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CMOS/TTL DC SPECIFICATIONS

V _{IH}	High Level Input Voltage		2.0		Vcc	V
V _{IL}	Low Level Input Voltage		GND		0.8	V
V _{OH}	High Level output Voltage	I _{OH} =-4mA	2.4			V
V _{OL}	Low Level Output Voltage	I _{OL} =4mA			0.4	V
I _{IN}	Input Current	0V ≤ V _{IN} ≤ Vcc			±10	μA
I _{PD}	Pull Down Current	R/F pin, V _{IH} =Vcc			100	μA
I _{OS}	Output Short Circuit Current	V _{OUT} =0V			-50	mA

LVDS DRIVER DC SPECIFICATIONS

V _{OD}	Differential Output Voltage	RL=100Ω	250	350	450	mV
ΔV _{OD}	Change in VOD between Complimentary Output States				35	mV
V _{OC}	Common Mode Voltage		1.125	1.25	1.375	V
ΔV _{OC}	Change in VOC between Complimentary Output States				35	mV
I _{OS}	Output Short Circuit Current	V _{OUT} =0V, RL=100Ω			-24	mA
I _{OZ}	Output TRI-STATE Current	/PDWN=0V, V _{OUT} =0V to Vcc			±10	μA

LVDS RECEIVER DC SPECIFICATIONS

V _{TH}	Differential Input High Threshold	V _{OC} =+1.2V			+100	mV
V _{TL}	Differential Input low Threshold		-100			mV
I _{IN}	Input Current	V _{IN} =+2.4V/ 0V VDD=3.6V			±10	μA

6. Backlight Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Input Voltage	VL	10.8	12.0	13.2	V	
Input Current	I _{LED}	--	(T.B.D)	--	mA	100% PWM duty, VL=12.0V
BLEN Logic High	V _{IH}	2.5	--	VL	V	
BLEN Logic Low	V _{IL}	0	--	0.4	V	
V _{PDIM} Logic High	V _{IH}	2.5	--	5.5	V	
V _{PDIM} Logic Low	V _{IL}	0	--	0.4	V	
V _{PDIM} PWM Frequency	F _{PDIM}	100	500	1000	Hz	
V _{PDIM} PWM Duty Range	DR	5	--	100	%	
LED Life Time	LT	80,000	100,000			

Note(1) Ta means ambient temperature of TFT-LCD module.

Note(2) VL, I_{LED} are defined for LED B/L. (100% duty of PWM dimming)

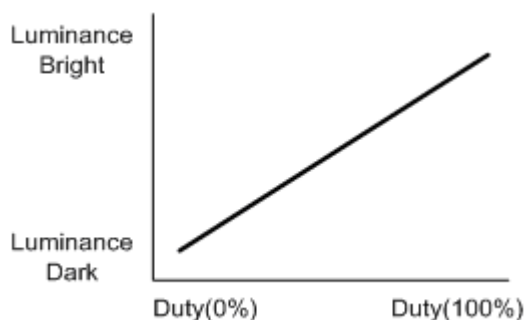
Note(3) F_{PDIM} are defined for LED Driver.

Note(4) If the module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

Note(5) Note 5: Operating life means brightness goes down to 50% minimum brightness. LED life time is estimated data.

Note(6) Note 6: the structure of LED B/L shows as below.

6.1 PWM Dimming Control



7. Interface

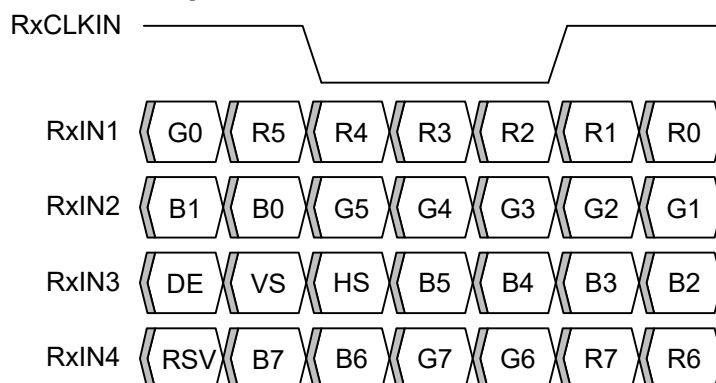
7.1 Interface Definition

CN 1(Interface Signal)

Used connector 20186-020E-11F (I-PEX) or FI-SEB20P-HFE (JAE) Corresponding connector 20197-20U-F (I-PEX) or FI-S20S [for discrete Wire FI-SE20ME (for FPC] (JAE)

Pin no	Symbol	Function
1	VCC	POWER SUPPLY:3.3V
2	VCC	POWER SUPPLY:3.3V
3	GND	Power Ground
4	GND	Power Ground
5	Link 0-	Transmission Data of Pixels 1
6	Link 0+	Transmission Data of Pixels 1
7	GND	Power Ground
8	Link 1-	Transmission Data of Pixels 2
9	Link 1+	Transmission Data of Pixels 2
10	GND	Power Ground
11	Link 2-	Transmission Data of Pixels 3
12	Link 2+	Transmission Data of Pixels 3
13	GND	Power Ground
14	CLKKIN-	Sampling Clock
15	CLKKIN+	Sampling Clock
16	GND	Power Ground
17	Link 3-	Transmission Data of Pixels 4
18	Link 3+	Transmission Data of Pixels 4
19	MODE	Not connect
20	SC	Scan direction control(Low = Normal, High = Reverse)

8 bits LVDS input



Note: R/G/B data 7: MSB, R/G/B data 0: LSB

Signal Name	Description	Remark
R7 R6 R5 R4 R3 R2 R1 R0	Red Data 7 (MSB) Red Data 6 Red Data 5 Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0 (LSB)	Red-pixel Data Each red pixel's brightness data consists of these 8 bits pixel data.
G7 G6 G5 G4 G3 G2 G1 G0	Green Date 7 (MSB) Green Date 6 Green Date 5 Green Date 4 Green Date 3 Green Date 2 Green Date 1 Green Date 0 (LSB)	Green-pixel Data Each green pixel's brightness data consists of these 8 bits pixel data.
B7 B6 B5 B4 B3 B2 B1 B0	Blue Data 7 (MSB) Blue Data 6 Blue Data 5 Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0 (LSB)	Blue-pixel Data Each blue pixel's brightness data consists of these 8 bits pixel data.
RxCLKIN+ RxCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

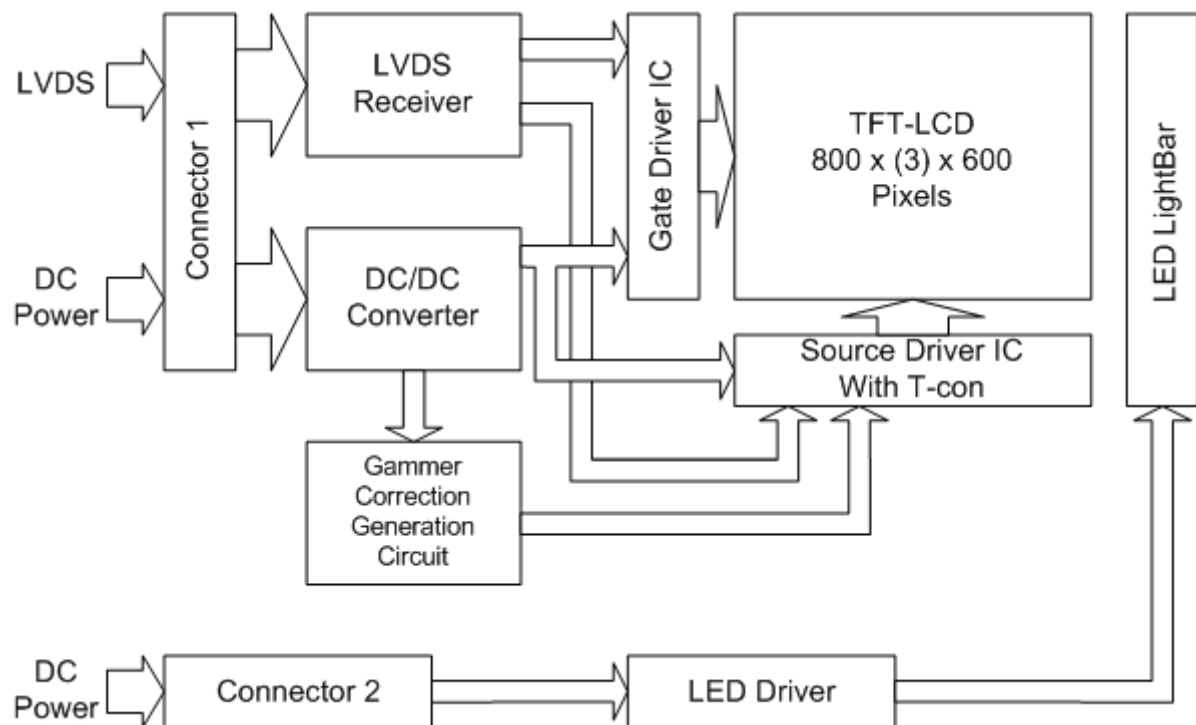
CN2: LED Driver Connector

Backlight-side connector FI-S6P-HFE (JAE)

Corresponding connector FI-S6S (JAE)

Pin NO.	Symbol	Description	Note
1	VL	LED Driver input voltage	12V
2	VL	LED Driver input voltage	12V
3	GND	LED Driver ground	Ground
4	GND	LED Driver ground	Ground
5	BLEN	LED Driver Enable pin. High : LED Back-light ON Low : LED Back-light Low	
6	V _{PDIM}	PWM input for Back-light brightness adjust	

7.2 Block Diagram



8. AC Timing characteristic

DE mode

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
DCLK Frequency	fclk	32.6	39.6	62.4	MHz
Horizontal Display Area	thd	800			DCLK
HSD Period	th	890	1000	1300	DCLK
HSD Blanking	thb+ thfp	90	200	500	DCLK
Vertical Display Area	tvd	600			T _H
VSD Period	tv	610	660	800	T _H
VSD Blanking	tvbp+ tvfp	10	60	200	T _H

Table 10.10: DE mode (800x600)

HV mode

Horizontal timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
DCLK Frequency	fclk	34.5	39.6	50.4	MHz
Horizontal Display Area	thd	800			DCLK
HSD Period	th	900	1000	1200	DCLK
HSD Pulse Width	thpw	1	-	40	DCLK
HSD Back Porch	thbp	88			DCLK
HSD Front Porch	thfp	12	112	312	DCLK

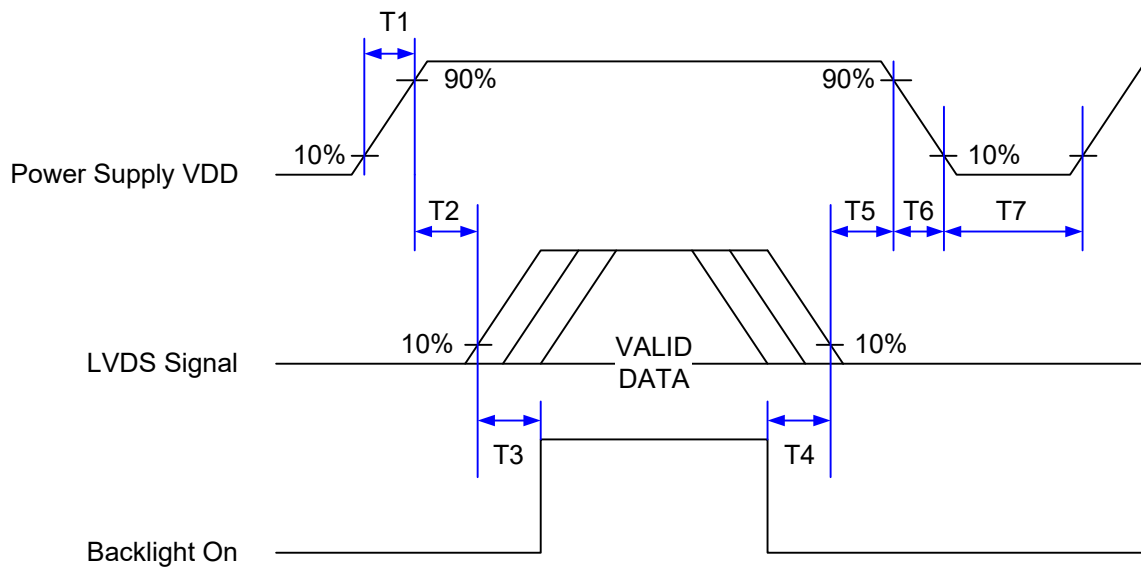
Table 10.11: HV mode horizontal timing (800x600)

Vertical timing

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Vertical Display Area	tvd	600			T _H
VSD Period	tv	640	660	700	T _H
VSD Pulse Width	tvpw	1	-	20	T _H
VSD Back Porch	tvbp	39			T _H
VSD Front Porch	tvfp	1	21	61	T _H

Table 10.12: HV mode vertical timing (800x600)

9. Power ON/OFF Sequence



Symbol	Value			Unit
	Min.	Typ.	Max.	
T1	0.5	--	20	ms
T2	0	40	50	ms
T3	200	--	--	ms
T4	200	--	--	ms
T5	0	40	50	ms
T6	0	--	20	ms
T7	1000	--	--	ms

10. Reliability Test Items

Test Item	Test Conditions	Note
High Temperature Operation	70±3°C , t=240 hrs	
Low Temperature Operation	-20±3°C , t=240 hrs	
High Temperature Storage	80±3°C , t=240 hrs	1,2
Low Temperature Storage	-30±3°C , t=240 hrs	1,2
Storage at High Temperature and Humidity	60°C, 90% RH , 240 hrs	1,2
Thermal Shock Test	-20°C (30min) ~ 70°C (30min) 100 cycles	1,2
Vibration Test (Packing)	Sweep frequency : 10 ~ 55 ~ 10 Hz/1min Amplitude : 0.75mm Test direction : X.Y.Z/3 axis Duration : 30min/each axis	2

Note(1) Condensation of water is not permitted on the module.

Note(2) The module should be inspected after 1 hour storage in normal conditions (15-35°C, 45-65%RH).

Definitions of life end point:

- Current drain should be smaller than the specific value.
- Function of the module should be maintained.
- Appearance and display quality should not have degraded noticeably.
- Contrast ratio should be greater than 50% of the initial value.

Note(3) The module shouldn't be tested more than one condition, and all the test conditions are independent.

Note(4) All the reliability tests should be done without protective film on the module.

11. Use Precautions

11.1 Handling precautions

- (1) The polarizing plate may break easily so be careful when handling it. Do not touch, press or rub it with a hard-material tool like tweezers.
- (2) Do not touch the polarizing plate surface with bare hands so as not to make it dirty. If the surface or other related part of the polarizing plate is dirty, soak a soft cotton cloth or chamois leather in benzine and wipe off with it. Do not use chemical liquids such as acetone, toluene and isopropyl alcohol. Failure to do so may bring chemical reaction phenomena and deteriorations.
- (3) Remove any spit or water immediately. If it is left for hours, the suffered part may deform or decolorize.
- (4) If the LCD element breaks and any LC stuff leaks, do not suck or lick it. Also if LC stuff is stuck on your skin or clothing, wash thoroughly with soap and water immediately.

11.2 Installing precautions

- (1) The PCB has many ICs that may be damaged easily by static electricity. To prevent breaking by static electricity from the human body and clothing, earth the human body properly using the high resistance and discharge static electricity during the operation. In this case, however, the resistance value should be approx. $1\text{M}\Omega$ and the resistance should be placed near the human body rather than the ground surface. When the indoor space is dry, static electricity may occur easily so be careful. We recommend the indoor space should be kept with humidity of 60% or more. When a soldering iron or other similar tool is used for assembly, be sure to earth it.
- (2) When installing the module and ICs, do not bend or twist them. Failure to do so may crack LC element and cause circuit failure.
- (3) To protect LC element, especially polarizing plate, use a transparent protective plate (e.g., acrylic plate, glass etc) for the product case.
- (4) Do not use an adhesive like a both-side adhesive tape to make LCD surface (polarizing plate) and product case stick together. Failure to do so may cause the polarizing plate to peel off.

11.3 Storage precautions

- (1) Avoid a high temperature and humidity area. Keep the temperature between 0°C and 35°C and also the humidity under 60%.
- (2) Choose the dark spaces where the product is not exposed to direct sunlight or fluorescent light.
- (3) Store the products as they are put in the boxes provided from us or in the same conditions as we recommend.

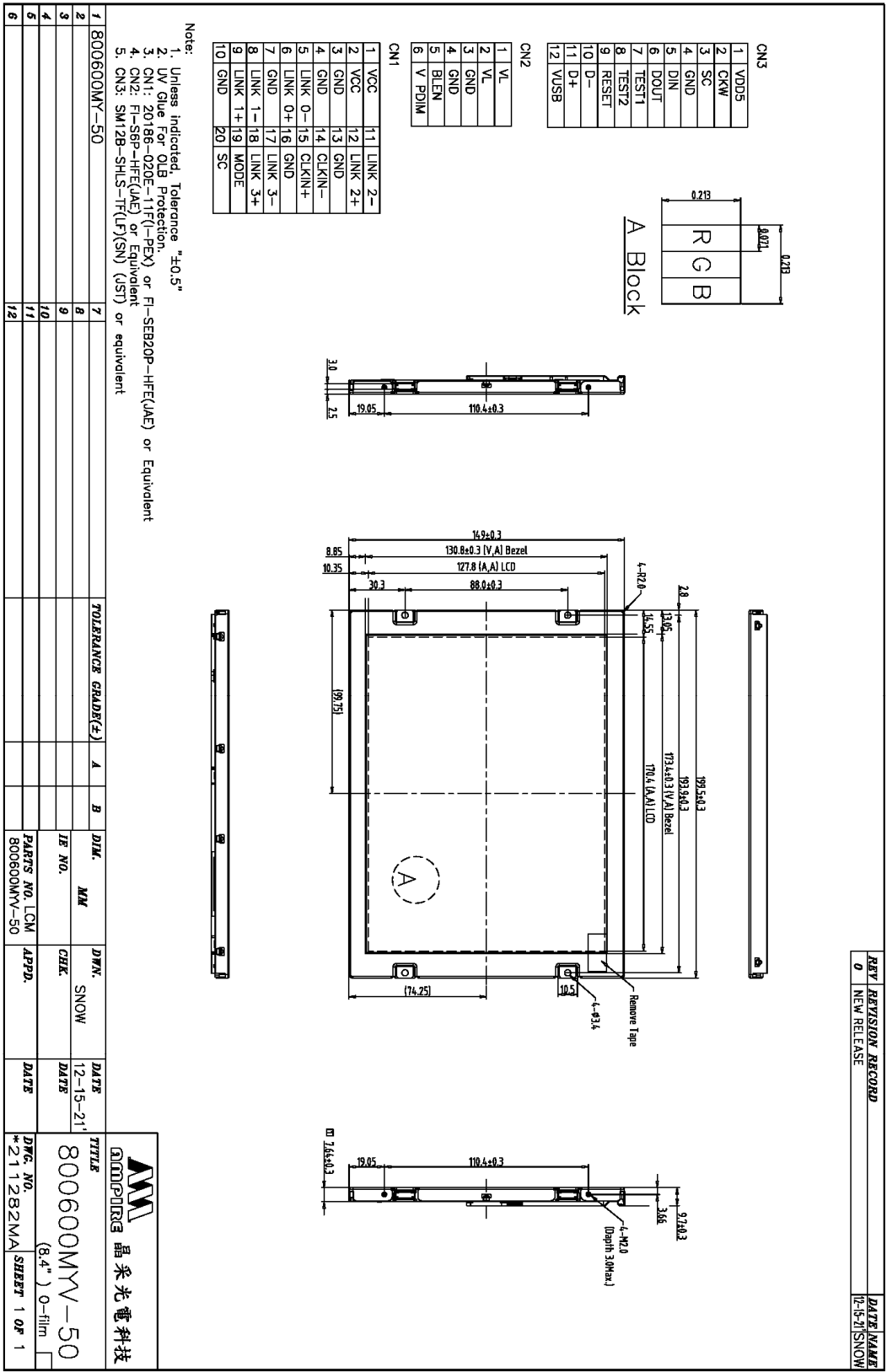
11.4 Operating precautions

- (1) Do not boost the applied drive voltage abnormally. Failure to do so may break ICs. When applying power voltage, check the electrical features beforehand and be careful. Always turn off the power to the LC module controller before removing or inserting the LC module input connector. If the input connector is removed or inserted while the power is turned on, the LC module internal circuit may break.
- (2) The display response may be late if the operating temperature is under the normal standard, and the display may be out of order if it is above the normal standard. But this is not a failure; this will be restored if it is within the normal standard.
- (3) The LCD contrast varies depending on the visual angle, ambient temperature, power voltage etc. Obtain the optimum contrast by adjusting the LC drive voltage.
- (4) When carrying out the test, do not take the module out of the low-temperature space suddenly. Failure to do so will cause the module condensing, leading to malfunctions.
- (5) Make certain that each signal noise level is within the standard (L level: 0.2Vcc or less and H level: 0.8Vcc or more) even if the module has functioned properly. If it is beyond the standard, the module may often malfunction. In addition, always connect the module when making noise level measurements.
- (6) The CMOS ICs are incorporated in the module and the pull-up and pull-down function is not adopted for the input so avoid putting the input signal open while the power is ON.
- (7) The characteristic of the semiconductor element changes when it is exposed to light emissions, therefore ICs on the LCD may malfunction if they receive light emissions. To prevent these malfunctions, design and assemble ICs so that they are shielded from light emissions.
- (8) Crosstalk occurs because of characteristics of the LCD. In general, crosstalk occurs when the regularized display is maintained. Also, crosstalk is affected by the LC drive voltage. Design the contents of the display, considering crosstalk.

11.5 Other

- (1) Do not disassemble or take the LC module into pieces. The LC modules once disassembled or taken into pieces are not the guarantee articles.
- (2) Do not keep the LCD at the same display pattern continually. The residual image will happen and it will damage the LCD. Please use screen saver.
- (3) AMIPRE will provide one year warranty for all products and three months warrantee for all repairing products.

12. Outline Dimension



Top View Dimensions:

- Overall Width: 110.4±0.3
- Overall Height: 84.44±0.3
- Pin 1 to Pin 12 Distance: 10.35±0.3
- Pin 12 to Pin 1 Distance: 78.3±0.3
- Pin 1 to Pin 12 Distance (Alternative): 70.35±0.3
- Pin 1 to Pin 12 Distance (Alternative): 69.25
- Pin 1 to Pin 12 Distance (Alternative): 33.6±0.3
- Pin 1 to Pin 12 Distance (Alternative): 85.44±0.3
- Pin 1 to Pin 12 Distance (Alternative): 20.36±0.5
- Pin 1 to Pin 12 Distance (Alternative): 44.35±0.3
- Pin 1 to Pin 12 Distance (Alternative): 50.9±0.5
- Pin 1 to Pin 12 Distance (Alternative): 19.05
- Pin 1 to Pin 12 Distance (Alternative): 3.0
- Pin 1 to Pin 12 Distance (Alternative): 2.5

Back View Dimensions:

- Overall Width: 110.4±0.3
- Overall Height: 84.44±0.3
- Pin 1 to Pin 12 Distance: 10.35±0.3
- Pin 12 to Pin 1 Distance: 78.3±0.3
- Pin 1 to Pin 12 Distance (Alternative): 70.35±0.3
- Pin 1 to Pin 12 Distance (Alternative): 69.25
- Pin 1 to Pin 12 Distance (Alternative): 33.6±0.3
- Pin 1 to Pin 12 Distance (Alternative): 85.44±0.3
- Pin 1 to Pin 12 Distance (Alternative): 20.36±0.5
- Pin 1 to Pin 12 Distance (Alternative): 44.35±0.3
- Pin 1 to Pin 12 Distance (Alternative): 50.9±0.5
- Pin 1 to Pin 12 Distance (Alternative): 19.05
- Pin 1 to Pin 12 Distance (Alternative): 3.0
- Pin 1 to Pin 12 Distance (Alternative): 2.5

Pin Configurations:

CN1:

1	VCC
2	GND
3	GND
4	GND
5	GND
6	GND
7	GND
8	GND
9	GND
10	GND
11	GND
12	GND

CN2:

1	VL
2	VL
3	GND
4	GND
5	BLN
6	VL
7	VL
8	VL
9	VL
10	VL
11	VL
12	VL

CN3:

1	VDD5
2	CKW
3	SC
4	GND
5	DIN
6	DOUT
7	TEST1
8	TEST2
9	RESET
10	D-
11	D+
12	VUSB

Notes:

- Unless Indicated, Tolerance "±0.5"
- UV Glue For QLB Protection.
- CN1: 20186-02DE-11F(I)-PEX or FI-SEB20P-HFE(JAE) or Equivalent
- CN2: FI-S6P-HFE(JAE) or Equivalent
- CN3: SM12B-SHLS-1F(LF)(SN) (JST) or equivalent

Date: 2022/03/10

AMPIRE CO., LTD.