TENTATIVE



Kaohsiung Opto-Electronics Inc.

FOR MESSRS :	DATE: Jun. 23 th ,	2021

TECHNICAL DATA

TX13D205VM0BAA

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ACCEPTED BY: _____ PROPOSED BY: Oblack Tsai

2. RECORD OF REVISION

DATE	SHEET No.			SUMMARY			
Jun.23,'21	7B64LTD-2699-2	3.1 DISPLA	Y FEATURES				
	Page 3-1/1	Revised:					
		Power Sup	ply Voltage	3.3V for LCD driving; 28.3 V f	or Backlig	ıht	
		Power Con	sumption	0.5 W for LCD; 3.81 W for B/L	_		
				↓			
			ply Voltage	3.3V for LCD driving; 21 V for		t	
		Power Con	sumption	0.5 W for LCD ; 3.36 W for B/L			
	7B64LTD-2699-2	9.1 FRONT	VIEW				
	Page 9-1/2	Revised : Po	CB Component	thickness			
	7B64LTD-2699-2	9.2 RAER V	IEW				
	Page 9-2/2	Added : PC	3 Outline				
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		100.					

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 5" WVGA of 16:9 format of amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R(red), G(green), B(blue) sequentially .This display is RoHS compliant , and COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX13D205VM0BAA
Module Dimensions	121.0(W)mm x 80.0(H)mm x 7.1(D)mm (W/O component & FPC)
LCD Active Area	108.0(W)mm x 64.8(H)mm
Pixel Pitch	0.135(W)mm x 0.135(H)mm
Resolution	800x3(R,G,B)(W)x480(H) Dots
Color Pixel Arrangement	R, G, B Vertical stripe
LCD Type	Transmissive Color TFT; Normally Black
Display Type	Active Matrix
Number of Colors	16.7M Colors (8-bit RGB)
Backlight	Light Emitting Diode (LED)
Weight	95 g (typ.)
Interface	45pin CMOS
Power Supply Voltage	3.3V for LCD driving ; 21 V for Backlight
Power Consumption	0.6 W for LCD ; 3.36 W for B/L
Viewing Direction	Super Wide version

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4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V_{DD}	0.3	4.0	٧	-
Input Voltage of Logic	Vı	0.3	4.0	٧	Note 1
Operating Temperature	Top	-30	85	°C	Note 2
Storage Temperature	T _{st}	-40	90	°C	Note 2
Backlight Input Voltage	VLED	-	(TBD)	V	-

- Note 1: The rating is defined for the signal voltages of the interface such as DE, CLK and RGB data bus.
- Note 2: The maximum rating is defined as above based on the chamber temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:
 - Background color, contrast and response time would be different in temperatures other than $25\,^{\circ}\mathrm{C}\,.$
 - -Operating under high temperature will shorten LED lifetime.

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5. ELECTRICAL CHARACTERISTICS

5.1 LCD CHARACTERISTICS

 $T_a = 25 \, {}^{\circ}C, \, \text{Vss} = 0\text{V}$

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-	3.0	3.3	3.6	V	-
Input Voltage of Logic	Vı	"H" level	0.7V _{DD}	-	V_{DD}	V	Note 1
		"L" level	0	-	0.3V _{DD}		
Power Supply Current	I _{DD}	V _{DD} =3.3V	-	-	180	mA	Note 2
Frame Frequency	$f_{\it Frame}$	-	55	60	65	Hz	-
CLK Frequency	f_{CLK}	-	23.2	27.7	33.5	MHz	-

Note 1: The rating is defined for the signal voltages of the interface such as DE, CLK and RGB data bus.

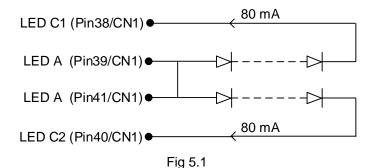
Note 2: An all white check pattern is used when measuring I_{DD} . f_{Frame} is set to 60 Hz. Moreover, 1.0A fuse is applied in the module for I_{DD} . For display activation and protection purpose, power supply is recommended larger than 2.5A to start the display and break fuse once any short circuit occurred.

5.2 BACKLIGHT CHARACTERISTICS

 $T_a = 25 \, {}^{\circ}C$

Item	Symbol	Condition	Min.	Тур.	Max.	Unit	Remarks
LED Input Voltage	V _{LED}	-	-	21	-	V	
LED Forward Current	I _{LED}	per LED	-	80	-	mA	
LED lifetime	-	I _{LED} =80 mA/per LED	30K	-	-	hrs	Note 1

Note 1: The estimated lifetime is specified as the time to reduce 50% brightness by applying 80 mA at 25° C.



6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The backlight unit needs to be turned on at least 15 minutes.
- The ambient temperature is 25 °C.
- In the dark room, the equipment has been set for the measurements as shown in Fig 6.1.

 $T_a = 25 \, ^{\circ}C, f_{Frame} = 60 \, \text{Hz}, \text{Vdd} = 3.3 \text{V}$ Max. Unit Remarks Item Symbol Condition Min. Note 1 Brightness of White - $\phi = 0^{\circ}, \theta = 0^{\circ},$ 1000 1300 cd/m² **Brightness Uniformity** I_{LED}=80 mA _ 70 % Note 2 (per LED) Contrast Ratio CR 800 1300 --Note 3 Response Time $\phi = 0^{\circ}, \theta = 0^{\circ}$ 25 Note 4 $T_r + T_f$ ms (Rising + Falling) **NTSC Ratio** $\phi = 0^{\circ}, \theta = 0^{\circ}$ 70 % θx $\phi = 0^{\circ}, CR \ge 10$ 80 $\theta x'$ 80 $\phi = 180^{\circ}, CR \ge 10$ Viewing Angle Degree Note 5 80 θ y $\phi = 90^{\circ}, CR \ge 10$ $\theta y'$ 80 $\phi = 270^{\circ}$, CR ≥ 10 Χ 0.60 0.64 0.68 Red Υ 0.29 0.33 0.37 0.28 0.32 Χ 0.36 Green Υ 0.58 0.62 0.66 Color $\phi = 0^{\circ}, \theta = 0^{\circ}$ Note 6 Chromaticity Χ 0.11 0.15 0.18 Blue Υ 0.02 0.06 0.10 Χ 0.27 0.31 0.35

Note 1: The brightness is measured from the panel center point, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

Υ

Brightness uniformity =
$$\frac{\text{Min. Brightness}}{\text{Max. Brightness}} \times 100\%$$

which is based on the brightness values of the 9 points in active area measured by BM-5 as

0.28

0.32

0.36

shown in Fig. 6.2.

White

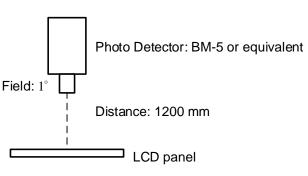


Fig 6.1

$= \frac{ +\dot{6}^{X} }{ +\dot{6}^{X} }$	$\overline{}^{\times} \longrightarrow \longleftarrow \frac{\overline{a}}{6}$	$-x \longrightarrow \leftarrow \frac{1}{6}x \rightarrow $
*	P2	P3
2 Y P4	P5	P6 Y
6	P8	P9
	x	

Fig 6.2

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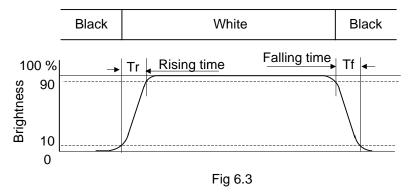
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Note 3: The Contrast Ratio is measured from the center point of the panel, P5, and defined as the following equation:

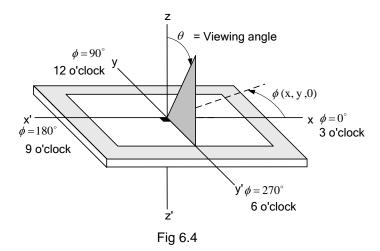
$$CR = \frac{Brightness of \ White}{Brightness of \ Black}$$

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 10% brightness to 90% brightness when the data is from black to white. Oppositely, Falling time is the period from 90% brightness rising to 10% brightness.



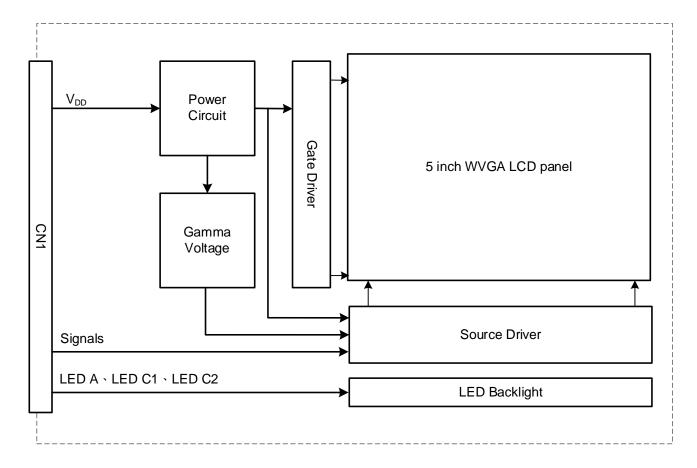
Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270^{\circ}$ means 6 o'clock, and $\phi = 0^{\circ}$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.



Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

7. BLOCK DIAGRAM



Note 1: Signals are DE, CLK and RGB data bus.

8. LCD INTERFACE

8.1 INTERFACE PIN CONNECTIONS

The display interface connector CN1 is FH28K-45S-0.5SH made by Hirose and pin assignment is as below:

Pin No.	Symbol	Signal	Pin No.	Symbol	Signal
1	M	Ground	24	В3	Blue Data
2	Vss	Ground	25	B4	Blue Data
3		Cumbi Valtara	26	B5	Blue Data
4	V_{DD}	Supply Voltage	27	В6	Blue Data
5	R0	Red Data (LSB)	28	В7	Blue Data (MSB)
6	R1	Red Data	29	Vss	Ground
7	R2	Red Data	30	CLK	Dot Clock
8	R3	Red Data	24	DICD	Display ON/OFF
9	R4	Red Data	31	DISP	("L" Display OFF ; "H" Display ON)
10	R5	Red Data	32	NC	No Connection
11	R6	Red Data	33	INC	No Connection
12	R7	Red Data (MSB)	34	DE	Data Enable Signal
13	G0	Green Data (LSB)	35	NC	No Connection
14	G1	Green Data	36	SD	Scan Direction Control (Note1)
15	G2	Green Data	37	V _{SS}	Ground
16	G3	Green Data	38	LED C1	LED Cathode 1
17	G4	Green Data	39	LED A	LED Anode
18	G5	Green Data	40	LED C2	LED Cathode 2
19	G6	Green Data	41	LED A	LED Anode
20	G7	Green Data (MSB)	42		
21	В0	Blue Data (LSB)	43	NC	No Connection
22	B1	Blue Data	44	NC	No Connection
23	B2	Blue Data	45		

Note 1: Please refer to <u>8.3 SCAN DIRECTION</u> for the setting methods of SD function.

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8.2 TIMING CHART th = 884 CLK (1H) DE CLK 27.7M Hz (typ thd = 800 CLK (fixed) thp+thb thf Invalid data Invalid data Display data R [0:7] G [0:7] B [0:7] Fig. 8.1 Horizontal Timing tv = 523 H (60 Hz)DE tvd = 480 H (fixed)tvp+tvb Display lines Invalid lines Invalid lines RGB Fig. 8.2 Vertical Timing Tcph CLK 30% 30% Tdsu Tdhd Tcwl 1st RGB 2nd RGB 800 RGB Data 30% Tehd Tesu 70% DE Fig. 8.3 Setup & Hold Time **SHEET** 7B64LTD-2699-2 **PAGE** KAOHSIUNG OPTO-ELECTRONICS INC. 8-2/6 NO.

A.The timings except mentioned above are referred to the specifications of your transmitter.

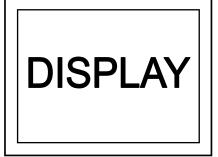
	Item	Symbol	Min.	Тур.	Max.	Unit	remarks
DCLK	Cycle time	Tc	29.9	36.1	43.1	ns	
l la nima natal	Horizontal period	T _H	862	884	920	Tc	
Horizontal	Horizontal width-Active	T _{HD}		800		Tc	
	Vertical period	Tv	490	523	560	T _H	
Vertical	Vertical width-Active	T_VD		480		T _H	
	Frame frequency	f _V	55	60	65	Hz	

B. Setup and Hold Time

	Item	Symbol	Min.	Тур.	Max.	Unit
OL K	Duty		40	50	60	%
CLK	Cycle Time	Tcph	-	36.1	-	
Data	Setup Time	Tdsu	5	-	-	
Data	Hold Time	Tdhd	5	-	ı	ns
DE	Setup Time	Tesu	5	-	ı	
DE	Hold Time	Tehd	5	-	-	

8.3 SCAN DIRECTION

Scan direction is available to be switched as below:

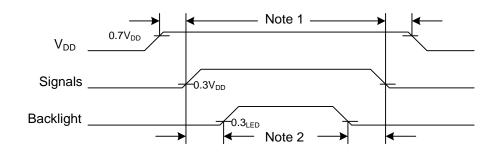


SD: L (Default)



SD:H

8.4 POWER SEQUENCE

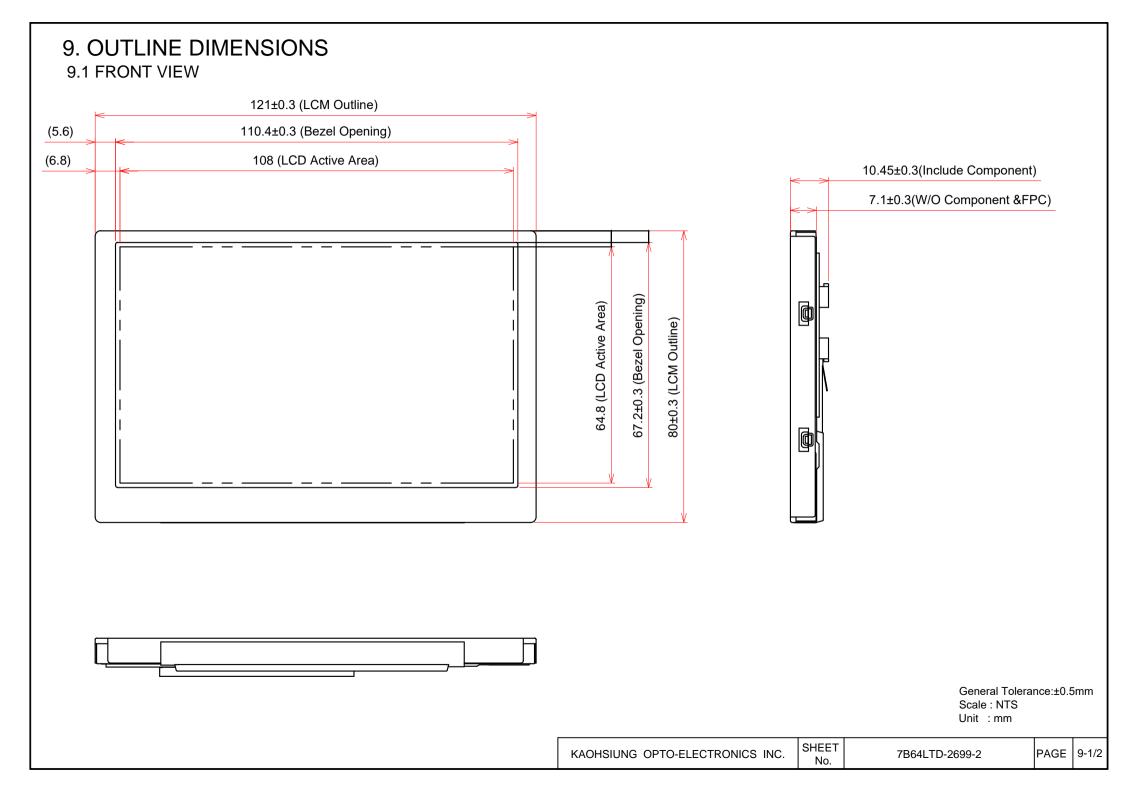


- Note 1: In order to avoid any damages, V_{DD} has to be applied before all other signals. The opposite is true for power off where V_{DD} has to be remained on until all other signals have been switch off. The recommended time period is 1 second.
- Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.

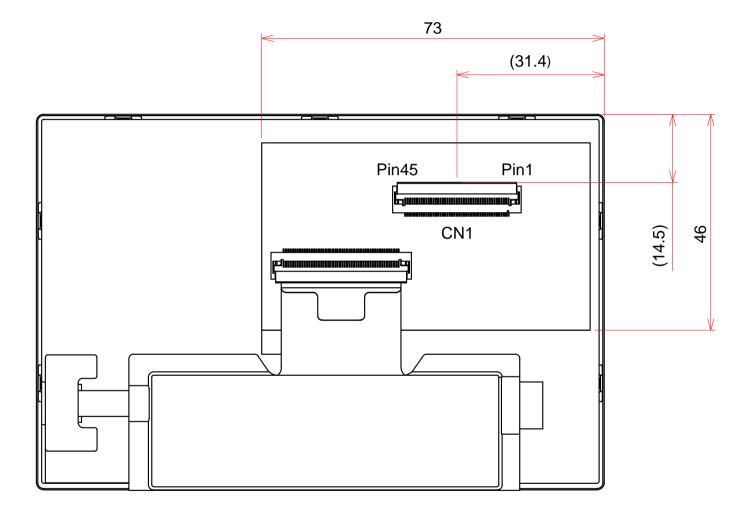
8.5 DATA INPUT for DISPLAY COLOR

				l	Red	Data	à					C	Greer	n Dat	а						Blue	Data	ì		
`Inp	out color	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	B6	B5	В4	ВЗ	B2	B1	В0
		MSB							LSB	MSB							LSB	MSB							LSB
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
Green	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

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9.2 RAER VIEW



General Tolerance:±0.5mm Scale: NTS

Scale : NTS Unit : mm

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10. DESIRNATION of LOT MArK

1) The lot mark is showing in Fig.10.1. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 6 digits are the serial number.

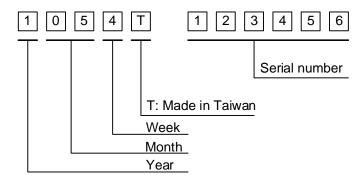


Fig. 10.1

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Lot Mark
2021	1
2022	2
2023	3
2024	4
2025	5

Month	Lot Mark	Month	Lot Mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun	06	Dec	12

Week	Lot Mark
1~7 days	1
8~14 days	2
15~21 days	3
22~28 days	4
29~31 days	5

- 3) Except letters I and O, revision number will be shown on lot mark and following letters A to Z.
- 4) The location of the lot mark is on the back of the display shown in Fig. 10.2.

Label example:

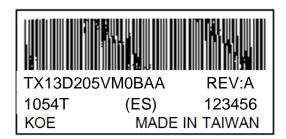


Fig. 10.2